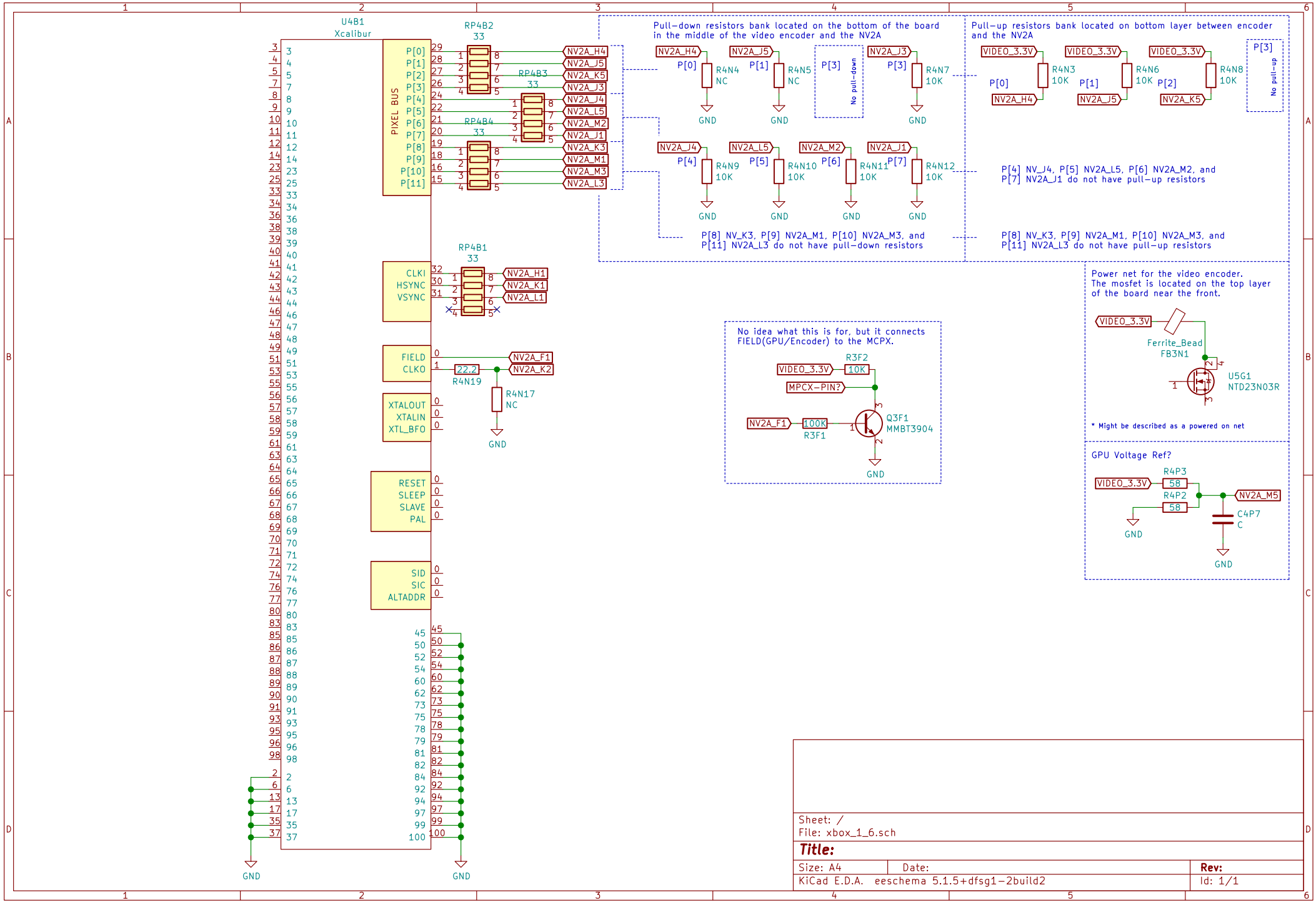
U4B1
Xcalibur
PIXEL BUS
P[0]
P[1]
P[2]
P[3]
P[4]
P[5]
P[6]
P[7]
P[8]
P[9]
P[10]
P[11]
RP4B2
33
RP4B3
33
RP4B4
33
RP4B1
33
NV2A_H4
NV2A_J5
NV2A_K5
NV2A_J3
NV2A_J4
NV2A_L5
NV2A_M2
NV2A_J1
NV2A_K3
NV2A_M1
NV2A_M3
NV2A_L3
CLKI
HSYNC
VSYNC
NV2A_H1
NV2A_K1
NV2A_L1
FIELD
CLKO
NV2A_F1
NV2A_K2
22.2
R4N19
R4N17
NC
XTALOUT
XTALIN
XTL_BFO
RESET
SLEEP
SLAVE
PAL
SID
SIC
ALTADDR
GND
Pull-down resistors bank located on the bottom of the board in the middle of the video encoder and the NV2A
R4N4
NC
R4N5
NC
No pull-down
R4N7
10K
R4N9
10K
R4N10
10K
R4N11
10K
R4N12
10K
P[8] NV_K3, P[9] NV2A_M1, P[10] NV2A_M3, and P[11] NV2A_L3 do not have pull-down resistors
Pull-up resistors bank located on bottom layer between encoder and the NV2A
VIDEO_3.3V
R4N3
10K
R4N6
10K
R4N8
10K
No pull-up
P[4] NV_J4, P[5] NV2A_L5, P[6] NV2A_M2, and P[7] NV2A_J1 do not have pull-up resistors
P[8] NV_K3, P[9] NV2A_M1, P[10] NV2A_M3, and P[11] NV2A_L3 do not have pull-up resistors
No idea what this is for, but it connects FIELD(GPU/Encoder) to the MCPX.
R3F2
10K
MPCX-PIN?
100K
R3F1
Q3F1
MMBT3904
Power net for the video encoder.
The mosfet is located on the top layer of the board near the front.
Ferrite_Bead
FB3N1
U5G1
NTD23N03R
* Might be described as a powered on net
GPU Voltage Ref?
R4P3
58
R4P2
58
C4P7
C
Sheet: /
File: xbox_1_6.sch
Title:
Size: A4
Date:
Rev:
KiCad E.D.A. eeschema 5.1.5+dfsg1-2build2
Id: 1/1