

- Bit 5** This bit is the command parameter buffer full (CPBF) bit and indicates that the parameter register contains a parameter. It is set when the processor deposits a parameter in the parameter register, and reset when the 8273 accepts the parameter.
- Bit 6** This bit is the command buffer full (CBF) bit and, when set, it indicates that a byte is present in the command register. This bit is normally not used.
- Bit 7** This bit is the command busy (CBSY) bit and indicates when the 8273 is in the command phase. It is set when the processor writes a command into the command register, starting the command phase. It is reset when the last parameter is deposited in the parameter register and accepted by the 8273, completing the command phase.

## Initializing the Adapter (Typical Sequence)

Before initialization of the 8273 protocol controller, the support devices on the card must be initialized to the proper modes of operation.

Configuration of the 8255A-5 programmable peripheral interface is accomplished by selecting the mode-set address for the 8255 (see the "SDLC Communications Adapter Device Addresses" table later in this section) and writing the appropriate control word to the device (hex 98) to set ports A, B, and C to the modes described previously in this section.

Next, a bit pattern is output to port C which disallows interrupts, sets wrap mode on, and gates the external clock pins (address = hex 382, data = hex 0D). The adapter is now isolated from the communications interface.

Using bit 4 of port B, the 8273 reset line is brought high, held and then dropped. This resets the internal registers of the 8273.

The 8253-5's counter 1 and 2 terminal-count values are now set to values which will provide the desired time delay before a level 4 interrupt is generated. These interrupts may be used to indicate to the communication software that a pre-determined period of time has elapsed without a result interrupt (interrupt level 3). The terminal count-values for these counters are set for any time delay which the programmer requires. Counter 0 is also set at this time to mode 3 (generates square wave signal, used to drive counter 2 input).

To setup the counter modes, the address for the 8253 counter mode register is selected (see the "SDLC Communications Adapter Device Addresses" table, later in this section), and the control word for each individual counter is written to the device separately. The control-word format and bit definitions for the 8253 are shown below. Note that the two most-significant bits of the control word select each individual counter, and each counter mode is defined separately.

Once the support devices have been initialized to the proper modes and the 8273 has been reset, the 8273 protocol controller is ready to be configured for the operating mode that defines the communications environment in which it will be used.

**Control Word Format**

D <sub>7</sub>	D <sub>6</sub>	D <sub>5</sub>	D <sub>4</sub>	D <sub>3</sub>	D <sub>2</sub>	D <sub>1</sub>	D <sub>0</sub>
SC1	SC0	RL1	RLO	M2	M1	M0	BCD

**Definitions of Control**

**SC - Select Counter:**

SC1      SC0

0	0	Select Counter 0
0	1	Select Counter 1
1	0	Select Counter 2
1	1	Illegal

**RL - Read/Load:**

RL1      RLO

0	0	Counter Latching operation
1	0	Read/Load most significant byte (MSB)
0	1	Read/Load least significant byte (LSB)
1	1	Read/Load least significant byte first, then most significant byte.

**M - Mode:**

M2      M1      M0      Mode

0	0	0	Mode 0
0	0	1	Mode 1
X	1	0	Mode 2
X	1	1	Mode 3
1	0	0	Mode 4
1	0	1	Mode 5

**BCD:**

0	Binary Counter 16-bits
1	Binary Coded Decimal (BCD) Counter (4 Decades)

**8253-5 Programmable Interval Timer Control Word**

# Initialization/Configuration Commands

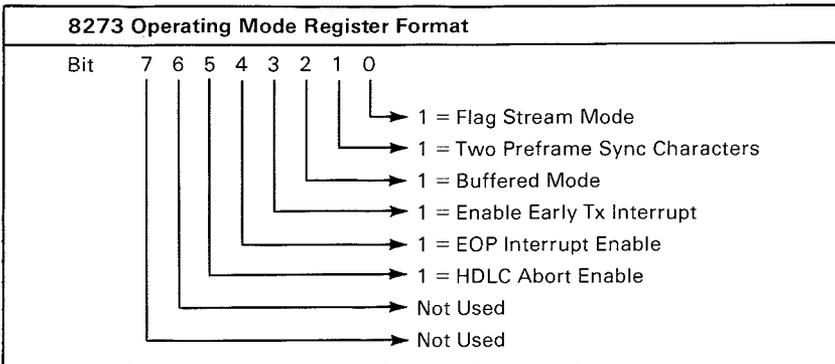
The initialization/configuration commands manipulate internal registers of the 8273, which define operating modes. After chip reset, the 8273 defaults to all 1's in the mode registers. The initialization/configuration commands either set or reset specified bits in the registers depending on the type of command. One parameter is required with the commands. The parameter is actually the bit pattern (mask) used by the set or reset command to manipulate the register bits.

Set commands perform a logical OR operation of the parameter (mask) of the internal register. This mask contains 1's where register bits are to be set. Zero (0's) in the mask cause no change to the corresponding register bit.

Reset commands perform a logical AND operation of the parameter (mask) and internal register. The mask 0 is reset to register bit, and 1 to cause no change.

The following are descriptions of each bit of the operating, serial I/O, one-bit delay, and data transfer mode registers.

## Operating Mode Register

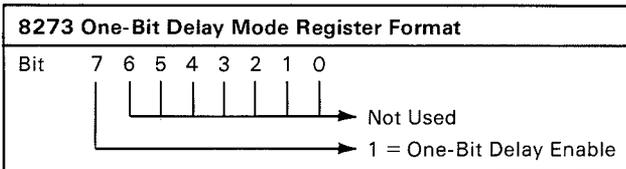


- Bit 0** If bit 0 is set to a 1, flags are sent immediately if the transmitter was idle when the bit was set. If a transmit or transmit-transparent command was active, flags are sent immediately after transmit completion. This mode is ignored if loop transmit is active or the one-bit-delay mode register is set for one-bit delay. If bit 0 is reset (to 0), the transmitter sends idles on the next character boundary if idle or, after transmission is complete, if the transmitter was active at bit-0 reset time.
- Bit 1** If bit 1 is set to a 1, the 8273 sends two characters before the first flag of a frame. These characters are hex 00 if NRZI is set or hex 55 if NRZI is not set. (See "Serial I/O Mode Register," for NRZI encoding mode format.)
- Bit 2** If bit 2 is set to a 1, the 8273 buffers the first two bytes of a received frame (the bytes are not passed to memory). Resetting this bit (to 0) causes these bytes to be passed to and from memory.
- Bit 3** This bit indicates to the 8273 when to generate an end-of-frame interrupt. If bit 3 is set, an early interrupt is generated when the last data character has been passed to the 8273. If the processor responds to the early interrupt with another transmit command before the final flag is sent, the final-flag interrupt will not be generated and a new frame will begin when the current frame is complete. Thus, frames may be sent separated by a single flag. A reset condition causes an interrupt to be generated only following a final flag.
- Bit 4** This is the EOP-interrupt-mode function and is not used on the SDLC communications adapter. This bit should always be in the reset condition.
- Bit 5** This bit is always reset for SDLC operation, which causes the 8273 protocol controller to recognize eight ones (0 1 1 1 1 1 1 1) as an abort character.



When the data transfer mode register is set, the 8273 protocol controller will interrupt when data bytes are required for transmission, or are available from a reception. If a transmit or receive interrupt occurs and the status register indicates that there is no transmit or receive interrupt result, the interrupt is a transmit or receive data request, respectively. Reset of this register causes DMA requests to be performed with no interrupts to the processor.

## One-Bit Delay Mode Register



When one-bit delay is set, the 8273 retransmits the received data stream one-bit delayed. Reset of this bit stops the one-bit delay mode.

The table below is a summary of all set and reset commands associated with the 8273 mode registers. The set or reset mask used to define individual bits is treated as a single parameter. No result or interrupt is generated by the 8273 after execution of these commands.

Register	Command	Hex Code	Parameter
One-Bit Delay Mode	Set	A4	Set Mask
	Reset	64	Reset Mask
Data Transfer Mode	Set	97	Set Mask
	Reset	57	Reset Mask
Operating Mode	Set	91	Set Mask
	Reset	51	Reset Mask
Serial I/O Mode	Set	A0	Set Mask
	Reset	60	Reset Mask

### 8273 SDLC Protocol Controller Mode Register Commands

## Command Phase

Although the 8273 is a full duplex device, there is only one command register. Thus, the command register must be used for only one command sequence at a time and the transmitter and receiver may never be simultaneously in a command phase.

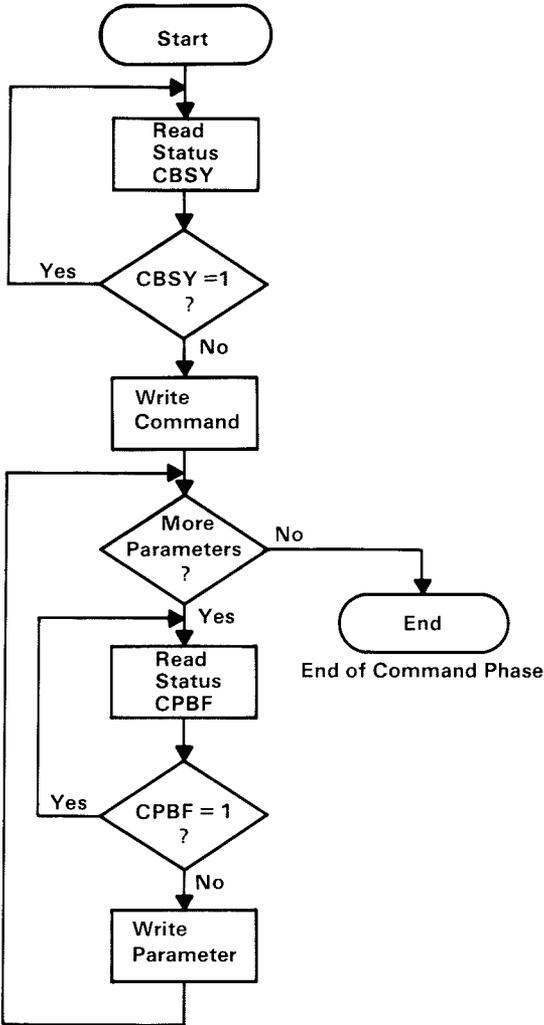
The system software starts the command phase by selecting the 8273 command register address and writing a command byte into the register. The following table lists command and parameter information for the 8273 protocol controller. If further information is required by the 8273 prior to execution of the command, the system software must write this information into the parameter register.

Command Description	Command (Hex)	Parameter	Results	Result Port	Completion Interrupt
Set One-Bit Delay	A4	Set Mask	None	—	No
Reset One-Bit Delay	64	Reset Mask	None	—	No
Set Data Transfer Mode	97	Set Mask	None	—	No
Reset Data Transfer Mode	57	Reset Mask	None	—	No
Set Operating Mode	91	Set Mask	None	—	No
Reset Operating Mode	51	Reset Mask	None	—	No
Set Serial I/O Mode	A0	Set Mask	None	—	No
Reset Serial I/O Mode	60	Reset Mask	None	—	No
General Receive	C0	80,81	RIC,R0,R1, A,C	RXI/R	Yes
Selective Receive	C1	80,81,A1, A2	RIC,R0,R1, A,C	RXI/R	Yes
Receive Disable	C5	None	None	—	No
Transmit Frame	C8	L0,L1,A,C	TIC	TXI/R	Yes
Transmit Transparent	C9	L0,L1	TIC	TXI/R	Yes
Abort Transmit Frame	CC	None	TIC	TXI/R	Yes
Abort Transmit Transparent	CD	None	TIC	TXI/R	Yes
Read Port A	22	None	Port Value	Result	No
Read Port B	23	None	Port Value	Result	No
Set Port B Bit	A3	Set Mask	None	—	No
Reset Port B Bit	63	Reset Mask	None	—	No

### 8273 Command Summary Key

- B0** — Least significant byte of the receiver buffer length.  
**B1** — Most significant byte of the receiver buffer length.  
**L0** — Least significant byte of the Tx frame length.  
**L1** — Most significant byte of the Tx frame length.  
**A1** — Receive frame address match field one.  
**A2** — Receive frame address match field two.  
**A** — Address field of received frame. If non-buffered mode is specified, this result is not provided.  
**C** — Control field of received frame. If non-buffered mode is specified, this result is not provided.  
**RXI/R** — Receive interrupt result register.  
**TXI/R** — Transmit interrupt result register.  
**R0** — Least significant byte of the length of the frame received.  
**R1** — Most significant byte of the length of the frame received.  
**RIC** — Receiver interrupt result code.  
**TIC** — Transmitter interrupt result code.

A flowchart of the command phase is shown below. Handshaking of the command and parameter bytes is accomplished by the CBSY and CPBF bits of the status register. A command may not be written if the 8273 is busy (CBSY = 1). The original command will be overwritten if a second command is issued while CBSY = 1. The flowchart also indicates a parameter buffer full check. The processor must wait until CPBF = 0 before writing a parameter to the parameter register. Previous parameters are overwritten and lost if a parameter is written while CPBF = 1.



8273 SDLC Protocol Controller Command Phase Flowchart

## Execution Phase

During the execution phase, the operation specified by the command phase is performed. If DMA is utilized for data transfers, no processor involvement is required.

For interrupt-driven transfers the 8273 raises the appropriate INT pin (TxINT or RxINT). When the processor responds to the interrupt, it must determine the cause by examining the status register and the associated IRA (interrupt result available) bit of the status register. If  $IRA = 0$ , the interrupt is a data transfer request. If  $IRA = 1$ , an operation is complete and the associated interrupt result register must be read to determine completion status.

## Result Phase

During the result phase, the 8273 notifies the processor of the outcome of a command execution. This phase is initiated by either a successful completion or error detection during execution.

Some commands such as reading or writing the I/O ports provide immediate results. These results are made available to the processor in the 8273 result register. Presence of a valid immediate result is indicated by the CRBF (command result buffer full) bit of the status register.

Non-immediate results deal with the transmitter and receiver. These results are provided in the TxI/R (transmit interrupt result) or RxI/R (receiver interrupt result) registers, respectively. The 8273 notifies the processor that a result is available with the TxIRA and RxIRA bits of the status register. Results consist of one-byte result interrupt code indicating the condition for the interrupt and, if required, one or more bytes supplying additional information. The "Result Code Summary" table later in this section provides information on the format and decode of the transmitter and receiver results.

The following are typical frame transmit and receive sequences. These examples assume DMA is utilized for data transfer operations.

## Transmit

Before a frame can be transmitted, the DMA controller is supplied, by the communication software, the starting address for the desired information field. The 8273 is then commanded to transmit a frame (by issuing a transmit frame command).

After a command, but before transmission begins, the 8273 needs some more information (parameters). Four parameters are required for the transmit frame command; the frame address field byte, the frame control field byte, and two bytes which are the least significant and most significant bytes of the information field byte length. Once all four parameters are loaded, the 8273 makes RTS (request to send) active and waits for CTS (clear to send) to go active from the modem interface. Once CTS is active, the 8273 starts the frame transmission. While the 8273 is transmitting the opening flag, address field, and control field, it starts making transmitter DMA requests. These requests continue at character (byte) boundaries until the pre-loaded number of bytes of information field have been transmitted. At this point, the requests stop, the FCS (frame check sequence) and closing flag are transmitted, and the TxINT line is raised, signaling the processor the frame transmission is complete and the result should be read. Note that after the initial command and parameter loading, no processor intervention was required (since DMA is used for data transfers) until the entire frame was transmitted.

## General Receive

Receiver operation is very similar. Like the initial transmit sequence, the processor's DMA controller is loaded with a starting address for a receive data buffer and the 8273 is commanded to receive. Unlike the transmitter, there are two different receive commands; a general receive, where all received frames are transferred to memory, and selective receive, where only frames having an address field matching one of two preprogrammed 8273 address fields are transferred to memory.

(This example covers a general receive operation.) After the receive command, two parameters are required before the receiver becomes active; the least significant and most significant bytes of the receiver buffer length. Once these bytes are loaded, the receiver is active and the processor may return to other tasks. The next frame appearing at the receiver input is transferred to memory using receiver DMA requests. When the closing flag is received, the 8273 checks the FCS and raises its RxINT line. The processor can then read the results, which indicate if the frame was error-free or not. (If the received frame had been longer than the pre-loaded buffer length, the processor would have been notified of that occurrence earlier with a receiver error interrupt. Like the transmit example, after the initial command, the processor is free for other tasks until a frame is completely received.

## Selective Receive

In selective receive, two parameters (A1 and A2) are required in addition to those for general receive. These parameters are two address match bytes. When commanded to selective receive, the 8273 passes to memory or the processor only those frames having an address field matching either A1 or A2. This command is usually used for secondary stations with A1 designating the secondary address and A2 being the "all parties" address. If only one match byte is needed, A1 and A2 should be equal. As in general receive, the 8273 counts the incoming data bytes and interrupts the processor if the received frame is larger than the preset receive buffer length.

# Result Code Summary

	Hex Code	Result	Status After Interrupt
T r a n s m i t	0C	Early Transmit Interrupt	Transmitter Active
	0D	Frame Transmit Complete	Idle or Flags
	0E	DMA Underrun	Abort
	0F	Clear to Send Error	Abort
	10	Abort Complete	Idle or Flags
R e c e i v e	X0	A1 Match or General Receive	Active
	X1	A2 Match	Active
	03	CRC Error	Active
	04	Abort Detected	Active
	05	Idle Detected	Disabled
	06	EOP Detected	Disabled
	07	Frame Less Than 32 Bits	Active
	08	DMA Overrun	Disabled
	09	Memory Buffer Overflow	Disabled
	0A	Carrier Detect Failure	Disabled
0B	Receiver Interrupt Overrun	Disabled	
<b>Note:</b> X decodes to number of bits in partial byte received.			

The first two codes in the receive result code table result from the error free reception of a frame. Since SDLC allows frames of arbitrary length (>32 bits), the high order bits of the receive result report the number of valid received bits in the last received information field byte. The chart below shows the decode of this receive result bit.

X	Bits Received in Last Byte
E	All Eight Bits of Last Byte
0	Bit0 Only
8	Bit1-Bit0
4	Bit2-Bit0
C	Bit3-Bit0
2	Bit4-Bit0
A	Bit5-Bit0
6	Bit6-Bit0

# Address and Interrupt Information

The following tables provide address and interrupt information for the SDLC adapter:

Hex Code	Device	Register Name	Function
380	8255	Port A Data	Internal/External Sensing
381	8255	Port B Data	External Modem Interface
382	8255	Port C Data	Internal Control
383	8255	Mode Set	8255 Mode Initialization
384	8253	Counter 0 LSB	Square Wave Generator
384	8253	Counter 0 MSB	Square Wave Generator
385	8253	Counter 1 LSB	Inactivity Time-Outs
385	8253	Counter 1 MSB	Inactivity Time-Outs
386	8253	Counter 2 LSB	Inactivity Time-Outs
386	8253	Counter 2 MSB	Inactivity Time-Outs
387	8253	Mode Register	8253 Mode Set
388	8273	Command/Status	Out=Command In=Status
389	8273	Parameter/Result	Out=Parameter In=Status
38A	8273	Transmit INT Status	DMA/INT
38B	8273	Receive INT Status	DMA/INT
38C	8273	Data	DPC (Direct Program Control)

## SDLC Communications Adapter Device Addresses

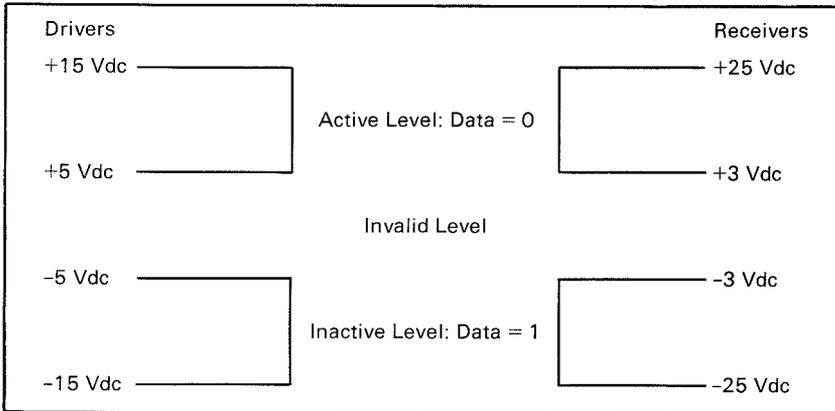
Interrupt Level 3	Transmit/Receive Interrupt
Interrupt Level 4	Timer 1 Interrupt Timer 2 Interrupt Clear to Send Changed Data Set Ready Changed
DMA Level One is used for Transmit and Receive	

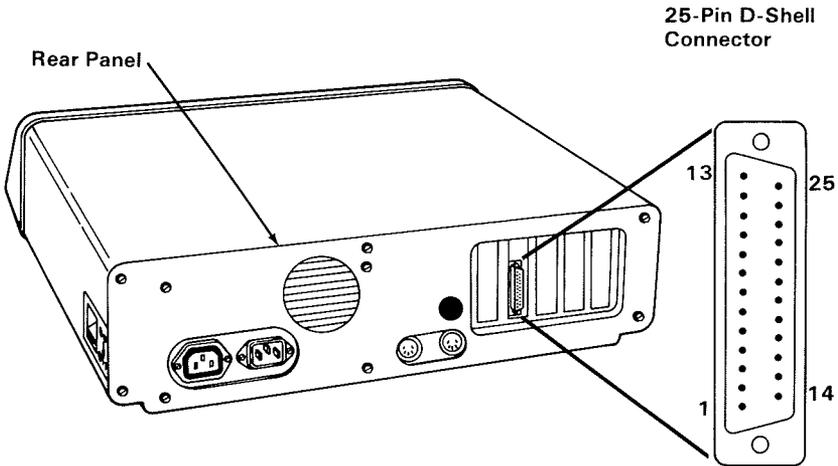
## Interrupt Information

# Interface Information

The SDLC communications adapter conforms to interface signal levels standardized by the Electronics Industries Association RS-232C Standard. These levels are shown in the figure below.

Additional lines used but not standardized by EIA are pins 11, 18, and 25. These lines are designated as select standby, test and test indicate, respectively. Select Standby is used to support the switched network backup facility of a modem providing this option. Test and test indicate support a modem wrap function on modems which are designed for business machine controlled modem wraps. Two jumpers on the adapter (P1 and P2) are used to connect test and test indicate to the interface, if required (see Appendix D for these jumpers).





Signal Name — Description	Pin
No Connection	1
Transmitted Data	2
Received Data	3
Request to Send	4
Clear to Send	5
Data Set Ready	6
Signal Ground	7
Received Line Signal Detector	8
No Connection	9
No Connection	10
Select Standby*	11
No Connection	12
No Connection	13
No Connection	14
Transmitter Signal Element Timing	15
No Connection	16
Receiver Signal Element Timing	17
Test (IBM Modems Only)*	18
No Connection	19
Data Terminal Ready	20
No Connection	21
Ring Indicator	22
Data Signal Rate Selector	23
No Connection	24
Test Indicate (IBM Modems Only)*	25

Synchronous Data Link Control Communications Adapter

\*Not standardized by EIA (Electronics Industry Association).

**Connector Specifications**

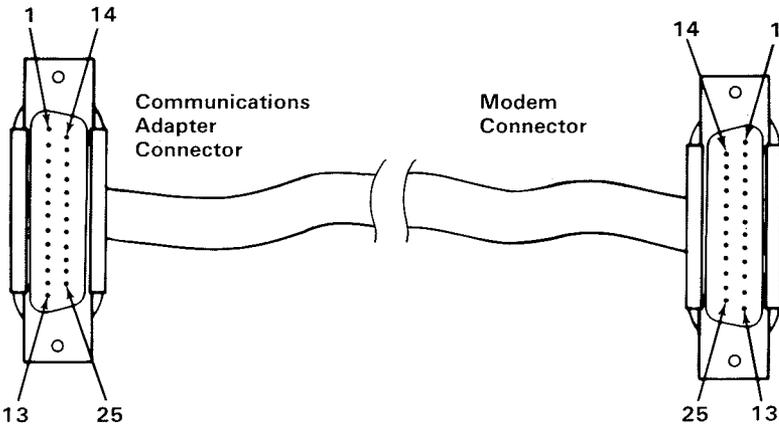
# Notes:

# IBM Communications Adapter Cable

The IBM Communications Adapter Cable is a ten foot cable for connection of an IBM communications adapter to a modem or other RS-232C DCE (data communications equipment). It is fully shielded and provides a high quality, low noise channel for interface between the communications adapter and DCE.

The connector ends are 25-pin D-shell connectors. All pin connections conform with the EIA RS-232C standard. In addition, connection is provided on pins 11, 18 and 25. These pins are designated as select standby, test and test indicate, respectively, on some modems. Select standby is used to support the switched network backup facility, if applicable. Test and test indicate support a modem wrap function on modems designed for business machine controlled modem wraps.

The IBM Communications Adapter Cable connects the following pins on the 25-pin D-shell connectors.



Communications Adapter Connector Pin #	Name	Modem Connector Pin #
NC	Outer Cable Shield	1
2	Transmitted Data	2
3	Received Data	3
4	Request to Send	4
5	Clear to Send	5
6	Data Set Ready	6
7	Signal Ground (Inner Lead Shields)	7
8	Received Line Signal Detector	8
NC		NC
NC		NC
11	Select Standby	11
NC		NC
NC		NC
NC		NC
15	Transmitter Signal Element Timing	15
NC		NC
17	Receiver Signal Element Timing	17
18	Test	18
NC		NC
20	Data Terminal Ready	20
NC		NC
22	Ring Indicator	22
23	Data Signal Rate Selector	23
NC		NC
25	Test Indicate	25

### Connector Specifications

## 1-302 Communications Cable

# SECTION 2: ROM BIOS AND SYSTEM USAGE

ROM BIOS ..... 2-2  
Keyboard Encoding and Usage ..... 2-11  
BIOS Cassette Logic ..... 2-21

**BIOS**

# Notes:

# ROM BIOS

The basic input/output system (BIOS) resides in ROM on the system board and provides device level control for the major I/O devices in the system. Additional ROM modules may be located on option adapters to provide device level control for that option adapter. BIOS routines enable the assembly language programmer to perform block (disk and diskette) or character-level I/O operations without concern for device address and operating characteristics. System services, such as time-of-day and memory size determination, are provided by the BIOS.

The goal is to provide an operational interface to the system and relieve the programmer of the concern about the characteristics of hardware devices. The BIOS interface insulates the user from the hardware, thus allowing new devices to be added to the system, yet retaining the BIOS level interface to the device. In this manner, user programs become transparent to hardware modifications and enhancements.

The IBM Personal Computer MACRO Assembler manual and the IBM Personal Computer Disk Operating System (DOS) manual provide useful programming information related to this section. A complete listing of the BIOS is given in Appendix A.

## Use of BIOS

Access to BIOS is through the 8088 software interrupts. Each BIOS entry point is available through its own interrupt, which can be found in the "8088 Software Interrupt Listing."

The software interrupts, hex 10 through hex 1A, each access a different BIOS routine. For example, to determine the amount of memory available in the system,

INT 12H

will invoke the BIOS routine for determining memory size and will return the value to the caller.

# Parameter Passing

All parameters passed to and from the BIOS routines go through the 8088 registers. The prolog of each BIOS function indicates the registers used on the call and the return. For the memory size example, no parameters are passed. The memory size, in 1K byte increments, is returned in the AX register.

If a BIOS function has several possible operations, the AH register is used at input to indicate the desired operation. For example, to set the time of day, the following code is required:

```
MOV AH,1           ;function is to set time of day.
MOV CX,HIGH_COUNT ;establish the current time.
MOV DX,LOW_COUNT
INT 1AH           ;set the time.
```

To read the time of day:

```
MOV AH,0           ;function is to read time of
                   ;day.
INT 1AH           ;read the timer.
```

Generally, the BIOS routines save all registers except for AX and the flags. Other registers are modified on return only if they are returning a value to the caller. The exact register usage can be seen in the prolog of each BIOS function.

Address (Hex)	Interrupt Number	Name	BIOS Entry
0-3	0	Divide by Zero	D_EOI
4-7	1	Single Step	D_EOI
8-B	2	Nonmaskable	NMI_INT
C-F	3	Breakpoint	D_EOI
10-13	4	Overflow	D_EOI
14-17	5	Print Screen	PRINT_SCREEN
18-1B	6	Reserved	D_EOI
1D-1F	7	Reserved	D_EOI
20-23	8	Time of Day	TIMER_INT
24-27	9	Keyboard	KB_INT
28-2B	A	Reserved	D_EOI
2C-2F	B	Communications	D_EOI
30-33	C	Communications	D_EOI
34-37	D	Disk	D_EOI
38-3B	E	Diskette	DISK_INT
3C-3F	F	Printer	D_EOI
40-43	10	Video	VIDEO_IO
44-47	11	Equipment Check	EQUIPMENT
48-4B	12	Memory	MEMORY_SIZE_DETERMINE
4C-4F	13	Diskette/Disk	DISKETTE_IO
50-53	14	Communications	RS232_IO
54-57	15	Cassette	CASSETTE_IO
58-5B	16	Keyboard	KEYBOARD_IO
5C-5F	17	Printer	PRINTER_IO
60-63	18	Resident BASIC	F600:0000
64-67	19	Bootstrap	BOOT_STRAP
68-6B	1A	Time of Day	TIME_OF_DAY
6C-6F	1B	Keyboard Break	DUMMY_RETURN
70-73	1C	Timer Tick	DUMMY_RETURN
74-77	1D	Video Initialization	VIDEO_PARMS
78-7B	1E	Diskette Parameters	DISK_BASE
7C-7F	1F	Video Graphics Chars	0

### 8088 Software Interrupt Listing

# Vectors with Special Meanings

## Interrupt Hex 1B – Keyboard Break Address

This vector points to the code to be exercised when the Ctrl and Break keys are pressed on the keyboard. The vector is invoked while responding to the keyboard interrupt, and control should be returned through an IRET instruction. The power-on routines initialize this vector to point to an IRET instruction, so that nothing will occur when the Ctrl and Break keys are pressed unless the application program sets a different value.

Control may be retained by this routine, with the following problems. The Break may have occurred during interrupt processing, so that one or more End of Interrupt commands must be sent to the 8259 controller. Also, all I/O devices should be reset in case an operation was underway at that time.

## Interrupt Hex 1C – Timer Tick

This vector points to the code to be executed on every system-clock tick. This vector is invoked while responding to the timer interrupt, and control should be returned through an IRET instruction. The power-on routines initialize this vector to point to an IRET instruction, so that nothing will occur unless the application modifies the pointer. It is the responsibility of the application to save and restore all registers that will be modified.

## Interrupt Hex 1D – Video Parameters

This vector points to a data region containing the parameters required for the initialization of the 6845 on the video card. Note that there are four separate tables, and all four must be reproduced if all modes of operation are to be supported. The power-on routines initialize this vector to point to the parameters contained in the ROM video routines.

## **Interrupt Hex 1E – Diskette Parameters**

This vector points to a data region containing the parameters required for the diskette drive. The power-on routines initialize the vector to point to the parameters contained in the ROM diskette routine. These default parameters represent the specified values for any IBM drives attached to the machine. Changing this parameter block may be necessary to reflect the specifications of the other drives attached.

## **Interrupt Hex 1F – Graphics Character Extensions**

When operating in the graphics modes of the IBM Color/Graphics Monitor Adapter (320 by 200 or 640 by 200), the read/write character interface will form the character from the ASCII code point, using a set of dot patterns. The dot patterns for the first 128 code points are contained in ROM. To access the second 128 code points, this vector must be established to point at a table of up to 1K bytes, where each code point is represented by eight bytes of graphic information. At power-on, this vector is initialized to 000:0, and it is the responsibility of the user to change this vector if the additional code points are required.

## **Interrupt Hex 40 – Reserved**

When an IBM Fixed Disk Drive Adapter is installed, the BIOS routines use interrupt hex 40 to revector the diskette pointer.

## **Interrupt Hex 41 – Fixed Disk Parameters**

This vector points to a data region containing the parameters required for the fixed disk drive. The power-on routines initialize the vector to point to the parameters contained in the ROM disk routine. These default parameters represent the specified values for any IBM Fixed Disk Drives attached to the machine. Changing this parameter block may be necessary to reflect the specifications of the other fixed disk drives attached.

# Other Read/Write Memory Usage

The IBM BIOS routines use 256 bytes of memory starting at absolute hex 400 to hex 4FF. Locations hex 400 to 407 contain the base addresses of any RS-232C cards attached to the system. Locations hex 408 to 40F contain the base addresses of the printer adapter.

Memory locations hex 300 to 3FF are used as a stack area during the power-on initialization, and bootstrap, when control is passed to it from power-on. If the user desires the stack in a different area, the area must be set by the application.

Address (Hex)	Interrupt (Hex)	Function
80-83	20	DOS Program Terminate
84-87	21	DOS Function Call
88-8B	22	DOS Terminate Address
8C-8F	23	DOS Ctrl Break Exit Address
90-93	24	DOS Fatal Error Vector
94-97	25	DOS Absolute Disk Read
98-9B	26	DOS Absolute Disk Write
9C-9F	27	DOS Terminate, Fix In Storage
A0-FF	28-3F	Reserved for DOS
100-17F	40-5F	Reserved
180-19F	60-67	Reserved for User Software Interrupts
1A0-1FF	68-7F	Not Used
200-217	80-85	Reserved by BASIC
218-3C3	86-F0	Used by BASIC Interpreter while BASIC is running
3C4-3FF	F1-FF	Not Used

## BASIC and DOS Reserved Interrupts

Address (Hex)	Mode	Function
400-48F 490-4EF 4F0-4FF	ROM BIOS	See BIOS Listing Reserved Reserved as Intra-Application Communication Area for any application
500-5FF 500	DOS	Reserved for DOS and BASIC Print Screen Status Flag Store 0-Print Screen Not Active or Successful Print Screen Operation 1-Print Screen In Progress 255-Error Encountered during Print Screen Operation
504	DOS	Single Drive Mode Status Byte
510-511	BASIC	BASIC's Segment Address Store
512-515	BASIC	Clock Interrupt Vector Segment: Offset Store
516-519	BASIC	Break Key Interrupt Vector Segment: Offset Store
51A-51D	BASIC	Disk Error Interrupt Vector Segment: Offset Store

### Reserved Memory Locations

If you do DEF SEG (Default workspace segment):

	Offset (Hex Value)	Length		
Line number of current line being executed	2E	2		
Line number of last error	347	2		
Offset into segment of start of program text	30	2		
Offset into segment of start of variables (end of program text 1-1)	358	2		
Keyboard buffer contents if 0-no characters in buffer if 1-characters in buffer	6A	1		
Character color in graphics mode Set to 1, 2, or 3 to get text in colors 1 to 3. Do not set to 0. (Default = 3)	4E	1		
<p>Example</p> <pre>100 Print PEEK (&amp;H2E) + 256*PEEK (&amp;H2F)</pre> <div style="display: flex; align-items: center; margin-left: 40px;"> <div style="font-size: 2em; margin-right: 10px;">}</div> <div style="display: flex; gap: 20px;"> <div style="text-align: center;">L</div> <div style="text-align: center;">H</div> </div> </div> <div style="display: flex; align-items: center; margin-left: 40px; margin-top: 10px;"> <div style="margin-right: 10px;">100</div> <table border="1" style="border-collapse: collapse;"> <tr> <td style="padding: 2px 10px;">Hex 64</td> <td style="padding: 2px 10px;">Hex 00</td> </tr> </table> </div>			Hex 64	Hex 00
Hex 64	Hex 00			

### BASIC Workspace Variables

## Starting Address in Hex

00000	BIOS Interrupt Vectors
00080	Available Interrupt Vectors
00400	BIOS Data Area
00500	User Read/Write Memory
C8000	Disk Adapter
F0000	Read Only Memory
FE000	Bios Program Area

## BIOS Memory Map

# BIOS Programming Hints

The BIOS code is invoked through software interrupts. The programmer should not “hard code” BIOS addresses into applications. The internal workings and absolute addresses within BIOS are subject to change without notice.

If an error is reported by the disk or diskette code, you should reset the drive adapter and retry the operation. A specified number of retries should be required on diskette reads to ensure the problem is not due to motor start-up.

When altering I/O port bit values, the programmer should change only those bits which are necessary to the current task. Upon completion, the programmer should restore the original environment. Failure to adhere to this practice may be incompatible with present and future applications.

# Adapter Cards with System-Accessible ROM Modules

The ROM BIOS provides a facility to integrate adapter cards with on board ROM code into the system. During the POST, interrupt vectors are established for the BIOS calls. After the default vectors are in place, a scan for additional ROM modules takes place. At this point, a ROM routine on the adapter card may gain control. The routine may establish or intercept interrupt vectors to hook themselves into the system.

The absolute addresses hex C8000 through hex F4000 are scanned in 2K blocks in search of a valid adapter card ROM. A valid ROM is defined as follows:

- Byte 0: Hex 55
- Byte 1: Hex AA
- Byte 2: A length indicator representing the number of 512 byte blocks in the ROM (length/512).  
A checksum is also done to test the integrity of the ROM module. Each byte in the defined ROM is summed modulo hex 100. This sum must be 0 for the module to be deemed valid.

When the POST identifies a valid ROM, it does a far call to byte 3 of the ROM (which should be executable code). The adapter card may now perform its power-on initialization tasks. The feature ROM should return control to the BIOS routines by executing a far return.

**Notes:**

# Keyboard Encoding and Usage

## Encoding

The keyboard routine provided by IBM in the ROM BIOS is responsible for converting the keyboard scan codes into what will be termed “Extended ASCII.”

Extended ASCII encompasses one-byte character codes with possible values of 0 to 255, an extended code for certain extended keyboard functions, and functions handled within the keyboard routine or through interrupts.

## Character Codes

The following character codes are passed through the BIOS keyboard routine to the system or application program. A “-1” means the combination is suppressed in the keyboard routine. The codes are returned in AL. See Appendix C for the exact codes. Also, see “Keyboard Scan Code Diagram” in Section 1.

Key Number	Base Case	Upper Case	Ctrl	Alt
1	Esc	Esc	Esc	-1
2	1	!	-1	Note 1
3	2	@	Nul (000) Note 1	Note 1
4	3	#	-1	Note 1
5	4	\$	-1	Note 1
6	5	%	-1	Note 1
7	6	^	RS(030)	Note 1
8	7	&	-1	Note 1
9	8	*	-1	Note 1
10	9	(	-1	Note 1
11	0	)	-1	Note 1
12	-	—	US(031)	Note 1
13	=	+	-1	Note 1
14	Backspace (008)	Backspace (008)	Del (127)	-1
15	→ (009)	← (Note 1)	-1	-1
16	q	Q	DC1 (017)	Note 1
17	w	W	ETB (023)	Note 1

Key Number	Base Case	Upper Case	Ctrl	Alt
18	e	E	ENQ (005)	Note 1
19	r	R	DC2 (018)	Note 1
20	t	T	DC4 (020)	Note 1
21	y	Y	EM (025)	Note 1
22	u	U	NAK (021)	Note 1
23	i	I	HT (009)	Note 1
24	o	O	SI (015)	Note 1
25	p	P	DLE (016)	Note 1
26	[	{	Esc (027)	-1
27	]	}	GS (029)	-1
28	CR	CR	LF (010)	-1
29 Ctrl	-1	-1	-1	-1
30	a	A	SOH (001)	Note 1
31	s	S	DC3 (019)	Note 1
32	d	D	EOT (004)	Note 1
33	f	F	ACK (006)	Note 1
34	g	G	BEL (007)	Note 1
35	h	H	BS (008)	Note 1
36	j	J	LF (010)	Note 1
37	k	K	VT (011)	Note 1
38	l	L	FF (012)	Note 1
39	;	:	-1	-1
40	'	"	-1	-1
41	`	~	-1	-1
42 Shift	-1	-1	-1	-1
43	\		FS (028)	-1
44	z	Z	SUB (026)	Note 1
45	x	X	CAN (024)	Note 1
46	c	C	ETX (003)	Note 1
47	v	V	SYN (022)	Note 1
48	b	B	STX (002)	Note 1
49	n	N	SO (014)	Note 1
50	m	M	CR (013)	Note 1
51	,	<	-1	-1
52	.	>	-1	-1
53	/	?	-1	-1
54 Shift	-1	-1	-1	-1
55	*	(Note 2)	(Note 1)	-1
56 Alt	-1	-1	-1	-1
57	SP	SP	SP	SP
58 Caps Lock	-1	-1	-1	-1
59	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)
60	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)
61	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)
62	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)
63	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)
64	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)

### Character Codes (Part 2 of 3)

Key Number	Base Case	Upper Case	Ctrl	Alt
65	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)
66	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)
67	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)
68	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)	Nul (Note 1)
69 Num Lock	-1	-1	Pause (Note 2)	-1
70 Scroll Lock	-1	-1	Break (Note 2)	-1

**Notes:** 1. Refer to "Extended Codes" in this section.  
2. Refer to "Special Handling" in this section.

### Character Codes (Part 3 of 3)

Keys 71 to 83 have meaning only in base case, in Num Lock (or shifted) states, or in Ctrl state. It should be noted that the shift key temporarily reverses the current Num Lock state.

Key Number	Num Lock	Base Case	Alt	Ctrl
71	7	Home (Note 1)	-1	Clear Screen
72	8	↑ (Note 1)	-1	-1
73	9	Page Up (Note 1)	-1	Top of Text and Home
74	-	-----	-1	-1
75	4	← (Note 1)	-1	Reverse Word (Note 1)
76	5	-1	-1	-1
77	6	→ (Note 1)	-1	Advance Word (Note 1)
78	+	+	-1	-1
79	1	End (Note 1)	-1	Erase to EOL (Note 1)
80	2	↓ (Note 1)	-1	-1
81	3	Page Down (Note 1)	-1	Erase to EOS (Note 1)
82	0	Ins	-1	-1
83	.	Del (Notes 1,2)	Note 2	Note 2

**Notes:** 1. Refer to "Extended Codes" in this section.  
2. Refer to "Special Handling" in this section.

# Extended Codes

## Extended Functions

For certain functions that cannot be represented in the standard ASCII code, an extended code is used. A character code of 000 (Nul) is returned in AL. This indicates that the system or application program should examine a second code that will indicate the actual function. Usually, but not always, this second code is the scan code of the primary key that was pressed. This code is returned in AH.

Second Code	Function
3	Nul Character
15	←
16-25	Alt Q, W, E, R, T, Y, U, I, O, P
30-38	Alt A, S, D, F, G, H, J, K, L
44-50	Alt Z, X, C, V, B, N, M
59-68	F1 to F10 Function Keys Base Case
71	Home
72	↑
73	Page Up and Home Cursor
75	←
77	→
79	End
80	↓
81	Page Down and Home Cursor
82	Ins (Insert)
83	Del (Delete)
84-93	F11 to F20 (Uppercase F1 to F10)
94-103	F21 to F30 (Ctrl F1 to F10)
104-113	F31 to F40 (Alt F1 to F10)
114	Ctrl PrtSc (Start/Stop Echo to Printer)
115	Ctrl ← (Reverse Word)
116	Ctrl → (Advance Word)
117	Ctrl End [Erase to End of Line (EOL)]
118	Ctrl PgDn [Erase to End of Screen (EOS)]
119	Ctrl Home (Clear Screen and Home)
120-131	Alt 1, 2, 3, 4, 5, 6, 7, 8, 9, 0, -, = (Keys 2-13)
132	Ctrl PgUp (Top 25 Lines of Text and Home Cursor)

### Keyboard Extended Functions

## Shift States

Most shift states are handled within the keyboard routine, transparent to the system or application program. In any case, the current set of active shift states are available by calling an entry point in the ROM keyboard routine. The following keys result in altered shift states:

### Shift

This key temporarily shifts keys 2-13, 15-27, 30-41, 43-53, 55, and 59-68 to upper case (base case if in Caps Lock state). Also, the Shift key temporarily reverses the Num Lock or non-Num-Lock state of keys 71-73, 75, 77, and 79-83.

### Ctrl

This key temporarily shifts keys 3, 7, 12, 14, 16-28, 30-38, 43-50, 55, 59-71, 73, 75, 77, 79, and 81 to the Ctrl state. Also, the Ctrl key is used with the Alt and Del keys to cause the “system reset” function, with the Scroll Lock key to cause the “break” function, and with the Num Lock key to cause the “pause” function. The system reset, break, and pause functions are described in “Special Handling” on the following pages.

### Alt

This key temporarily shifts keys 2-13, 16-25, 30-38, 44-50, and 59-68 to the Alt state. Also, the Alt key is used with the Ctrl and Del keys to cause the “system reset” function described in “Special Handling” on the following pages.

The Alt key has another use. This key allows the user to enter any character code from 0 to 255 into the system from the keyboard. The user holds down the Alt key and types the decimal value of the characters desired using the numeric keypad (keys 71-73, 75-77, and 79-82). The Alt key is then released. If more than three digits are typed, a modulo-256 result is created. These three digits are interpreted as a character code and are transmitted through the keyboard routine to the system or application program. Alt is handled internal to the keyboard routine.

## **Caps Lock**

This key shifts keys 16-25, 30-38, and 44-50 to upper case. A second depression of the Caps Lock key reverses the action. Caps Lock is handled internal to the keyboard routine.

## **Scroll Lock**

This key is interpreted by appropriate application programs as indicating use of the cursor-control keys should cause windowing over the text rather than cursor movement. A second depression of the Scroll Lock key reverses the action. The keyboard routine simply records the current shift state of the Scroll Lock key. It is the responsibility of the system or application program to perform the function.

## **Shift Key Priorities and Combinations**

If combinations of the Alt, Ctrl, and Shift keys are pressed and only one is valid, the precedence is as follows: the Alt key is first, the Ctrl key is second, and the Shift key is third. The only valid combination is Alt and Ctrl, which is used in the “system reset” function.

## **Special Handling**

### **System Reset**

The combination of the Alt, Ctrl, and Del keys will result in the keyboard routine initiating the equivalent of a “system reset” or “reboot.” System reset is handled internal to the keyboard.

## Break

The combination of the Ctrl and Break keys will result in the keyboard routine signaling interrupt hex 1A. Also, the extended characters (AL = hex 00, AH = hex 00) will be returned.

## Pause

The combination of the Ctrl and Num Lock keys will cause the keyboard interrupt routine to loop, waiting for any key except the Num Lock key to be pressed. This provides a system- or application-transparent method of temporarily suspending list, print, and so on, and then resuming the operation. The “unpause” key is thrown away. Pause is handled internal to the keyboard routine.

## Print Screen

The combination of the Shift and PrtSc (key 55) keys will result in an interrupt invoking the print screen routine. This routine works in the alphanumeric or graphics mode, with unrecognizable characters printing as blanks.

## Other Characteristics

The keyboard routine does its own buffering. The keyboard buffer is large enough to support a fast typist. However, if a key is entered when the buffer is full, the key will be ignored and the “bell” will be sounded.

Also, the keyboard routine suppresses the typematic action of the following keys: Ctrl, Shift, Alt, Num Lock, Scroll Lock, Caps Lock, and Ins.

# Keyboard Usage

This section is intended to outline a set of guidelines of key usage when performing commonly used functions.

Function	Key(s)	Comment
Home Cursor	Home	Editors; word processors
Return to outermost menu	Home	Menu driven applications
Move cursor up	↑	Full screen editor, word processor
Page up, scroll backward 25 lines and home	PgUp	Editors; word processors
Move cursor left	← Key 75	Text, command entry
Move cursor right	→	Text, command entry
Scroll to end of text Place cursor at end of line	End	Editors; word processors
Move cursor down	↓	Full screen editor, word processor
Page down, scroll forward 25 lines and home	Pg Dn	Editors; word processors
Start/Stop insert text at cursor, shift text right in buffer	Ins	Text, command entry
Delete character at cursor	Del	Text, command entry
Destructive backspace	← Key 14	Text, command entry
Tab forward	→	Text entry
Tab reverse	←	Text entry
Clear screen and home	Ctrl Home	Command entry
Scroll up	↑	In scroll lock mode
Scroll down	↓	In scroll lock mode
Scroll left	←	In scroll lock mode
Scroll right	→	In scroll lock mode
Delete from cursor to EOL	Ctrl End	Text, command entry
Exit/Escape	Esc	Editor, 1 level of menu, and so on
Start/Stop Echo screen to printer	Ctrl Prt Sc (Key 55)	Any time
Delete from cursor to EOS	Ctrl PgDn	Text, command entry
Advance word	Ctrl →	Text entry
Reverse word	Ctrl ←	Text entry
Window Right	Ctrl →	When text is too wide to fit screen
Window Left	Ctrl ←	When text is too wide to fit screen
Enter insert mode	Ins	Line editor

Keyboard - Commonly Used Functions (Part 1 of 2)

## 2-20 Keyboard Encoding

Function	Key(s)	Comment
Exit insert mode	Ins	Line editor
Cancel current line	Esc	Command entry, text entry
Suspend system (pause)	Ctrl Num Lock	Stop list, stop program, and so on Resumes on any key
Break interrupt	Ctrl Break	Interrupt current process
System reset	Alt Ctrl Del	Reboot
Top of document and home cursor	Ctrl PgUp	Editors, word processors
Standard function keys	F1-F10	Primary function keys
Secondary function keys	Shift F1-F10 Ctrl F1-F10 Alt F1-F10	Extra function keys if 10 are not sufficient
Extra function keys	Alt Keys 2-13 (1-9,0,-,=)	Used when stickers are put along top of keyboard
Extra function keys	Alt A-Z	Used when function starts with same letter as one of the alpha keys

### Keyboard - Commonly Used Functions (Part 2 of 2)

Function	Key
Carriage return	↵
Line feed	Ctrl ↵
Bell	Ctrl G
Home	Home
Cursor up	↑
Cursor down	↓
Cursor left	←
Cursor right	→
Advance one word	Ctrl →
Reverse one word	Ctrl ←
Insert	Ins
Delete	Del
Clear screen	Ctrl Home
Freeze output	Ctrl Num Lock
Tab advance	→
Stop execution (break)	Ctrl Break
Delete current line	Esc
Delete to end of line	Ctrl End
Position cursor to end of line	End

### BASIC Screen Editor Special Functions

Function	Key
Suspend	Ctrl Num Lock
Echo to printer	Ctrl PrtSc
Stop echo to printer	Ctrl PrtSc (Key 55 any case)
Exit current function (break)	Ctrl Break
Backspace	← Key 14
Line feed	Ctrl ↵
Cancel line	Esc
Copy character	F1 or →
Copy until match	F2
Copy remaining	F3
Skip character	Del
Skip until match	F4
Enter skip mode	Ins
Exit insert mode	ins
Make new line the template	F5
String separator in REPLACE	F6
End of file in keyboard input	F6

### DOS Special Functions

# BIOS Cassette Logic

## Software Algorithms – Interrupt Hex 15

The cassette routine will be called by the request type in AH. The address of the bytes to be read from or written to the tape will be specified by ES:BX and the number of bytes to be read or written will be specified by CX. The actual number of bytes read will be returned in DX. The read block and write block will automatically turn the cassette motor on at the start and off at the end. The request types in AH and the cassette status descriptions follow:

Request Type	Function
AH = 0	Turn Cassette Motor On
AH = 1	Turn Cassette Motor Off
AH = 2	Read Tape Block Read CX bytes into memory starting at Address ES:BX Return actual number of bytes read in DX Return Cassette Status in AH
AH = 3	Write Tape Block Write CX bytes onto cassette starting at Address DS:BX Return Cassette Status in AH

Cassette Status	Description
AH = 00	No Errors
AH = 01	Cyclic Redundancy Check (CRC) Error in Read Block
AH = 02	No Data Transitions
AH = 04	No Leader
AH = 80	Invalid Command

**Note:** The carry flag will be set on any error.

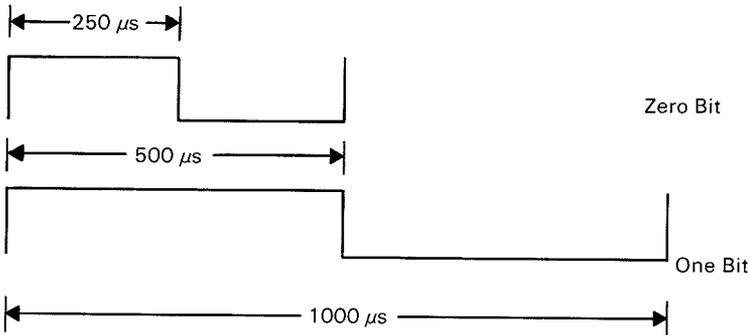
# Cassette Write

The write-block routine writes a tape block onto the cassette tape. The block is described in "Data Record Architecture" later in this section.

The write-block routine turns on the cassette drive motor and a synchronization bit (0) and then writes the leader (256 bytes of all 1's) to the tape. Next, the routine writes the number of data blocks specified by CX. After each data block of 256 bytes, a 2-byte cyclic redundancy check (CRC) is written. The data bytes are taken from the memory location pointed at by ES.

The write-byte routine disassembles and writes the byte a bit at a time to the cassette. The method used is to set Timer 2 to the period of the desired data bit. The timer is set to a period of 1.0 millisecond for a 1 bit and 0.5 millisecond for a 0 bit.

The timer is set to mode 3, which means the timer outputs a square wave with a period given by its counter register. The timer's period is changed on the fly for each data bit written to the cassette. If the number of data bytes to be written is not an integral multiple of 256, then, after the last desired data byte from memory has been written, the data block is extended to 256 bytes of writing multiples of the last data byte. The last block is closed with two CRC bytes as usual. After the last data block, a trailer consisting of four bytes of all 1 bits is written. Finally, the cassette motor is turned off, if there are no errors reported by the routine.



## Cassette Read

The read-block routine turns on the cassette drive motor and then delays for approximately 0.5 second to allow the motor to come up to speed.

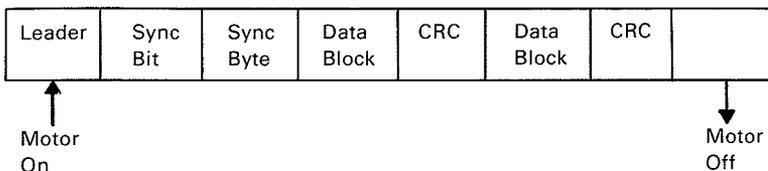
The read-block routine then searches for the leader and must detect all 1 bits for approximately 1/4 of the leader length before it can look for the sync (0) bit. After the sync bit is detected, the sync byte (ASCII character hex 16) is read. If the sync byte is read correctly, the data portion can be read. If a correct sync byte is not found, the routine goes back and searches for the leader again. The data is read a bit at a time and assembled into bytes. After each byte is assembled, it is written into memory at location ES:BX and BX is incremented by 1.

After each multiple of 256 data bytes is read, the CRC is read and compared to the CRC generated. If a CRC error is detected, the routine will exit with the carry flag set to indicate an error and the status of AH set to hex 01. DX will contain the number of bytes written memory.

The time of day interrupt (IRQ0) is disabled during the cassette-read operation.

# Data Record Architecture

The write-block routine uses the following format to record a tape block onto a cassette tape:



Component	Description
Leader	256 Bytes (of All 1's)
Sync Bit	One 0 Bit
Sync Byte	ASCII Character Hex 16
Data Blocks	256 Bytes in Length
CRC	2 Bytes for each Data Block

## Data Record Components

## Error Recovery

Error recovery is handled through software. A CRC is used to detect errors. The polynomial used is  $G(X) = X^{16} + X^{12} + X^5 + 1$ , which is the polynomial used by the synchronous data link control interface. Essentially, as bits are written to or read from the cassette tape, they are passed through the CRC register in software. After a block of data is written, the complemented value of the calculated CRC register is written on the tape. Upon reading the cassette data, the CRC bytes are read and compared to the generated CRC value. If the read CRC does not equal the generated CRC, the processor's carry flag is set and the status of AH is set to hex 01, which indicates a CRC error has occurred. Also, the routine is exited on a CRC error.

# APPENDIX A: ROM BIOS LISTINGS

	Page	Line Number
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 <b>Fixed Disk ROM BIOS</b>		
Fixed Disk I/O Interface .....	A-87	1
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Appendix A

```

LOC OBJ          LINE  SOURCE
1               $TITLE(BIOS FOR IBM PERSONAL COMPUTER)
2
3               ;-----
4               ; THE BIOS ROUTINES ARE MEANT TO BE ACCESSED THROUGH :
5               ; SOFTWARE INTERRUPTS ONLY. ANY ADDRESSES PRESENT IN :
6               ; THE LISTINGS ARE INCLUDED ONLY FOR COMPLETENESS, :
7               ; NOT FOR REFERENCE. APPLICATIONS WHICH REFERENCE :
8               ; ABSOLUTE ADDRESSES WITHIN THE CODE SEGMENT :
9               ; VIOLATE THE STRUCTURE AND DESIGN OF BIOS. :
10              ;-----
11
12              ;-----
13              ; EQUATES :
14              ;-----
0060 15 PORT_A EQU 60H ; 8255 PORT A ADDR
0061 16 PORT_B EQU 61H ; 8255 PORT B ADDR
0062 17 PORT_C EQU 62H ; 8255 PORT C ADDR
0063 18 CHD_PORT EQU 63H
0020 19 INTA00 EQU 20H ; 8259 PORT
0021 20 INTA01 EQU 21H ; 8259 PORT
0020 21 EOI EQU 20H
0040 22 TIMER EQU 40H
0043 23 TIM_CTL EQU 43H ; 8253 TIMER CONTROL PORT ADDR
0040 24 TIMER0 EQU 40H ; 8253 TIMER/CNTNR 0 PORT ADDR
0001 25 TINT EQU 01 ; TIMER 0 INTR RECVD MASK
0000 26 DMA00 EQU 08 ; DMA STATUS REG PORT ADDR
0000 27 DMA EQU 00 ; DMA CHANNEL 0 ADDR REG PORT ADDR
0540 28 MAX_PERIOD EQU 540H
0410 29 MIN_PERIOD EQU 410H
0060 30 KBD_IN EQU 60H ; KEYBOARD DATA IN ADDR PORT
0002 31 KBDINT EQU 02 ; KEYBOARD INTR MASK
0060 32 KB_DATA EQU 60H ; KEYBOARD SCAN CODE PORT
0061 33 KB_CTL EQU 61H ; CONTROL BITS FOR KB SENSE DATA
34              ;-----
35              ; 8088 INTERRUPT LOCATIONS :
36              ;-----
---- 37 ABS0 SEGMENT AT 0
0000 38 STG_LOCO LABEL BYTE
0008 39 ORG 2*4
0008 40 NMI_PTR LABEL WORD
0014 41 ORG 5*4
0014 42 INTS_PTR LABEL WORD
0020 43 ORG 8*4
0020 44 INT_ADDR LABEL WORD
0020 45 INT_PTR LABEL DWORD
0040 46 ORG 10H*4
0040 47 VIDEO_INT LABEL WORD
0074 48 ORG 10H*4
0074 49 PARM_PTR LABEL DWORD ; POINTER TO VIDEO PARMS
0060 50 ORG 18H*4
0060 51 BASIC_PTR LABEL WORD ; ENTRY POINT FOR CASSETTE BASIC
0078 52 ORG 01EH*4 ; INTERRUPT IEH
0078 53 DISK_POINTER LABEL DWORD
007C 54 ORG 01FH*4 ; LOCATION OF POINTER
007C 55 EXT_PTR LABEL DWORD ; POINTER TO EXTENSION
0100 56 ORG 040H*4 ; ROUTINE
0100 57 IO_ROM_INIT DW ? ;
0102 58 IO_ROM_SEG DW ? ; OPTIONAL ROM SEGMENT
0400 59 ORG 400H
0400 60 DATA_AREA LABEL BYTE ; ABSOLUTE LOCATION OF DATA SEGMENT
0400 61 DATA_WORD LABEL WORD
7C00 62 ORG 7C00H
7C00 63 BOOT_LOCN LABEL FAR
---- 64 ABS0 ENDS
65
66              ;-----
67              ; STACK -- USED DURING INITIALIZATION ONLY :
68              ;-----
---- 69 STACK SEGMENT AT 30H
0000 (128 70 DW 128 DUP(?)
????
)
0100 71 TOS LABEL WORD
---- 72 STACK ENDS
73
74              ;-----
75              ; ROM BIOS DATA AREAS :
76              ;-----
---- 77 DATA SEGMENT AT 40H

```

LOC OBJ	LINE	SOURCE				
0000 (4 ???? )	76	RS232_BASE	DW	4 DUP(?)		ADDRESSES OF RS232 ADAPTERS
0008 (4 ???? )	79	PRINTER_BASE	DW	4 DUP(?)		ADDRESSES OF PRINTERS
0010 ???? 0012 ?? 0013 ???? 0015 ???? 0016 ???? 0017 ??	80 81 82 83 84 85 86 87	EQUIP_FLAG MFG_TST MEMORY_SIZE IO_RAM_SIZE -----   KEYBOARD DATA AREAS   ----- KB_FLAG	DW DB DW DW -----   ----- DB	? ? ? ? -----   ----- ?		INSTALLED HARDWARE INITIALIZATION FLAG MEMORY SIZE IN K BYTES MEMORY IN I/O CHANNEL -----   ----- SHIFT FLAG EQUATES WITHIN KB_FLAG
0080 0040 0020 0010 0008 0004 0002 0001 0018 ??	91 92 93 94 95 96 97 98 99 100 101	INS_STATE CAPS_STATE NUM_STATE SCROLL_STATE ALT_SHIFT CTL_SHIFT LEFT_SHIFT RIGHT_SHIFT KB_FLAG_1	EQU EQU EQU EQU EQU EQU EQU EQU DB	80H 40H 20H 10H 08H 04H 02H 01H ?		INSERT STATE IS ACTIVE CAPS LOCK STATE HAS BEEN TOGGLED NUM LOCK STATE HAS BEEN TOGGLED SCROLL LOCK STATE HAS BEEN TOGGLED ALTERNATE SHIFT KEY DEPRESSED CONTROL SHIFT KEY DEPRESSED LEFT SHIFT KEY DEPRESSED RIGHT SHIFT KEY DEPRESSED SECOND BYTE OF KEYBOARD STATUS
0080 0040 0020 0010 0008 0019 ?? 001A ???? 001C ???? 001E (16 ???? )	102 103 104 105 106 107 108 109 110 111	INS_SHIFT CAPS_SHIFT NUM_SHIFT SCROLL_SHIFT HOLD_STATE ALT_INPUT BUFFER_HEAD BUFFER_TAIL KB_BUFFER	EQU EQU EQU EQU EQU DB DW DW DW	80H 40H 20H 10H 08H ? ? ? 16 DUP(?)		INSERT KEY IS DEPRESSED CAPS LOCK KEY IS DEPRESSED NUM LOCK KEY IS DEPRESSED SCROLL LOCK KEY IS DEPRESSED SUSPEND KEY HAS BEEN TOGGLED STORAGE FOR ALTERNATE KEYPAD ENTRY POINTER TO HEAD OF KEYBOARD BUFFER POINTER TO TAIL OF KEYBOARD BUFFER ROOM FOR 15 ENTRIES
003E	112 113 114 115	KB_BUFFER_END	LABEL	WORD		
0045 0046 0038 001D 003A 002A 0036 0052 0053	116 117 118 119 120 121 122 123 124	NUM_KEY SCROLL_KEY ALT_KEY CTL_KEY CAPS_KEY LEFT_KEY RIGHT_KEY INS_KEY DEL_KEY	EQU EQU EQU EQU EQU EQU EQU EQU EQU	69 70 56 29 58 42 54 82 83		SCAN CODE FOR NUMBER LOCK SCROLL LOCK KEY ALTERNATE SHIFT KEY SCAN CODE SCAN CODE FOR CONTROL KEY SCAN CODE FOR SHIFT LOCK SCAN CODE FOR LEFT SHIFT SCAN CODE FOR RIGHT SHIFT SCAN CODE FOR INSERT KEY SCAN CODE FOR DELETE KEY
003E ??	126 127 128 129 130 131 132 133 134 135 136 137 138	-----   DISKETTE DATA AREAS   ----- SEEK_STATUS ; ; INT_FLAG MOTOR_STATUS ; ; MOTOR_COUNT MOTOR_WAIT	DB ; ; EQU DB ; ; DB EQU	? ; ; 080H ? ; ; ? 37		-----   ----- DRIVE RECALIBRATION STATUS BIT 3-0 = DRIVE 3-0 NEEDS RECAL BEFORE NEXT SEEK IF BIT IS = 0 INTERRUPT OCCURRENCE FLAG MOTOR STATUS BIT 3-0 = DRIVE 3-0 IS CURRENTLY RUNNING BIT 7 = CURRENT OP IS A WRITE, REQUIRES DELAY TIME OUT COUNTER FOR DRIVE TURN OFF TWO SEC OF COUNT FOR MOTOR TURN OFF
0041 ?? 0080 0040 0020 0010 0009 0008 0004 0003 0002	139 140 141 142 143 144 145 146 147 148	DISKETTE_STATUS TIME_OUT BAD_SEEK BAD_NEC BAD_CRC DMA_BOUNDARY BAD_DMA RECORD_NOT_FND WRITE_PROTECT BAD_ADDR_MARK	DB EQU EQU EQU EQU EQU EQU EQU EQU EQU	? 80H 40H 20H 10H 09H 08H 04H 03H 02H		BYTE OF RETURN CODE INFO FOR STATUS ATTACHMENT FAILED TO RESPOND SEEK OPERATION FAILED NEC CONTROLLER HAS FAILED BAD CRC ON DISKETTE READ ATTEMPT TO DMA ACROSS 64K BOUNDARY DMA OVERRUN ON OPERATION REQUESTED SECTOR NOT FOUND WRITE ATTEMPTED ON WRITE PROT DISK ADDRESS MARK NOT FOUND

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LOC OBJ          LINE  SOURCE
0001             149  BAD_CMD      EQU    01H      ; BAD COMMAND PASSED TO DISKETTE I/O
                  150
0042 (7         151  NEC_STATUS   DB      7 DUP(?) ; STATUS BYTES FROM NEC
??
)
                  152
                  153  ;-----
                  154  ; VIDEO DISPLAY DATA AREA :
                  155  ;-----
0049 ??         156  CRT_MODE    DB      ?      ; CURRENT CRT MODE
004A ?????     157  CRT_COLS    DW      ?      ; NUMBER OF COLUMNS ON SCREEN
004C ?????     158  CRT_LEN     DW      ?      ; LENGTH OF REGEN IN BYTES
004E ?????     159  CRT_START   DW      ?      ; STARTING ADDRESS IN REGEN BUFFER
0050 (8         160  CURSOR_POSN DW      8 DUP(?) ; CURSOR FOR EACH OF UP TO 8 PAGES
????
)
0060 ?????     161  CURSOR_MODE DW      ?      ; CURRENT CURSOR MODE SETTING
0062 ??         162  ACTIVE_PAGE DB      ?      ; CURRENT PAGE BEING DISPLAYED
0063 ?????     163  ADDR_6845  DW      ?      ; BASE ADDRESS FOR ACTIVE DISPLAY CARD
0065 ??         164  CRT_MODE_SET DB      ?      ; CURRENT SETTING OF THE 3X8 REGISTER
0066 ??         165  CRT_PALETTE DB      ?      ; CURRENT PALETTE SETTING COLOR CARD
                  166
                  167  ;-----
                  168  ; CASSETTE DATA AREA :
                  169  ;-----
0067 ?????     170  EDGE_CNT    DW      ?      ; TIME COUNT AT DATA EDGE
0069 ?????     171  CRC_REG     DW      ?      ; CRC REGISTER
006B ??         172  LAST_VAL    DB      ?      ; LAST INPUT VALUE
                  173
                  174  ;-----
                  175  ; TIMER DATA AREA :
                  176  ;-----
006C ?????     177  TIMER_LOW   DW      ?      ; LOW WORD OF TIMER COUNT
006E ?????     178  TIMER_HIGH  DW      ?      ; HIGH WORD OF TIMER COUNT
0070 ??         179  TIMER_OFL   DB      ?      ; TIMER HAS ROLLED OVER SINCE LAST READ
                  180  ;COUNTS_SEC EQU    18
                  181  ;COUNTS_MIN EQU   1092
                  182  ;COUNTS_HOUR EQU  65543
                  183  ;COUNTS_DAY EQU  1573040 = 1800B0H
                  184
                  185  ;-----
                  186  ; SYSTEM DATA AREA :
                  187  ;-----
0071 ??         188  BIOS_BREAK  DB      ?      ; BIT 7 = 1 IF BREAK KEY WAS DEPRESSED
0072 ?????     189  RESET_FLAG  DW      ?      ; WORD = 1234H IF KB RESET UNDERWAY
                  190
                  191  ; FIXED DISK DATA AREA :
                  192  ;-----
0074 ?????     193  DW      ?      ;
0076 ?????     194  DW      ?      ;
                  195
                  196  ; PRINTER AND RS232 TIMEOUT CTPS :
                  197  ;-----
0078 (4         198  PRINT_TIM_OUT DB      4 DUP(?) ; PRINTER TIME OUT COUNTER
??
)
007C (4         199  RS232_TIM_OUT DB      4 DUP(?) ; RS232 TIME OUT COUNTER
??
)
                200  ;-----
                201  ; EXTRA KEYBOARD DATA AREA :
                202  ;-----
0080 ?????     203  BUFFER_START DW      ?      ;
0082 ?????     204  BUFFER_END  DW      ?      ;
-----         205  DATA ENDS
                206  ;-----
                207  ; EXTRA DATA AREA :
                208  ;-----
-----         209  ;XQDATA SEGMENT AT 50H
0000 ??         210  STATUS_BYTE DB      ?      ;
-----         211  ;XQDATA ENDS
                212
                213  ;-----
                214  ; VIDEO DISPLAY BUFFER :
                215  ;-----
-----         216  VIDEO_RAM SEGMENT AT 0B800H

```

LOC OBJ	LINE	SOURCE
0000	217	REGEN LABEL BYTE
0000	218	REGENM LABEL WORD
0000 (16384	219	DB 16384 DUP(?)
??		
)		
----	220	VIDEO_RAM ENDS
	221	;
	222	ROM RESIDENT CODE :
	223	;
----	224	CODE SEGMENT AT 0F000H
0000 (57344	225	DB 57344 DUP(?) ; FILL LOWEST 56K
??		
)		
	226	
E000 31353031343736	227	DB '1501476 COPR. IBM 1951' ; COPYRIGHT NOTICE
20434F50522E20		
49424D20313938		
32		
	228	
	229	;
	230	INITIAL RELIABILITY TESTS -- PHASE 1 :
	231	;
	232	ASSUME CS:CODE,SS:CODE,ES:ABS0,DS:DATA
	233	;
	234	DATA DEFINITIONS :
	235	;
E016 D1E0	236	CI DW C11 ; RETURN ADDRESS
	237	
	238	;
	239	THIS SUBROUTINE PERFORMS A READ/WRITE STORAGE TEST ON :
	240	A 16K BLOCK OF STORAGE. :
	241	ENTRY REQUIREMENTS: :
	242	ES = ADDRESS OF STORAGE SEGMENT BEING TESTED :
	243	DS = ADDRESS OF STORAGE SEGMENT BEING TESTED :
	244	WHEN ENTERING AT STGTST_CNT, CX MUST BE LOADED WITH :
	245	THE BYTE COUNT. :
	246	EXIT PARAMETERS: :
	247	ZERO FLAG = 0 IF STORAGE ERROR (DATA COMPARE OR PARITY CHECK. :
	248	AL = 0 DENOTES A PARITY CHECK. ELSE AL=XOR'ED BIT. :
	249	PATTERN OF THE EXPECTED DATA PATTERN VS THE :
	250	ACTUAL DATA READ. :
	251	AX,BX,CX,DX,DI, AND SI ARE ALL DESTROYED. :
	252	;
	253	;
E018	254	STGTST PROC NEAR
E018 B90040	255	MOV CX,4000H ; SETUP CNT TO TEST A 16K BLK
E01B	256	STGTST_CNT:
E01B FC	257	CLD ; SET DIR FLAG TO INCREMENT
E01C 8B09	258	MOV BX,CX ; SAVE BYTE CNT (4K FOR VIDEO OR 16K)
E01E B8AAAA	259	MOV AX,0AAAAH ; GET DATA PATTERN TO WRITE
E021 BA55FF	260	MOV DX,0FF55H ; SETUP OTHER DATA PATTERNS TO USE
E024 2BFF	261	SUB DI,DI ; DI = OFFSET 0 RELATIVE TO ES REG
E026 F3	262	REP STOSB ; WRITE STORAGE LOCATIONS
E027 AA		
E028	263	C3: ; STG01
E028 4F	264	DEC DI ; POINT TO LAST BYTE JUST WRITTEN
E029 FD	265	STD ; SET DIR FLAG TO GO BACKWARDS
E02A	266	C4:
E02A BBF7	267	MOV SI,DI
E02C 8BCB	268	MOV CX,BX ; SETUP BYTE CNT
E02E	269	C5: ; INNER TEST LOOP
E02E AC	270	LODSB ; READ OLD TST BYTE FROM STORAGE [SI]+
E02F 32C4	271	XOR AL,AH ; DATA READ AS EXPECTED ?
E031 7525	272	JNE C7 ; NO - GO TO ERROR ROUTINE
E033 8AC2	273	MOV AL,DL ; GET NEXT DATA PATTERN TO WRITE
E035 AA	274	STOSB ; WRITE INTO LOCATION JUST READ [DI]+
E036 E2F6	275	LOOP C5 ; DECREMENT BYTE COUNT AND LOOP CX
	276	
E038 22E4	277	AND AH,AH ; ENDING ZERO PATTERN WRITTEN TO STG ?
E03A 7416	278	JZ C6X ; YES - RETURN TO CALLER WITH AL=0
E03C 8AE0	279	MOV AH,AL ; SETUP NEW VALUE FOR COMPARE
E03E 86F2	280	XCHG DH,DL ; MOVE NEXT DATA PATTERN TO DL
E040 22E4	281	AND AH,AH ; READING ZERO PATTERN THIS PASS ?
E042 7504	282	JNZ C6 ; CONTINUE TEST SEQUENCE TILL ZERO DATA
E044 8AD4	283	MOV DL,AH ; ELSE SET ZERO FOR END READ PATTERN
E046 EBEO	284	JMP C3 ; AND MAKE FINAL BACKWARDS PASS
E048	285	C6:

LOC 08J	LINE	SOURCE	
E048 FC	286	CLD	; SET DIR FLAG TO GO FORWARD
E049 47	287	INC DI	; SET POINTER TO BEG LOCATION
E04A 74DE	288	JZ C4	; READ/WRITE FORWARD IN STG
E04C 4F	289	DEC DI	; ADJUST POINTER
E04D BA0100	290	MOV DX,00001H	; SETUP 01 FOR PARITY BIT
	291		; AND 00 FOR END
E050 EBD6	292	JMP C3	; READ/WRITE BACKWARD IN STG
E052	293	C6X:	
E052 E462	294	IN AL,PORT_C	; DID A PARITY ERROR OCCUR ?
E054 24C0	295	AND AL,0C0H	; ZERO FLAG WILL BE OFF PARITY ERROR
E056 B000	296	MOV AL,000H	; AL=0 DATA COMPARE OK
E058	297	C7:	
E058 FC	298	CLD	; SET DEFAULT DIRCTN FLAG BACK TO INC
E059 C3	299	RET	
	300	STGTST ENDP	
	301	;	-----
	302	; 8088 PROCESSOR TEST	;
	303	; DESCRIPTION	;
	304	; VERIFY 8088 FLAGS, REGISTERS AND CONDITIONAL JUMPS	;
	305	;	-----
	306	ASSUME CS:CODE,DS:NOTHING,ES:NOTHING,SS:NOTHING	
E05B	307	ORG 0E05BH	
E05B	308	RESET LABEL FAR	
E05B	309	START:	
E05B FA	310	CLI	; DISABLE INTERRUPTS
E05C B405	311	MOV AH,0D5H	; SET SF, CF, ZF, AND AF FLAGS ON
E05E 9E	312	SAHF	
E05F 734C	313	JNC ERR01	; GO TO ERR ROUTINE IF CF NOT SET
E061 754A	314	JNZ ERR01	; GO TO ERR ROUTINE IF ZF NOT SET
E063 7B48	315	JNP ERR01	; GO TO ERR ROUTINE IF PF NOT SET
E065 7946	316	JNS ERR01	; GO TO ERR ROUTINE IF SF NOT SET
E067 9F	317	LAHF	; LOAD FLAG IMAGE TO AH
E068 B105	318	MOV CL,5	; LOAD CNT REG WITH SHIFT CNT
E06A D2EC	319	SHR AH,CL	; SHIFT AF INTO CARRY BIT POS
E06C 733F	320	JNC ERR01	; GO TO ERR ROUTINE IF AF NOT SET
E06E B040	321	MOV AL,40H	; SET THE OF FLAG ON
E070 00E0	322	SHL AL,1	; SETUP FOR TESTING
E072 7139	323	JNO ERR01	; GO TO ERR ROUTINE IF OF NOT SET
E074 32E4	324	XOR AH,AH	; SET AH = 0
E076 9E	325	SAHF	; CLEAR SF, CF, ZF, AND PF
E077 7634	326	JBE ERR01	; GO TO ERR ROUTINE IF CF ON
	327		; OR TO TO ERR ROUTINE IF ZF ON
E079 7832	328	JS ERR01	; GO TO ERR ROUTINE IF SF ON
E07B 7A30	329	JP ERR01	; GO TO ERR ROUTINE IF PF ON
E07D 9F	330	LAHF	; LOAD FLAG IMAGE TO AH
E07E B105	331	MOV CL,5	; LOAD CNT REG WITH SHIFT CNT
E080 D2EC	332	SHR AH,CL	; SHIFT 'AF' INTO CARRY BIT POS
E082 7229	333	JC ERR01	; GO TO ERR ROUTINE IF ON
E084 00E4	334	SHL AH,1	; CHECK THAT 'OF' IS CLEAR
E086 7025	335	JD ERR01	; GO TO ERR ROUTINE IF ON
	336		
	337	;	----- READ/WRITE THE 8088 GENERAL AND SEGMENTATION REGISTERS
	338	;	WITH ALL ONE'S AND ZEROES'S.
	339		
E088 B8FFFF	340	MOV AX,0FFFFH	; SETUP ONE'S PATTERN IN AX
E08B F9	341	STC	
E08C	342	C8:	
E08C 8ED8	343	MOV DS,AX	; WRITE PATTERN TO ALL REGS
E08E 8CDB	344	MOV BX,DS	
E090 8EC3	345	MOV ES,BX	
E092 8CC1	346	MOV CX,ES	
E094 8ED1	347	MOV SS,CX	
E096 8CD2	348	MOV DX,SS	
E098 8BE2	349	MOV SP,DX	
E09A 8BEC	350	MOV BP,SP	
E09C 8BF5	351	MOV SI,BP	
E09E 8BFE	352	MOV DI,SI	
E0A0 7307	353	JNC C9	; TSTIA
E0A2 33C7	354	XOR AX,DI	; PATTERN MAKE IT THRU ALL REGS
E0A4 7507	355	JNZ ERR01	; NO - GO TO ERR ROUTINE
E0A6 F8	356	CLC	
E0A7 EBE3	357	JMP C8	
E0A9	358	C9:	; TSTIA
E0A9 08C7	359	OR AX,DI	; ZERO PATTERN MAKE IT THRU?
E0AB 7401	360	JZ C10	; YES - GO TO NEXT TEST
E0AD F4	361	ERR01: HLT	; HALT SYSTEM
	362	;	-----

```

LOC OBJ          LINE   SOURCE
363             ;       ROS CHECKSUM TEST I           ;
364             ;       DESCRIPTION                 ;
365             ;       A CHECKSUM IS DONE FOR THE 8K ROS MODULE ;
366             ;       CONTAINING POD AND BIOS.     ;
367             ;-----
EOAE            368     C10:
369             ;       ZERO IN AL ALREADY
EOAE E6A0       370             OUT    0A0H,AL           ; DISABLE NMI INTERRUPTS
EOB0 E6B3       371             OUT    83H,AL           ; INITIALIZE DMA PAGE REG
EOB2 BAD803     372             MOV    DX,3D8H
EOB5 EE         373             OUT    DX,AL           ; DISABLE COLOR VIDEO
EOB6 FEC0       374             INC    AL
EOB8 B2B8       375             MOV    DL,0B8H
EOBA EE         376             OUT    DX,AL           ; DISABLE B/W VIDEO,EN HIGH RES
EOBB B099       377             MOV    AL,99H           ; SET 8255 A,C-INPUT,B-OUTPUT
EOBD E663       378             OUT    CMD_PORT,AL        ; WRITE 8255 CMD/MODE REG
EOBF B0FC       379             MOV    AL,0FCH          ; DISABLE PARITY CHECKERS AND
EOC1 E661       380             OUT    PORT_B,AL        ; GATE SNS SMS,CASS MOTOR OFF
EOC3 8CC8       381             MOV    AX,CS           ; SETUP SS SEG REG
EOC5 8ED0       382             MOV    SS,AX
EOC7 0ED8       383             MOV    DS,AX           ; SET UP DATA SEG TO POINT TO
384             ;       ROM ADDRESS
385             ASSUME SS:CODE
EOC9 B7E0       386             MOV    BH,0E0H          ; SETUP STARTING ROS ADDR (E0000)
EOCB BC16E0     387             MOV    SP,OFFSET C1    ; SETUP RETURN ADDRESS
EOCE E97B0B     388             JMP    ROS_CHECKSUM
EOD1            389
EOD1 75DA       390     C11:
391             ;       JNE    ERROR1           ; HALT SYSTEM IF ERROR
392             ;-----
393             ;       8237 DMA INITIALIZATION CHANNEL REGISTER TEST ;
394             ;       DESCRIPTION                 ;
395             ;       DISABLE THE 8237 DMA CONTROLLER.  VERIFY THAT TIMER 1 ;
396             ;       FUNCTIONS OK.  WRITE/READ THE CURRENT ADDRESS AND WORD ;
397             ;       COUNT REGISTERS FOR ALL CHANNELS.  INITIALIZE AND ;
398             ;       START DMA FOR MEMORY REFRESH.  ;
399             ;-----
EOD3 B004       399             MOV    AL,04           ; DISABLE DMA CONTROLLER
EOD5 E608       400             OUT    DMA08,AL
401
402             ;----- VERIFY THAT TIMER 1 FUNCTIONS OK
403
EOD7 B054       404             MOV    AL,54H           ; SEL TIMER 1,LSB,MODE 2
EOD9 E643       405             OUT    TIMER+3,AL
EODB 8AC1       406             MOV    AL,CL           ; SET INITIAL TIMER CNT TO 0
EODD E641       407             OUT    TIMER+1,AL
EODF            408     C12:
EODF B040       409             MOV    AL,40H           ; TIMER1_BITS_ON
EOE1 E643       410             OUT    TIMER+3,AL        ; LATCH TIMER 1 COUNT
EOE3 80FBFF     411             CHP    BL,OFFH          ; YES - SEE IF ALL BITS GO OFF
EOE6 7407       412             JE     C13             ; TIMER1_BITS_OFF
EOE8 E441       413             IN     AL,TIMER+1        ; READ TIMER 1 COUNT
EOEA 0AD8       414             OR     BL,AL           ; ALL BITS ON IN TIMER
EOEC E2F1       415             LOOP  C12             ; TIMER1_BITS_ON
EOEE F4         416             HLT                    ; TIMER 1 FAILURE, HALT SYS
EOEF            417     C13:
EOEF 8AC3       418             MOV    AL,BL           ; TIMER1_BITS_OFF
EOF1 2BC9       419             SUB    CX,CX           ; SET TIMER 1 CNT
EOF3 E641       420             OUT    TIMER+1,AL
EOF5            421     C14:
EOF5 B040       422             MOV    AL,40H           ; TIMER_LOOP
EOF7 E643       423             OUT    TIMER+3,AL        ; LATCH TIMER 1 COUNT
EOF9 90         424             NOP                    ; DELAY FOR TIMER
EOFA 90         425             NOP
EOFB E441       426             IN     AL,TIMER+1        ; READ TIMER 1 COUNT
Eofd 22D8       427             AND    BL,AL
EOFF 7403       428             JZ     C15             ; GO TO WRAP_DMA_REG
E101 E2F2       429             LOOP  C14             ; TIMER_LOOP
E103 F4         430             HLT                    ; TIMER ERROR - HALT SYSTEM
431
432             ;----- INITIALIZE TIMER 1 TO REFRESH MEMORY
433
E104            434     C15:
E104 B012       435             MOV    AL,18           ; WRAP_DMA_REG
E106 E641       436             OUT    TIMER+1,AL        ; SETUP DIVISOR FOR REFRESH
E108 E60D       437             OUT    DMA+0DH,AL       ; WRITE TIMER 1 CNT REG
438             ; SEND MASTER CLEAR TO DMA

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LOC OBJ          LINE    SOURCE
439              ;----- WRAP DMA CHANNELS ADDRESS AND COUNT REGISTERS
440
E10A B0FF       441          MOV     AL,0FFH          ; WRITE PATTERN FF TO ALL REGS
E10C            442          C16:
E10C 8AD8       443          MOV     BL,AL          ; SAVE PATTERN FOR COMPARE
E10E 8AF8       444          MOV     BH,AL
E110 B90800     445          MOV     CX,8          ; SETUP LOOP CNT
E113 2BD2       446          SUB     DX,DX          ; SETUP I/O PORT ADDR OF REG (0000)
E115            447          C17:
E115 EE         448          OUT     DX,AL          ; WRITE PATTERN TO REG, LSB
E116 50         449          PUSH   AX
E117 EE         450          OUT     DX,AL          ; MSB OF 16 BIT REG
E118 B80101     451          MOV     AX,0101H       ; AX TO ANOTHER PAT BEFORE RD
E11B EC         452          IN     AL,DX          ; READ 16-BIT DMA CH REG, LSB
E11C 8AE0       453          MOV     AH,AL          ; SAVE LSB OF 16-BIT REG
E11E EC         454          IN     AL,DX          ; READ MSB OF DMA CH REG
E11F 3B08       455          CMP     BX,AX          ; PATTERN READ AS WRITTEN?
E121 7401       456          JE     C18             ; YES - CHECK NEXT REG
E123 F4         457          HLT
E124            458          C18:
E124 42         459          INC     DX             ; SET I/O PORT TO NEXT CH REG
E125 E2EE       460          LOOP  C17             ; WRITE PATTERN TO NEXT REG
E127 FEC0       461          INC     AL             ; SET PATTERN TO 0
E129 74E1       462          JZ     C16             ; WRITE TO CHANNEL REGS
463
464              ;----- INITIALIZE AND START DMA FOR MEMORY REFRESH.
465
E12B 8ED8       466          MOV     DS,BX          ; SET UP ABS0 INTO DS AND ES
E12D 8EC3       467          MOV     ES,BX
468          ASSUME DS:ABS0,ES:ABS0
469
E12F B0FF       470          MOV     AL,0FFH       ; SET CNT OF 64K FOR RAM REFRESH
E131 E601       471          OUT    DMA+1,AL
E133 50         472          PUSH   AX
E134 E601       473          OUT    DMA+1,AL
E136 B208       474          MOV     DL,0BH        ; DX=000B
E138 B058       475          MOV     AL,05BH       ; SET DMA MODE,CH 0,READ,AUTOINT
E13A EE         476          OUT    DX,AL          ; WRITE DMA MODE REG
E13B B000       477          MOV     AL,0          ; ENABLE DMA CONTROLLER
E13D E608       478          OUT    DMA+8,AL       ; SETUP DMA COMMAND REG
E13F 50         479          PUSH   AX
E140 E60A       480          OUT    DMA+10,AL      ; ENABLE CHANNEL 0 FOR REFRESH
E142 B103       481          MOV     CL,3
E144 B041       482          MOV     AL,41H        ; SET MODE FOR CHANNEL 1
E146            483          C18A:
E146 EE         484          OUT    DX,AL
E147 FEC0       485          INC     AL             ; POINT TO NEXT CHANNEL
E149 E2FB       486          LOOP  C18A
487
488          ;-----
488          ;     BASE 16K READ/WRITE STORAGE TEST
488          ;
489          ; DESCRIPTION
489          ;
490          ; WRITE/READ/VERIFY DATA PATTERNS FF,55,AA,01, AND 00
490          ;
491          ; TO 1ST 16K OF STORAGE. VERIFY STORAGE ADDRESSABILITY.
491          ;
492          ; INITIALIZE THE 8259 INTERRUPT CONTROLLER CHIP FOR
492          ;
493          ; CHECKING MANUFACTURING TEST 2 MODE.
493          ;
494          ;-----
495
496              ;----- DETERMINE MEMORY SIZE AND FILL MEMORY WITH DATA
497
E14B 8A1302     498          MOV     DX,0213H       ; ENABLE EXPANSION BOX
E14E B001       499          MOV     AL,01H
E150 EE         500          OUT    DX,AL
E151 8B2E7204   501          MOV     BP,DATA_WORD[OFFSET RESET_FLAG] ; SAVE 'RESET_FLAG' IN BP
E155 81FD3412   502          CMP     BP,1234H       ; WARM START?
E159 740A       503          JE     C18B           ; BYPASS STG TST.
E15B BC41F090   504          MOV     SP,OFFSET C2
E15F E9B6FE     505          JMP     STGTST
E162            506          C24:
E162 7401       507          JE     C18B           ; PROCEED IF STGTST OK
E164 F4         508          HLT
E165            509          C18B:
E165 2BFF       510          SUB     DI,DI
E167 E460       511          IN     AL,PORT_A      ; DETERMINE BASE RAM SIZE
E169 240C       512          AND    AL,0CH         ; ISOLATE RAM SIZE SHS
E16B 0404       513          ADD    AL,4           ; CALCULATE MEMORY SIZE
E16D B10C       514          MOV     CL,12

```

LOC OBJ	LINE	SOURCE	
E16F D3E0	515	SHL	AX, CL
E171 8BC6	516	MOV	CX, AX
E173 FC	517	CLD	
E174	518		; SET DIR FLAG TO INCR
E174 AA	519	STOSB	
E175 E2FD	520	LOOP	C19 ; FILL BASE RAM WITH DATA
E177 892E7204	521	MOV	DATA_WORD[OFFSET RESET_FLAG],BP ; LOOP TIL ALL ZERO
	522		
	523		);----- DETERMINE IO CHANNEL RAM SIZE
	524		
E17B B0F8	525	MOV	AL,0F8H ; ENABLE SWITCH 5
E17D E661	526	OUT	PORT_B,AL
E17F E462	527	IN	AL,PORT_C ; READ SWITCHES
E181 2401	528	AND	AL,0000001B ; ISOLATE SWITCH 5
E183 B10C	529	MOV	CL,12D
E185 D3C0	530	ROL	AX,CL
E187 B0FC	531	MOV	AL,0FCH ; DISABLE SW. 5
E189 E661	532	OUT	PORT_B,AL
E18B E462	533	IN	AL,PORT_C
E18D 240F	534	AND	AL,0FH
E18F 0AC4	535	OR	AL,AH ; COMBINE SWITCH VALUES
E191 8AD8	536	MOV	BL,AL ; SAVE
E193 B420	537	MOV	AH,32
E195 F6E4	538	MUL	AH ; CALC. LENGTH
E197 A31504	539	MOV	DATA_WORD[OFFSET IO_RAM_SIZE],AX ;SAVE IT
E19A 7418	540	JZ	C21
E19C BA0010	541	MOV	DX,1000H ; SEGMENT FOR I/O RAM
E19F 8AE0	542	MOV	AH,AL
E1A1 B000	543	MOV	AL,0
E1A3	544		C20: ; FILL_IO:
E1A3 8EC2	545	MOV	ES,DX
E1A5 B90080	546	MOV	CX,8000H ; FILL 32K BYTES
E1A8 2BFF	547	SUB	DI,DI
E1AA F3	548	REP	STOSB
E1AB AA			
E1AC 81C20008	549	ADD	DX,800H ; NEXT SEGMENT VALUE
E1B0 FECB	550	DEC	BL
E1B2 75EF	551	JNZ	C20 ; FILL_IO
	552		);-----
	553		; INITIALIZE THE 8259 INTERRUPT CONTROLLER CHIP ;
	554		);-----
E1B4	555		C21:
E1B4 8013	556	MOV	AL,13H ; ICW1 - EDGE, SNGL, ICW4
E1B6 E620	557	OUT	INTA00,AL
E1B8 B008	558	MOV	AL,8 ; SETUP ICW2 - INT TYPE 8 (8-F)
E1BA E621	559	OUT	INTA01,AL
E1BC B009	560	MOV	AL,9 ; SETUP ICW4 - BUFFRD,8086 MODE
E1BE E621	561	OUT	INTA01,AL
E1C0 28C0	562	SUB	AX,AX ; POINT ES TO BEGIN
E1C2 8EC0	563	MOV	ES,AX ; OF R/W STORAGE
	564		);-----
	565		; CHECK FOR MANUFACTURING TEST 2 TO LOAD TEST PROGRAMS FROM KEYBOARD. ;
	566		);-----
	567		
	568		);----- SETUP STACK SEG AND SP
	569		
E1C4 B83000	570	MOV	AX,STACK ; GET STACK VALUE
E1C7 8ED0	571	MOV	SS,AX ; SET THE STACK UP
E1C9 BC0001	572	MOV	SP,OFFSET TOS ; STACK IS READY TO GO
E1CC 81FD3412	573	CHP	BP,1234H ; RESET_FLAG SET?
E1D0 7425	574	JE	C25 ; YES - SKIP MFG TEST
E1D2 2BFF	575	SUB	DI,DI
E1D4 8EDF	576	MOV	DS, DI
E1D6 BB2400	577	MOV	BX, 24H
E1D9 C70747FF	578	MOV	WORD PTR [BX],OFFSET D11 ; SET UP KB INTERRUPT
E1DD 43	579	INC	BX
E1DE 43	580	INC	BX
E1DF 8C0F	581	MOV	[BX],CS
E1E1 E85F04	582	CALL	KBD_RESET ; READ IN KB RESET CODE TO BL
E1E4 80FB65	583	CMP	BL,065H ; IS THIS MANUFACTURING TEST 2?
E1E7 750E	584	JNZ	C25 ; JUMP IF NOT MAN. TEST
E1E9 B2FF	585	MOV	DL,255 ; READ IN TEST PROGRAM
E1EB	586		C22:
E1EB E86204	587	CALL	SP_TEST
E1EE 8AC3	588	MOV	AL,BL
E1F0 AA	589	STOSB	

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LOC OBJ          LINE  SOURCE
EIF1 FECA        590          DEC    DL
EIF3 75F6        591          JNZ   C22          ; JUMP IF NOT DONE YET
EIF5 CD3E        592          INT   3EH         ; SET INTERRUPT TYPE 62 ADDRESS F8H
EIF7             593      C25:
594
595      ;---- SET UP THE BIOS INTERRUPT VECTORS TO TEMP INTERRUPT
596
EIF7 B92000      597          MOV   CX,32        ; FILL ALL 32 INTERRUPTS
EIFA 2BFF        598          SUB   DI,DI        ; FIRST INTERRUPT LOCAT01N
EIFC             599      D3:
EIFC B847FF      600          MOV   AX,OFFSET D11 ; MOVE ADDR OF INTR PROC TO TBL
EIFE AB          601          STOSH          ;
E200 8CC8        602          MOV   AX,CS        ; GET ADDR OF INTR PROC SEG
E202 AB          603          STOSH          ;
E203 E2F7        604          LOOP  D3          ; VECTBL0
605
606      ;---- SET UP OTHER INTERRUPTS AS NECESSARY
607
E205 C7060800C3E2 608          MOV   NMI_PTR,OFFSET NMI_INT ; NMI INTERRUPT
E20B C706140054FF 609          MOV   INT5_PTR,OFFSET PRINT_SCREEN ; PRINT SCREEN
E211 C706620000F6 610          MOV   BASIC_PTR+2,0F600H    ; SEGMENT FOR CASSETTE BASIC
611
612      ;-----
613      ;      8259 INTERRUPT CONTROLLER TEST
614      ; DESCRIPTION
615      ; READ/WRITE THE INTERRUPT MASK REGISTER (IMR) WITH ALL
616      ; ONES AND ZEROES. ENABLE SYSTEM INTERRUPTS. MASK DEVICE
617      ; INTERRUPTS OFF. CHECK FOR HOT INTERRUPTS (UNEXPECTED).
618      ;-----
619
620      ;---- TEST THE IMR REGISTER
621
E217 BA2100      622          MOV   DX,0021H    ; POINT INTR. CHIP ADDR 21
E21A B000        623          MOV   AL,0          ; SET IMR TO ZERO
E21C EE          624          OUT  DX,AL
E21D EC          625          IN   AL,DX      ; READ IMR
E21E 0AC0        626          OR   AL,AL         ; IMR = 0?
E220 7515        627          JNZ  D6            ; GO TO ERR ROUTINE IF NOT 0
E222 B0FF        628          MOV   AL,OFFFH     ; DISABLE DEVICE INTERRUPTS
E224 EE          629          OUT  DX,AL       ; WRITE TO IMR
E225 EC          630          IN   AL,DX      ; READ IMR
E226 0401        631          ADD  AL,1          ; ALL IMR BIT ON?
E228 7500        632          JNZ  D6            ; NO - GO TO ERR ROUTINE
633
634      ;---- CHECK FOR HOT INTERRUPTS
635
636      ;---- INTERRUPTS ARE MASKED OFF. CHECK THAT NO INTERRUPTS OCCUR.
637
E22A 32E4        638          XOR   AH,AH        ; CLEAR AH REG
E22C FB          639          STI          ; ENABLE EXTERNAL INTERRUPTS
E22D 2BC9        640          SUB   CX,CX        ; WAIT 1 SEC FOR ANY INTRs THAT
E22F             641      D4:
E22F E2FE        642          LOOP D4          ; MIGHT OCCUR
E231             643      D5:
E231 E2FE        644          LOOP D5
E233 0AE4        645          OR   AH,AH        ; DID ANY INTERRUPTS OCCUR?
E235 7408        646          JZ   D7            ; NO - GO TO NEXT TEST
E237             647      D6:
E237 BA0101      648          MOV   DX,101H     ; BEEP SPEAKER IF ERROR
E23A E09203      649          CALL ERR_BEEP    ; GO TO BEEP SUBROUTINE
E23D FA          650          CLI
E23E F4          651          HLT          ; HALT THE SYSTEM
652      ;-----
653      ;      8253 TIMER CHECKOUT
654      ; DESCRIPTION
655      ; VERIFY THAT THE SYSTEM TIMER (D)
656      ; DOESN'T COUNT TOO FAST OR TOO SLOW.
657      ;-----
658      D7:
E23F             659          MOV   AL,0FEH    ; MASK ALL INTRs EXCEPT LVL 0
E241 EE          660          OUT  DX,AL     ; WRITE THE 8259 IMR
E242 B010        661          MOV   AL,00010000B ; SEL TIM 0, LSB, MODE 0, BINARY
E244 E643        662          OUT  TIM_CTL,AL  ; WRITE TIMER CONTROL MODE REG
E246 B91600      663          MOV   CX,16H      ; SET PGM LOOP CNT
E249 8AC1        664          MOV   AL,CL       ; SET TIMER 0 CNT REG
E24B E640        665          OUT  TIMER0,AL   ; WRITE TIMER 0 CNT REG

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LOC OBJ	LINE	SOURCE	
E24D	666	DB:	
E24D F6C4FF	667	TEST AH,OFFH	; DID TIMER 0 INTERRUPT OCCUR?
E258 7504	668	JNZ D9	; YES - CHECK TIMER OP FOR SLOW TIME
E252 E2F9	669	LOOP D8	; WAIT FOR INTR FOR SPECIFIED TIME
E254 EBE1	670	JHP D6	; TIMER 0 INTR DIDN'T OCCUR - ERR
E256	671	D9:	
E256 B112	672	MOV CL,18	; SET PGM LOOP CNT
E258 B0FF	673	MOV AL,OFFH	; WRITE TIMER 0 CNT REG
E25A E640	674	OUT TIMER0,AL	
E25C B8FE00	675	MOV AX,OFEH	
E25F EE	676	OUT DX,AL	
E260	677		
E260 F6C4FF	678	D10:	
E263 75D2	679	TEST AH,OFFH	; DID TIMER 0 INTERRUPT OCCUR?
E265 E2F9	680	JNZ D6	; YES - TIMER CNTING TOO FAST, ERR
	681	LOOP D10	; WAIT FOR INTR FOR SPECIFIED TIME
	682		
	683		
	684		
E267 1E	684	PUSH DS	; SAVE POINTER TO DATA AREA
E268 BF4000	685	MOV DI,OFFSET VIDEO_INT	; SETUP ADDR TO INTR AREA
E268 0E	686	PUSH CS	
E26C 1F	687	POP DS	; SETUP ADDR OF VECTOR TABLE
E26D BE03FF90	688	MOV SI,OFFSET VECTOR_TABLE+16	; START WITH VIDEO ENTRY
E271 B91000	689	MOV CX,16	
	690		
	691		
	692		
	693		
E274 B0FF	693	MOV AL,OFFH	; DISABLE ALL DEVICE INTERRUPTS
E276 EE	694	OUT DX,AL	
E277 B036	695	MOV AL,36H	; SEL TIM 0,LSB,MSB,MODE 3
E279 E643	696	OUT TIMER+3,AL	; WRITE TIMER MODE REG
E27B B000	697	MOV AL,0	
E27D E640	698	OUT TIMER,AL	; WRITE LSB TO TIMER 0 REG
E27F	699	E1A:	
E27F A5	700	MOVSW	; MOVE VECTOR TABLE TO RAM
E280 47	701	INC DI	; MOVE PAST SEGMENT POINTER
E281 47	702	INC DI	
E282 E2FB	703	LOOP E1A	
E284 E640	704	OUT TIMER,AL	; WRITE MSB TO TIMER 0 REG
E286 1F	705	POP DS	; RECOVER DATA SEG POINTER
	706		
	707		
	708		
	709		
E287 E8B903	709	CALL KBD_RESET	; SEND SOFTWARE RESET TO KEYBRD
E28A 80FBAA	710	CMF BL,0AAH	; SCAN CODE 'AA' RETURNED?
E28D 741E	711	JE E6	; YES - CONTINUE (NON MFG MODE)
E28F B03C	712	MOV AL,3CH	; EN KBD, SET KBD CLK LINE LOW
E291 E661	713	OUT PORT_B,AL	; WRITE 8255 PORT B
E293 90	714	NOP	
E294 90	715	NOP	
E295 E460	716	IN AL,PORT_A	; WAS A BIT CLOCKED IN?
E297 24FF	717	AND AL,OFFH	
E299 750E	718	JNZ E2	; YES - CONTINUE (NON MFG MODE)
E29B FE0612D4	719	INC DATA_AREA[OFFSET MFG_TST]	; ELSE SET SW FOR MFG TEST MODE
E29F C70620006DE6	720	MOV INT_ADDR,OFFSET BLINK_INT	; SETUP TIMER INTR TO BLINK LED
E2A5 B0FE	721	MOV AL,OFEH	; ENABLE TIMER INTERRUPT
E2A7 E621	722	OUT INTA01,AL	
E2A9	723	E2:	; JUMPER_NOT_IN:
E2A9 B0CC	724	MOV AL,0CCH	; RESET THE KEYBOARD
E2AB E661	725	OUT PORT_B,AL	
	726		
	727		
	728		
	729		
	730		
	731		
	732		
	733		
	734		
	735		
	736		
E2AD	736	E6:	
E2AD E460	737	IN AL,PORT_A	; READ SENSE SWITCHES
E2AF B400	738	MOV AH,0	
E2B1 A31004	739	MOV DATA_WORD[OFFSET EQUIP_FLAG],AX	; STORE SENSE SW INFO
E2B4	740	E6A:	
E2B4 2430	741	AND AL,30H	; ISOLATE VIDEO SMS
E2B6 7529	742	JNZ E7	; VIDEO SMS SET TO 0?

LOC OBJ	LINE	SOURCE	
E2B8	C706400053FF	743	MOV VIDEO_INT,OFFSET DUMMY_RETURN
E2BE	E9A200	744	JMP E18_1 ; SKIP VIDEO TESTS FOR BURN-IN
		745	
E2C3		746	ORG 0E2C3H
E2C3		747	NMI_INT PROC NEAR
E2C3	50	748	PUSH AX ; SAVE ORIG CONTENTS OF AX
E2C4	E462	749	IN AL,PORT_C
E2C6	ABC0	750	TEST AL,0C0H ; PARITY CHECK?
E2C8	7415	751	JZ D14 ; NO, EXIT FROM ROUTINE
E2CA	BEDAFF90	752	MOV SI,OFFSET D1 ; ADDR OF ERROR MSG
E2CE	A840	753	TEST AL,40H ; I/O PARITY CHECK
E2D0	7504	754	JNZ D13 ; DISPLAY ERROR MSG
E2D2	BE23FF90	755	MOV SI,OFFSET D2 ; MUST BE PLANAR
E2D6		756	D13:
E2D6	2BC0	757	SUB AX,AX ; INIT AND SET MODE FOR VIDEO
E2D8	CD10	758	INT 10H ; CALL VIDEO_IO PROCEDURE
E2DA	E8DD03	759	CALL P_MSG ; PRINT ERROR MSG
E2DD	FA	760	CLI
E2DE	F4	761	HLT ; HALT SYSTEM
E2DF		762	D14:
E2DF	58	763	POP AX ; RESTORE ORIG CONTENTS OF AX
E2E0	CF	764	IRET
		765	NMI_INT ENDP
E2E1		766	E7:
E2E1	3C30	767	CMP AL,30H ; TEST_VIDEO:
E2E3	7408	768	JE E8 ; B/W CARD ATTACHED?
E2E5	FEC4	769	INC AH ; YES - SET MODE FOR B/W CARD
E2E7	3C20	770	CMP AL,20H ; SET COLOR MODE FOR COLOR CD
E2E9	7502	771	JNE E8 ; 80X25 MODE SELECTED?
E2EB	B403	772	MOV AH,3 ; NO - SET MODE FOR 40X25
E2ED		773	E8:
E2ED	86E0	774	XCHG AH,AL ; SET_MODE
E2EF	50	775	PUSH AX ; SAVE VIDEO MODE ON STACK
E2F0	2AE4	776	SUB AH,AH ; INITIALIZE TO ALPHANUMERIC MD
E2F2	CD10	777	INT 10H ; CALL VIDEO_IO
E2F4	58	778	POP AX ; RESTORE VIDEO SENSE SHS IN AH
E2F5	50	779	PUSH AX ; RESAVE VALUE
E2F6	B800B0	780	MOV BX,0B000H ; BEG VIDEO RAM ADDR B/W CD
E2F9	BAB003	781	MOV DX,3B8H ; MODE REG FOR B/W
E2FC	B90010	782	MOV CX,4096 ; RAM BYTE CNT FOR B/W CD
E2FF	B001	783	MOV AL,1 ; SET MODE FOR BH CARD
E301	80FC30	784	CHP AH,30H ; B/W VIDEO CARD ATTACHED?
E304	7408	785	JE E9 ; YES - GO TEST VIDEO STG
E306	B7B8	786	MOV BH,0B8H ; BEG VIDEO RAM ADDR COLOR CD
E308	B2D8	787	MOV DL,0D8H ; MODE REG FOR COLOR CD
E30A	B540	788	MOV CH,40H ; RAM BYTE CNT FOR COLOR CD
E30C	FEC8	789	DEC AL ; SET MODE TO 0 FOR COLOR CD
E30E		790	E9:
E30E	EE	791	OUT DX,AL ; TEST_VIDEO_STG:
E30F	81FD3412	792	CHP BP,1234H ; DISABLE VIDEO FOR COLOR CD
E313	8EC3	793	MOV ES,BX ; P00 INITIATED BY KBD RESET?
E315	7407	794	JE E10 ; POINT ES TO VIDEO RAM STG
E317	8E08	795	MOV DS,BX ; YES - SKIP VIDEO RAM TEST
		796	ASSUME DS:NOTHING,ES:NOTHING ; POINT DS TO VIDEO RAM STG
E319	E8FFFC	797	CALL STGTST_CNT ; GO TEST VIDEO R/W STG
E31C	7532	798	JNE E17 ; R/W STG FAILURE - BEEP SPK
		799	;
		800	; SETUP VIDEO DATA ON SCREEN FOR VIDEO LINE TEST. ;
		801	; DESCRIPTION ;
		802	; ENABLE VIDEO SIGNAL AND SET MODE. ;
		803	; DISPLAY A HORIZONTAL BAR ON SCREEN. ;
		804	;
E31E		805	E10:
E31E	58	806	POP AX ; GET VIDEO SENSE SHS (AH)
E31F	50	807	PUSH AX ; SAVE IT
E320	B400	808	MOV AH,0 ; ENABLE VIDEO AND SET MODE
E322	CD10	809	INT 10H ; VIDEO
E324	B82070	810	MOV AX,7020H ; WRT BLANKS IN REVERSE VIDEO
E327	2BFF	811	SUB DI,DI ; SETUP STARTING LOC
E329	B92800	812	MOV CX,40 ; NO. OF BLANKS TO DISPLAY
E32C	F3	813	REP STOSW ; WRITE VIDEO STORAGE
E32D	AB		
		814	;
		815	; CRT INTERFACE LINES TEST ;
		816	; DESCRIPTION ;
		817	; SENSE ON/OFF TRANSITION OF THE VIDEO ENABLE ;

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LOC OBJ          LINE   SOURCE
                818   ;   AND HORIZONTAL SYNC LINES.   ;
                819   ;-----:
E32E 58          820   POP   AX           ; GET VIDEO SENSE SW INFO
E32F 50          821   PUSH  AX           ; SAVE IT
E330 80FC30      822   CMP   AH,30H       ; B/M CARD ATTACHED?
E333 BABA03      823   MOV   DX,03BAH     ; SETUP ADDR OF BW STATUS PORT
E336 7402        824   JE    E11           ; YES - GO TEST LINES
E338 B2DA        825   MOV   DL,0DAH     ; COLOR CARD IS ATTACHED
E33A             826   E11:              ; LINE_TST:
E33A B408        827   MOV   AH,8         ;
E33C             828   E12:              ; OFLOOP_CNT:
E33C 2BC9        829   SUB   CX,CX
E33E             830   E13:
E33E EC          831   IN    AL,DX        ; READ CRT STATUS PORT
E33F 22C4        832   AND   AL,AH        ; CHECK VIDEO/HORZ LINE
E341 7504        833   JNZ  E14           ; ITS ON - CHECK IF IT GOES OFF
E343 E2F9        834   LOOP E13          ; LOOP TILL ON OR TIMEOUT
E345 EB09        835   JMP   SHORT E17    ; GO PRINT ERROR MSG
E347             836   E14:
E347 2BC9        837   SUB   CX,CX
E349             838   E15:
E349 EC          839   IN    AL,DX        ; READ CRT STATUS PORT
E34A 22C4        840   AND   AL,AH        ; CHECK VIDEO/HORZ LINE
E34C 740A        841   JZ    E16           ; ITS ON - CHECK NEXT LINE
E34E E2F9        842   LOOP E15          ; LOOP IF OFF TILL IT GOES ON
E350             843   E17:              ; CRT_ERR
E350 BA0201      844   MOV   DX,102H     ;
E353 EB7902      845   CALL ERR_BEEP     ; GO BEEP SPEAKER
E356 EB06        846   JMP   SHORT E18    ;
E358             847   E16:              ; NXT_LINE
E358 B103        848   MOV   CL,3         ; GET NEXT BIT TO CHECK
E35A D2EC        849   SHR   AH,CL
E35C 75DE        850   JNZ  E12           ; GO CHECK HORIZONTAL LINE
E35E             851   E18:              ; DISPLAY_CURSOR:
E35E 58          852   POP   AX           ; GET VIDEO SENSE SWS (AH)
E35F B400        853   MOV   AH,0         ; SET MODE AND DISPLAY CURSOR
E361 CD10        854   INT  10H          ; CALL VIDEO I/O PROCEDURE
                855
E363             856   E18_I:
E363 BA00C0      857   MOV   DX,0C000H   ;
E366             858   E18A:
E366 8EDA        859   MOV   DS,DX
E368 2BDB        860   SUB   BX,BX
E36A 8B07        861   MOV   AX,1BX1     ; GET FIRST 2 LOCATIONS
E36C 53          862   PUSH BX
E36D 5B          863   POP  BX           ; LET BUS SETTLE
E36E 3D55AA      864   CMP   AX,0AA55H   ; PRESENT?
E371 7505        865   JNZ  E18B         ; NO? GO LOOK FOR OTHER MODULES
E373 E80E03      866   CALL ROM_CHECK    ; GO SCAN MODULE
E376 EB04        867   JMP   SHORT E18C  ;
E378             868   E18B:
E378 81C28000     869   ADD   DX,0080H    ; POINT TO NEXT 2K BLOCK
E37C             870   E18C:
E37C 81FA00C8     871   CMP   DX,0C800H   ; TOP OF VIDEO ROM AREA YET?
E380 7CE4        872   JL   E18A         ; GO SCAN FOR ANOTHER MODULE
                873   ;-----:
                874   ; EXPANSION I/O BOX TEST   ;
                875   ; CHECK TO SEE IF EXPANSION BOX PRESENT - IF INSTALLED, ;
                876   ; TEST DATA AND ADDRESS BUSES TO I/O BOX. ;
                877   ; ERROR='1801' ;
                878   ;-----:
                879
                880   ;---- DETERMINE IF BOX IS PRESENT
                881
E382             882   EXP_ID:
E382 BA1002      883   MOV   DX,0210H    ; (CARD WAS ENABLED EARLIER)
E385 B85555      884   MOV   AX,5555H    ; CONTROL PORT ADDRESS
E388 EE          885   OUT  DX,AL        ; SET DATA PATTERN
E389 B001        886   MOV   AL,01H
E38B EC          887   IN   AL,DX        ; RECOVER DATA
E38C 3AC4        888   CMP   AL,AH        ; REPLY?
E38E 7534        889   JNE  E19           ; NO RESPONSE, GO TO NEXT TEST
E390 F7D0        890   NOT  AX           ; MAKE DATA=AAAA
E392 EE          891   OUT  DX,AL
E393 B001        892   MOV   AL,01H
E395 EC          893   IN   AL,DX        ; RECOVER DATA
E396 3ACA        894   CMP   AL,AH

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LOC OBJ          LINE  SOURCE
E398 752A        895          JNE    E19          ; NO ANSWER=NEXT TEST
E398          896
E398          897          :----- CHECK ADDRESS AND DATA BUS
E398          898
E39A          899          EXPI:
E39A 8BD8        900          MOV    BX,AX
E39C 8A1402      901          MOV    DX,0214H      ; LOAD DATA REG ADDRESS
E39F 2E8807      902          MOV    CS:[BX],AL    ; WRITE ADDRESS F000+BX
E3A2 EE          903          OUT    DX,AL         ; WRITE DATA
E3A3 90          904          NOP
E3A4 EC          905          IN     AL,DX         ; READ DATA
E3A5 3AC7        906          CMP    AL,BH
E3A7 7514        907          JNE    EXP_ERR
E3A9 42          908          INC    DX            ; DX=215H (ADDR. HI REG)
E3AA EC          909          IN     AL,DX
E3AB 3AC4        910          CMP    AL,AH         ; COMPARE TO HI ADDRESS
E3AD 750E        911          JNE    EXP_ERR
E3AF 42          912          INC    DX            ; DX=216H (ADDR. LOW REG)
E3B0 EC          913          IN     AL,DX
E3B1 3AC4        914          CMP    AL,AH         ; ADDR. LOW OK?
E3B3 7508        915          JNE    EXP_ERR
E3B5 F7D0        916          NOT    AX            ; INVERT AX
E3B7 3CAA        917          CMP    AL,0AAH      ; BACK TO STARTING VALUE (AAAA) YET
E3B9 7409        918          JE     E19           ; GO ON TO NEXT TEST IF SO
E3BB EBDD        919          JMP    EXP1          ; LOOP BACK THROUGH WITH DATA OF 5555
E3B0          920
E3BD BEEDFE90    921          MOV    SI,OFFSET F3B
E3C1 E8F602      922          CALL  P_MSG
E3C1          923          :-----
E3C1          924          ; ADDITIONAL READ/WRITE STORAGE TEST :
E3C1          925          ; DESCRIPTION :
E3C1          926          ; WRITE/READ DATA PATTERNS TO ANY READ/WRITE STORAGE :
E3C1          927          ; AFTER THE BASIC 16K. STORAGE ADDRESSABILITY IS CHECKED. :
E3C1          928          :-----
E3C1          929          ASSUME DS:DATA
E3C4          930          E19:
E3C4          931
E3C4          932          :----- DETERMINE RAM SIZE ON PLANAR BOARD
E3C4          933
E3C4 E8771B      934          CALL  DDS
E3C7 A01000      935          MOV    AL,BYTE PTR EQUIP_FLAG ; GET SENSE SMS INFO
E3CA 240C        936          AND    AL,0CH        ; ISOLATE RAM SIZE SMS
E3CC B404        937          MOV    AH,4
E3CE F6E4        938          MUL    AH
E3D0 0410        939          ADD    AL,16         ; ADD BASIC 16K
E3D2 8BD0        940          MOV    DX,AX         ; SAVE PLANAR RAM SIZE IN DX
E3D4 8BD8        941          MOV    BX,AX         ; AND IN BX
E3D4          942
E3D4          943          :----- DETERMINE IO CHANNEL RAM SIZE
E3D4          944
E3D6 A11500      945          MOV    AX,IO_RAM_SIZE ; GET IO CHANNEL RAM SIZE
E3D9 83FB40      946          CMP    BX,40H        ; PLANAR RAM SIZE = 64K?
E3DC 7402        947          JE     E20           ; YES - ADD IO CHN RAM SIZE
E3DE 2BC0        948          SUB    AX,AX         ; NO - DON'T ADD ANY IO RAM
E3E0          949          E20:
E3E0 03C3        950          ADD    AX,BX         ; ADD_IO_SIZE:
E3E2 A31300      951          MOV    MEMORY_SIZE,AX ; SUM TOTAL RAM SIZE
E3E5 81FD3412    952          CMP    BP,1234H      ; SETUP MEMORY SIZE PARM
E3E9 1E          953          PUSH  DS             ; P00 INITIATED BY KBD RESET?
E3EA 744F        954          JE     TST12         ; SAVE DATA SEGMENT
E3EA          955          ; YES - SKIP MEMORY TEST
E3EA          956          :----- TEST ANY OTHER READ/WRITE STORAGE AVAILABLE
E3EA          957
E3EC BB0004      958          MOV    BX,400H
E3EF B91000      959          MOV    CX,16
E3F2          960          E21:
E3F2 3BD1        961          CMP    DX,CX         ; ANY MORE STG TO BE TESTED?
E3F4 762D        962          JBE    E23           ; NO - GO TO NEXT TEST
E3F6 8ED8        963          MOV    DS,BX         ; SETUP STG ADDR IN DS AND ES
E3F8 8EC3        964          MOV    ES,BX
E3FA 83C110      965          ADD    CX,16         ; INCREMENT STG BYTE COUNTER
E3FD 81C30004    966          ADD    BX,400H      ; SET POINTER TO NEXT 16K BLK
E401 51          967          PUSH  CX             ; SAVE REGS
E402 53          968          PUSH  BX
E403 52          969          PUSH  DX
E404 E811FC      970          CALL  STGTST        ; GO TEST A 16K BLK OF STG
E407 5A          971          POP   DX

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LOC OBJ	LINE	SOURCE		
E408 5B	972	POP	BX	; RESTORE REGS
E409 59	973	POP	CX	
E40A 74E6	974	JE	E21	; CHECK IF MORE STG TO TEST
	975			
	976			;----- PRINT FAILING ADDRESS AND XOR'ED PATTERN IF DATA COMPARE ERROR
	977			
E40C 8CDA	978	MOV	DX,DS	; CONVERT FAILING HIGH-ORDER
E40E 8AE8	979	MOV	CH,AL	; SAVE FAILING BIT PATTERN
E410 8AC6	980	MOV	AL,0H	; GET FAILING ADDR
E412 E81002	981	CALL	XPC_BYTE	; CONVERT AND PRINT CODE
E415 8AC5	982	MOV	AL,CH	; GET FAILING BIT PATTERN
E417 E80B02	983	CALL	XPC_BYTE	; CONVERT AND PRINT CODE
E41A BE67FA90	984	MOV	SI,OFFSET E1	; SETUP ADDRESS OF ERROR MSG
E41E E89902	985	CALL	P_MSG	; PRINT ERROR MSG
E421	986	E22:		
E421 EB18	987	JMP	SHORT TST12	; GO TO NEXT TEST
E423	988	E23:		
E423 1F	989	POP	DS	; STG_TEST_DONE
E424 1E	990	PUSH	DS	; POINT DS TO DATA SEGMENT
E425 8B161500	991	MOV	DX,IO_RAM_SIZE	; GET IO CHANNEL RAM SIZE
E429 0BD2	992	OR	DX,DX	; SET FLAG RESULT
E42B 740E	993	JZ	TST12	; NO IO RAM, GO TO NEXT TEST
E42D B90000	994	MOV	CX,0	
E430 81FB0010	995	CMP	BX,1000H	; HAS IO RAM BEEN TESTED
E434 7705	996	JA	TST12	; YES - GO TO NEXT TEST
E436 BB0010	997	MOV	BX,1000H	; SETUP BEG LOC FOR IO RAM
E439 EBB7	998	JMP	E21	; GO TEST IO CHANNEL RAM
	999			;-----
	1000			; KEYBOARD TEST :
	1001			; DESCRIPTION :
	1002			; RESET THE KEYBOARD AND CHECK THAT SCAN CODE :
	1003			; 'AA' IS RETURNED TO THE CPU. CHECK FOR STUCK :
	1004			; KEYS. :
	1005			;-----
	1006			ASSUME DS:DATA
	1007	TST12:		
E43B	1008	POP	DS	
E43C 803E120001	1009	CMP	HFG_TST,1	; MANUFACTURING TEST MODE?
E441 742A	1010	JE	F7	; YES - SKIP KEYBOARD TEST
E443 E8FD01	1011	CALL	KBD_RESET	; ISSUE SOFTWARE RESET TO KEYBOD
E446 E31E	1012	JCZX	F6	; PRINT ERR MSG IF NO INTERRUPT
E448 B04D	1013	MOV	AL,40H	; ENABLE KEYBOARD
E44A E661	1014	OUT	PORT_B,AL	
E44C 80FBAA	1015	CMP	BL,0AAH	; SCAN CODE AS EXPECTED?
E44F 7515	1016	JNE	F6	; NO - DISPLAY ERROR MSG
	1017			
	1018			;----- CHECK FOR STUCK KEYS
	1019			
E451 B0CC	1020	MOV	AL,0CCH	; CLR KBD, SET CLK LINE HIGH
E453 E661	1021	OUT	PORT_B,AL	
E455 B04C	1022	MOV	AL,4CH	; ENABLE KBD,CLK IN NEXT BYTE
E457 E661	1023	OUT	PORT_B,AL	
E459 2BC9	1024	SUB	CX,CX	
E45B	1025	F5:		; KBD_WAIT
E45B E2FE	1026	LOOP	F5	; DELAY FOR A WHILE
E45D E460	1027	IN	AL,KBD_IN	; CHECK FOR STUCK KEYS
E45F 3C00	1028	CMP	AL,0	; SCAN CODE = 0?
E461 740A	1029	JE	F7	; YES - CONTINUE TESTING
E463 E8BF01	1030	CALL	XPC_BYTE	; CONVERT AND PRINT
E466 BE33FF90	1031	MOV	SI,OFFSET F1	; GET MSG ADDR
E46A E84D02	1032	CALL	P_MSG	; PRINT MSG ON SCREEN
	1033			
	1034			;----- SETUP INTERRUPT VECTOR TABLE
	1035			
E46D	1036	F7:		; SETUP_INT_TABLE:
E46D 2BC0	1037	SUB	AX,AX	
E46F 8ECC	1038	MOV	ES,AX	
E471 B90800	1039	MOV	CX,8	; GET VECTOR CNT
E474 1E	1040	PUSH	DS	; SAVE DATA SEGMENT
E475 0E	1041	PUSH	CS	; SETUP DS SEG REG
E476 1F	1042	POP	DS	
E477 BEF3FE90	1043	MOV	SI,OFFSET VECTOR_TABLE	
E47B BF2000	1044	MOV	DI,OFFSET INT_PTR	
E47E	1045	F7A:		
E47E A5	1046	MOVSB		
E47F 47	1047	INC	DI	; SKIP OVER SEGMENT
E480 47	1048	INC	DI	

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LOC OBJ          LINE  SOURCE
E481 E2FB        1049          LOOP    F7A
1050          ;-----
1051          ; CASSETTE DATA WRAP TEST          :
1052          ; DESCRIPTION                      :
1053          ;   TURN CASSETTE MOTOR OFF. WRITE A BIT OUT TO THE :
1054          ;   CASSETTE DATA BUS. VERIFY THAT CASSETTE DATA :
1055          ;   READ IS WITHIN A VALID RANGE.          :
1056          ;-----
1057
1058          ;----- TURN THE CASSETTE MOTOR OFF
1059
E483            1060          TST13:
E483 1F          1061          POP     DS
E484 1E          1062          PUSH  DS
E485 B04D        1063          MOV     AL,04DH          ; SET TIMER 2 SPK OUT, AND CASST
E487 E661        1064          OUT     PORT_B,AL      ; OUT BITS ON, CASSETTE MOT OFF
1065
          ;----- WRITE A BIT
1066
E489 B0FF        1068          MOV     AL,0FFH          ; DISABLE TIMER INTERRUPTS
E48B E621        1069          OUT     INTA01,AL
E48D B0B6        1070          MOV     AL,0B6H          ; SEL TIM 2, LSB, MSB, MD 3
E48F E643        1071          OUT     TIMER+3,AL      ; WRITE 8253 CHD/MODE REG
E491 B8D304      1072          MOV     AX,1235          ; SET TIMER 2 CNT FOR 1000 USEC
E494 E642        1073          OUT     TIMER+2,AL      ; WRITE TIMER 2 COUNTER REG
E496 8AC4        1074          MOV     AL,AH            ; WRITE MSB
E498 E642        1075          OUT     TIMER+2,AL
1076
1077          ;----- READ CASSETTE INPUT
1078
E49A E462        1079          IN     AL,PORT_C        ; READ VALUE OF CASS IN BIT
E49C 2410        1080          AND     AL,10H          ; ISOLATE FROM OTHER BITS
E49E A26B00      1081          MOV     LAST_VAL,AL
E4A1 E8D514      1082          CALL   READ_HALF_BIT
E4A4 E8D214      1083          CALL   READ_HALF_BIT
E4A7 E30C        1084          JCKZ   F8                ; CAS_ERR
E4A9 81FB4005    1085          CMP     BX,MAX_PERIOD
E4AD 7306        1086          JNC    F8                ; CAS_ERR
E4AF 81FB1004    1087          CMP     BX,MIN_PERIOD
E4B3 7307        1088          JNC    ROM_SCAN          ; GO TO NEXT TEST IF OK
E4B5            1089          F8:
E4B5 BE39FF90    1090          MOV     SI,OFFSET F2
E4B9 E8FE01        1091          CALL   P_MSG            ; GO PRINT ERROR MSG
1092
          ;-----
1093          ; CHECK FOR OPTIONAL ROM FROM C8000->F4000 IN 2K INCREMENTS :
1094          ; (A VALID MODULE HAS '55AA' IN THE FIRST 2 LOCATIONS, LENGTH :
1095          ; INDICATOR (LENGTH/512) IN THE 3RD LOCATION AND TEST/INIT. :
1096          ; CODE STARTING IN THE 4TH LOCATION.) :
1097          ;-----
E4BC            1098          ROM_SCAN:
E4BC BA00C8      1099          MOV     DX,0C800H          ; SET BEGINNING ADDRESS
E4BF            1100          ROM_SCAN_1:
E4BF 8EDA        1101          MOV     DS,DX
E4C1 2BDB        1102          SUB     BX,BX            ; SET BX=0000
E4C3 8B07        1103          MOV     AX,1BX1          ; GET 1ST WORD FROM MODULE
E4C5 3D55AA      1104          CMP     AX,0AA55H        ; = TO ID WORD?
E4C8 7505        1105          JNZ    NEXT_ROM          ; PROCEED TO NEXT ROM IF NOT
E4CA E8B701      1106          CALL   ROM_CHECK        ; GO DO CHECKSUM AND CALL
E4CD EB04        1107          JMP    SHORT ARE_WE_DONE ; CHECK FOR END OF ROM SPACE
E4CF            1108          NEXT_ROM:
E4CF 81C2B000    1109          ADD     DX,0080H          ; POINT TO NEXT 2K ADDRESS
E4D3            1110          ARE_WE_DONE:
E4D3 81FA00F6    1111          CMP     DX,0F600H        ; AT F6000 YET?
E4D7 7CE6        1112          JL     ROM_SCAN_1        ; GO CHECK ANOTHER ADD. IF NOT
E4D9 EB0190      1113          JMP     BASE_ROM_CHK      ; GO CHECK BASIC ROM
1114
          ;-----
1115          ; ROS CHECKSUM II          :
1116          ; DESCRIPTION                      :
1117          ;   A CHECKSUM IS DONE FOR THE 4 ROS :
1118          ;   MODULES CONTAINING BASIC CODE :
1119          ;-----
E4DC            1120          BASE_ROM_CHK:
E4DC            1121          E4:
E4DC 2BDB        1122          SUB     BX,BX            ; SETUP STARTING ROS ADDR
E4DE 8EDA        1123          MOV     DS,DX
E4E0 EB6907      1124          CALL   ROS_CHECKSUM      ; CHECK ROS

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LOC OBJ	LINE	SOURCE	
E4E3 7403	1125	JE E5	; CONTINUE IF OK
E4E5 E82103	1126	CALL ROM_ERR	; POST ERROR
E4E8	1127	E5:	
E4E8 80C602	1128	ADD DH,02H	; POINT TO NEXT 8K MODULE
E4E8 80FEFE	1129	CMF DH,0FEH	
E4EE 75EC	1130	JNZ E4	; YES - CONTINUE
E4F0 1F	1131	POP DS	; RECOVER DATA SEG PTR
	1132		
	1133	-----	
	1134	DISKETTE ATTACHMENT TEST	
	1135	DESCRIPTION	
	1136	CHECK IF IPL DISKETTE DRIVE IS ATTACHED TO SYSTEM. IF ATTACHED,	
	1137	VERIFY STATUS OF NEC FDC AFTER A RESET. ISSUE A RECAL AND SEEK	
	1138	CHD TO FDC AND CHECK STATUS. COMPLETE SYSTEM INITIALIZATION	
	1139	THEN PASS CONTROL TO THE BOOT LOADER PROGRAM.	
	1140	F9:	
E4F1 A01000	1141	MOV AL, BYTE PTR EQUIP_FLAG	; GET SENSE SMS INFO
E4F4 A801	1142	TEST AL, 01H	; IPL DISKETTE DRIVE ATTCH?
E4F6 750A	1143	JNZ F10	; NO - SKIP THIS TEST
E4F8 803E120001	1144	CMF MFG_TST, 1	; MANUFACTURING TEST MODE?
E4FD 7530	1145	JNE F15A	; NO - GO TO BOOT LOADER
E4FF E959FB	1146	JMP START	; YES - LOOP POWER-ON-DIAGS
E502	1147	F10:	
E502 E421	1148	IN AL, INTA01	; DISK_TEST
E504 24BF	1149	AND AL, 0BFH	; ENABLE DISKETTE INTERRUPTS
E506 E621	1150	OUT INTA01, AL	
E508 B400	1151	MOV AH, 0	; RESET NEC FDC
E50A 8AD4	1152	MOV DL, AH	; (POINT TO DISKETTE)
E50C CD13	1153	INT 13H	; VERIFY STATUS AFTER RESET
E50E 7221	1154	JC F13	
	1155		
	1156	-----	
	1157	TURN DRIVE 0 MOTOR ON	
E510 BAF203	1158	MOV DX, 03F2H	; GET ADDR OF FDC CARD
E513 52	1159	PUSH DX	; SAVE IT
E514 B01C	1160	MOV AL, 1CH	; TURN MOTOR ON, EN DMA/INT
E516 EE	1161	OUT DX, AL	; WRITE FDC CONTROL REG
E517 2BC9	1162	SUB CX, CX	
E519	1163	F11:	
E519 E2FE	1164	LOOP F11	; MOTOR_WAIT:
E51B	1165	F12:	
E51B E2FE	1166	LOOP F12	; MOTOR_WAIT1:
E51D 33D2	1167	XOR DX, DX	; SELECT DRIVE 0
E51F B501	1168	MOV CH, 1	; SELECT TRACK 1
E521 68163E00	1169	MOV SEEK_STATUS, DL	
E525 E85509	1170	CALL SEEK	; RECALIBRATE DISKETTE
E528 7207	1171	JC F13	; GO TO ERR SUBROUTINE IF ERR
E52A B522	1172	MOV CH, 34	; SELECT TRACK 34
E52C E84E09	1173	CALL SEEK	; SEEK TO TRACK 34
E52F 7307	1174	JNC F14	; OK, TURN MOTOR OFF
E531	1175	F13:	
E531 BEAFF90	1176	MOV SI, OFFSET F3	; GET ADDR OF MSG
E535 E68201	1177	CALL P_MSG	; GO PRINT ERROR MSG
	1178		
	1179	-----	
	1180	TURN DRIVE 0 MOTOR OFF	
E538	1181	F14:	
E538 B00C	1182	MOV AL, 0CH	; DR0_OFF:
E53A 5A	1183	POP DX	; TURN DRIVE 0 MOTOR OFF
E53B EE	1184	OUT DX, AL	; RECOVER FDC CTL ADDRESS
	1185		
	1186	-----	
	1187	SETUP PRINTER AND RS232 BASE ADDRESSES IF DEVICE ATTACHED	
E53C	1188	F15A:	
E53C BE1E00	1189	MOV SI, OFFSET KB_BUFFER	
E53F 89361A00	1190	MOV BUFFER_HEAD, SI	; SETUP KEYBOARD PARAMETERS
E543 89361C00	1191	MOV BUFFER_TAIL, SI	
E547 89368000	1192	MOV BUFFER_START, SI	; DEFAULT TO STANDARD BUFFER
E54B 83C620	1193	ADD SI, 32	; (32 BYTES LONG)
E54E 89368200	1194	MOV BUFFER_END, SI	
E552 E421	1195	IN AL, INTA01	
E554 24FC	1196	AND AL, 0FCH	; ENABLE TIMER AND KBD INTS
E556 E621	1197	OUT INTA01, AL	
E558 B03DE690	1198	MOV BP, OFFSET F4	; PRT_SRC_TBL
E55C 2BF6	1199	SUB SI, SI	
E55E	1200	F16:	
E55E 2E885600	1201	MOV DX, CS:[BP]	; PRT_BASE:
			; GET PRINTER BASE ADDR

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LOC OBJ          LINE  SOURCE
E562 80AA       1202      MOV    AL,0AAH          ; WRITE DATA TO PORT A
E564 EE         1203      OUT   DX,AL
E565 52         1204      PUSH  DX
E566 EC         1205      IN    AL,DX            ; READ PORT A
E567 5A         1206      POP   DX
E568 3CAA       1207      CMP   AL,0AAH          ; DATA PATTERN SAME
E56A 75D5       1208      JNE   F17              ; NO - CHECK NEXT PRT CD
E56C 895408     1209      MOV   PRINTER_BASE[SI],DX ; YES - STORE PRT BASE ADDR
E56F 46         1210      INC   SI                ; INCREMENT TO NEXT WORD
E570 46         1211      INC   SI
E571           1212      F17:                   ; NO_STORE:
E571 45         1213      INC   BP                ; POINT TO NEXT BASE ADDR
E572 45         1214      INC   BP
E573 81FD43E6   1215      CMP   BP,OFFSET F4E    ; ALL POSSIBLE ADDRS CHECKED?
E577 75E5       1216      JNE   F16              ; PRT_BASE
E579 2BDB       1217      SUB   BX,BX            ; POINTER TO RS232 TABLE
E57B BAFAD3     1218      MOV   DX,3FAH          ; CHECK IF RS232 CD 1 ATTCH?
E57E EC         1219      IN    AL,DX            ; READ INTR ID REG
E57F A8F8       1220      TEST  AL,0F8H
E581 7506       1221      JNZ   F18              ;
E583 C707F803   1222      MOV   RS232_BASE[BX],3F6H ; SETUP RS232 CD #1 ADDR
E587 43         1223      INC   BX
E588 43         1224      INC   BX
E589           1225      F18:                   ;
E589 B602       1226      MOV   DH,02H          ; CHECK IF RS232 CD 2 ATTCH (AT 2FA)
E58B EC         1227      IN    AL,DX            ; READ INTERRUPT ID REG
E58C A8F8       1228      TEST  AL,0F8H
E58E 7506       1229      JNZ   F19              ; BASE_END
E590 C707F802   1230      MOV   RS232_BASE[BX],2F6H ; SETUP RS232 CD #2
E594 43         1231      INC   BX
E595 43         1232      INC   BX
E596           1233
E596           1234      ;----- SET UP EQUIP FLAG TO INDICATE NUMBER OF PRINTERS AND RS232 CARDS
E596           1235
E596           1236      F19:                   ; BASE_END:
E596 8BC6       1237      MOV   AX,SI            ; SI HAS 2* NUMBER OF RS232
E598 B103       1238      MOV   CL,3            ; SHIFT COUNT
E59A D2C8       1239      ROR   AL,CL            ; ROTATE RIGHT 3 POSITIONS
E59C 0AC3       1240      OR    AL,BL            ; OR IN THE PRINTER COUNT
E59E AC1100     1241      MOV   BYTE PTR EQUIP_FLAG+1,AL ; STORE AS SECOND BYTE
E5A1 B201       1242      MOV   DL,01H          ; DX=201
E5A3 EC         1243      IN    AL,DX
E5A4 A80F       1244      TEST  AL,0FH
E5A6 7505       1245      JNZ   F20              ; NO_GAME_CARD
E5A8 800E110010 1246      OR    BYTE PTR EQUIP_FLAG+1,16
E5AD           1247      F20:                   ;
E5AD           1248
E5AD           1249      ;----- SET DEFAULT TIMEOUT VALUES FOR PRINTER AND RS232
E5AD           1250
E5AD 1E         1251      PUSH  DS
E5AE 07         1252      POP   ES
E5AF BF7800     1253      MOV   DI,OFFSET PRINT_TIM_OUT
E5B2 B81414     1254      MOV   AX,1414H        ; PRINTER DEFAULTS (COUNT=20)
E5B5 AB         1255      STOSW
E5B6 AB         1256      STOSW
E5B7 B80101     1257      MOV   AX,0101H        ; RS232 DEFAULTS=01
E5BA AB         1258      STOSW
E5BB AB         1259      STOSW
E5BC           1260
E5BC           1261      ;----- ENABLE NMI INTERRUPTS
E5BC           1262
E5BC B080       1263      MOV   AL,80H          ; ENABLE NMI INTERRUPTS
E5BE E6A0       1264      OUT   0A0H,AL
E5C0 803E120001 1265      CMP   MFG_TST,1        ; MFG MODE?
E5C5 7406       1266      JE    F21              ; LOAD_BOOT_STRAP
E5C7 BA0100     1267      MOV   DX,1
E5CA E80200     1268      CALL  ERR_BEEP         ; BEEP 1 SHORT TONE
E5CB           1269
E5CD           1270      F21:                   ; LOAD_BOOT_STRAP:
E5CD CD19       1271      INT   19H             ; BOOTSTRAP
E5CE           1272
E5CE           1273      ;-----
E5CE           1274      ; INITIAL RELIABILITY TEST -- SUBROUTINES ;
E5CE           1275      ;-----
E5CE           1276      ASSUME CS:CODE,DS:DATA
E5CE           1277      ;-----
E5CE           1278      ; SUBROUTINES FOR POWER ON DIAGNOSTICS ;

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LOC OBJ	LINE	SOURCE
	1279	; THIS PROCEDURE WILL ISSUE ONE LONG TONE (3 SECS) AND ONE OR
	1280	; MORE SHORT TONES (1 SEC) TO INDICATE A FAILURE ON THE PLANAR
	1281	; BOARD, A BAD RAM MODULE, OR A PROBLEM WITH THE CRT.
	1282	; ENTRY PARAMETERS:
	1283	; DH = NUMBER OF LONG TONES TO BEEP
	1284	; DL = NUMBER OF SHORT TONES TO BEEP
	1285	-----
ESCF	1286	ERR_BEEP PROC NEAR
ESCF 9C	1287	PUSHF ; SAVE FLAGS
ESD0 FA	1288	CLI ; DISABLE SYSTEM INTERRUPTS
ESD1 1E	1289	PUSH DS ; SAVE DS REG CONTENTS
ESD2 E86919	1290	CALL DDS
ESD5 0AF6	1291	OR DH,DH ; ANY LONG ONES TO BEEP
ESD7 7418	1292	JZ G3 ; NO, DO THE SHORT ONES
ESD9	1293	G1: ; LONG_BEEP:
ESD9 B306	1294	MOV BL,6 ; COUNTER FOR BEEPS
ESDB E82500	1295	CALL BEEP ; DO THE BEEP
ESDE E2FE	1296	G2: LOOP G2 ; DELAY BETWEEN BEEPS
ESEO FECE	1297	DEC DH ; ANY MORE TO DO
ESE2 75F5	1298	JNZ G1 ; DO IT
ESE6 803E120001	1299	CHP MFG_TST,1 ; MFG TEST MODE?
ESE9 7506	1300	JNE G3 ; YES - CONTINUE BEEPING SPEAKER
ESEB B0CD	1301	MOV AL,0CDH ; STOP BLINKING LED
ESD E661	1302	OUT PORT_B,AL
ESEF EBE8	1303	JMP SHORT G1
ESF1	1304	G3: ; SHORT_BEEP:
ESF1 B301	1305	MOV BL,1 ; COUNTER FOR A SHORT BEEP
ESF3 E80D00	1306	CALL BEEP ; DO THE SOUND
ESF6	1307	G4: ;
ESF6 E2FE	1308	LOOP G4 ; DELAY BETWEEN BEEPS
ESF8 FECA	1309	DEC DL ; DONE WITH SHORTS
ESFA 75F5	1310	JNZ G3 ; DO SOME MORE
ESFC	1311	G5: ;
ESFC E2FE	1312	LOOP G5 ; LONG DELAY BEFORE RETURN
ESFE	1313	G6: ;
ESFE E2FE	1314	LOOP G6 ;
E600 1F	1315	POP DS ; RESTORE ORIG CONTENTS OF DS
E601 9D	1316	POPF ; RESTORE FLAGS TO ORIG SETTINGS
E602 C3	1317	RET ; RETURN TO CALLER
	1318	ERR_BEEP ENDP
	1319	
	1320	;----- ROUTINE TO SOUND BEEPER
	1321	
E603	1322	BEEP PROC NEAR
E603 B0B6	1323	MOV AL,10110110B ; SEL TIM 2,LSB,MSB,BINARY
E605 E643	1324	OUT TIMER+3,AL ; WRITE THE TIMER MODE REG
E607 B83305	1325	MOV AX,533H ; DIVISOR FOR 1000 HZ
E60A E642	1326	OUT TIMER+2,AL ; WRITE TIMER 2 CNT - LSB
E60C 8AC4	1327	MOV AL,AH ;
E60E E642	1328	OUT TIMER+2,AL ; WRITE TIMER 2 CNT - MSB
E610 E461	1329	IN AL,PORT_B ; GET CURRENT SETTING OF PORT
E612 8AE0	1330	MOV AH,AL ; SAVE THAT SETTING
E614 0C03	1331	OR AL,03 ; TURN SPEAKER ON
E616 E661	1332	OUT PORT_B,AL
E618 2BC9	1333	SUB CX,CX ; SET CNT TO WAIT 500 MS
E61A	1334	G7: ;
E61A E2FE	1335	LOOP G7 ; DELAY BEFORE TURNING OFF
E61C FECB	1336	DEC BL ; DELAY CNT EXPIRED?
E61E 75FA	1337	JNZ G7 ; NO - CONTINUE BEEPING SPK
E620 8AC4	1338	MOV AL,AH ; RECOVER VALUE OF PORT
E622 E661	1339	OUT PORT_B,AL
E624 C3	1340	RET ; RETURN TO CALLER
	1341	BEEP ENDP
	1342	
	1343	-----
	1344	; CONVERT AND PRINT ASCII CODE :
	1345	; AL MUST CONTAIN NUMBER TO BE CONVERTED. :
	1346	; AX AND BX DESTROYED. :
	1347	-----
E625	1348	XPC_BYTE PROC NEAR
E625 50	1349	PUSH AX ; RESAVE FOR LOW NIBBLE DISPLAY
E626 B104	1350	MOV CL,4 ; SHIFT COUNT
E628 D2E8	1351	SHR AL,CL ; NIBBLE SWAP
E62A E80300	1352	CALL XLAT_PR ; DO THE HIGH NIBBLE DISPLAY
E62D 58	1353	POP AX ; RECOVER THE NIBBLE
E62E 240F	1354	AND AL,0FH ; ISOLATE TO LOW NIBBLE
	1355	; FALL INTO LOW NIBBLE CONVERSION

```

LOC OBJ          LINE  SOURCE
E630             1356  XLAT_PR  PROC   NEAR           ; CONVERT 00-0F TO ASCII CHARACTER
E630 0490       1357          ADD   AL,09DH           ; ADD FIRST CONVERSION FACTOR
E632 27         1358          DAA                   ; ADJUST FOR NUMERIC AND ALPHA RANGE
E633 1440       1359          ADC   AL,040H          ; ADD CONVERSION AND ADJUST LOW NIBBLE
E635 27         1360          DAA                   ; ADJUST HI NIBBLE TO ASCII RANGE
E636           1361  PRT_HEX  PROC   NEAR
E636 840E       1362          MOV   AH,14           ; DISPLAY CHAR. IN AL
E638 B700       1363          MOV   BH,0
E63A CD10       1364          INT  10H             ; CALL VIDEO_IO
E63C C3         1365          RET
E636           1366  PRT_HEX  ENDP
E636           1367  XLAT_PR  ENDP
E636           1368  XPC_BYTE  ENDP
E636           1369
E630           1370  F4      LABEL  WORD           ; PRINTER SOURCE TABLE
E63D BC03       1371          DW   38CH
E63F 7803       1372          DW   378H
E641 7802       1373          DW   278H
E643           1374  F4E     LABEL  WORD
E643           1375
E643           1376
E643           1377 ; -----
E643           1377 ;           THIS PROCEDURE WILL SEND A SOFTWARE RESET TO THE KEYBOARD. ;
E643           1378 ;           SCAN CODE 'AA' SHOULD BE RETURNED TO THE CPU. ;
E643           1379 ; -----
E643           1380  KBD_RESET  PROC   NEAR
E643 B09C       1381          MOV   AL,0CH          ; SET KBD CLK LINE LOW
E645 E661       1382          OUT  PORT_B,AL        ; WRITE 8255 PORT B
E647 B95629     1383          MOV   CX,10582        ; HOLD KBD CLK LOW FOR 20 MS
E64A           1384  G8:
E64A E2FE       1385          LOOP  G8              ; LOOP FOR 20 MS
E64C B0CC       1386          MOV   AL,0CCH        ; SET CLK, ENABLE LINES HIGH
E64E E661       1387          OUT  PORT_B,AL
E650           1388  SP_TEST:
E650 B04C       1389          MOV   AL,4CH          ; ENTRY FOR MANUFACTURING TEST 2
E652 E661       1390          OUT  PORT_B,AL        ; SET KBD CLK HIGH, ENABLE LOW
E654 B0FD       1391          MOV   AL,0FDH        ; ENABLE KEYBOARD INTERRUPTS
E656 E621       1392          OUT  INTA01,AL       ; WRITE 8259 IMR
E658 FB         1393          STI                   ; ENABLE SYSTEM INTERRUPTS
E659 B400       1394          MOV   AH,0           ; RESET INTERRUPT INDICATOR
E65B 2BC9       1395          SUB  CX,CX            ; SETUP INTERRUPT TIMEOUT CNT
E65D           1396  G9:
E65D F6C4FF     1397          TEST  AH,0FFH        ; DID A KEYBOARD INTR OCCUR?
E660 7502       1398          JNZ  G10              ; YES - READ SCAN CODE RETURNED
E662 E2F9       1399          LOOP  G9              ; NO - LOOP TILL TIMEOUT
E664           1400  G10:
E664 E440       1401          IN   AL,PORT_A        ; READ KEYBOARD SCAN CODE
E666 8AD8       1402          MOV  BL,AL            ; SAVE SCAN CODE JUST READ
E668 B0CC       1403          MOV  AL,0CCH        ; CLEAR KEYBOARD
E66A E661       1404          OUT  PORT_B,AL
E66C C3         1405          RET                   ; RETURN TO CALLER
E66C           1406  KBD_RESET  ENDP
E66C           1407
E66C           1408
E66C           1409 ; -----
E66C           1409 ;           BLINK LED PROCEDURE FOR MFG BURN-IN AND RUN-IN TESTS ;
E66C           1410 ;           IF LED IS ON, TURN IT OFF. IF OFF, TURN ON. ;
E66C           1411 ; -----
E66D           1412  BLINK_INT  PROC   NEAR
E66D FB         1413          STI
E66E 50         1414          PUSH  AX              ; SAVE AX REG CONTENTS
E66F E461       1415          IN   AL,PORT_B        ; READ CURRENT VAL OF PORT B
E671 8AE0       1416          MOV  AH,AL
E673 F6D0       1417          NOT  AL               ; FLIP ALL BITS
E675 2440       1418          AND  AL,01000000B     ; ISOLATE CONTROL BIT
E677 80E48F     1419          AND  AH,101111111B   ; MASK OUT OF ORIGINAL VAL
E67A 0AC4       1420          OR   AL,AH            ; OR NEW CONTROL BIT IN
E67C E661       1421          OUT  PORT_B,AL
E67E B020       1422          MOV  AL,EDI
E680 E620       1423          OUT  INTA00,AL
E682 58         1424          POP  AX               ; RESTORE AX REG
E683 CF         1425          IRET
E683           1426  BLINK_INT  ENDP
E683           1427
E683           1428 ;----- CHECKSUM AND CALL INIT CODE IN OPTIONAL ROMS
E683           1429
E684           1430  ROM_CHECK  PROC   NEAR
E684 B94000      1431          MOV  AX,DATA          ; SET ES=DATA
E687 8EC0       1432          MOV  ES,AX

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LOC OBJ	LINE	SOURCE			
E689 2AE9	1433	SUB	AH,AH		; ZERO OUT AH
E68B 8A4702	1434	MOV	AL,[BX*2]		; GET LENGTH INDICATOR
E68E B109	1435	MOV	CL,09H		; MULTIPLY BY 512
E690 D3E0	1436	SHL	AX,CL		
E692 8BC8	1437	MOV	CX,AX		; SET COUNT
E694 51	1438	PUSH	CX		
E695 B104	1439	MOV	CL,4		
E697 D3E8	1440	SHR	AX,CL		
E699 03D0	1441	ADD	DX,AX		; SET POINTER TO NEXT MODULE
E69B 59	1442	POP	CX		
	1443				
E69C E8B005	1444	CALL	ROS_CHECKSUM_CNT		; DO CHECKSUM
E69F 7405	1445	JZ	ROM_CHECK_1		
E6A1 E86501	1446	CALL	ROM_ERR		; PRINT ERROR INFO
E6A4 EB13	1447	JMP	SHORT ROM_CHECK_END		
E6A6	1448	ROM_CHECK_1:			
E6A6 52	1449	PUSH	DX		; SAVE POINTER
E6A7 26C70600010300	1450	MOV	ES:IO_ROM_INIT,0003H		; LOAD OFFSET
E6AE 268C1E0201	1451	MOV	ES:IO_ROM_SEG,DS		; LOAD SEGMENT
E6B3 26FF1E0001	1452	CALL	DWORD PTR ES:IO_ROM_INIT		; CALL INIT RTN.
E6B8 5A	1453	POP	DX		
E6B9	1454	ROM_CHECK_END:			
E6B9 C3	1455	RET			
	1456	ROM_CHECK	ENDP		
	1457				
	1458				
	1459				; THIS SUBROUTINE WILL PRINT A MESSAGE ON THE DISPLAY :
	1460				;
	1461				; ENTRY REQUIREMENTS: :
	1462				; SI = OFFSET(ADDRESS) OF MESSAGE BUFFER :
	1463				; CX = MESSAGE BYTE COUNT :
	1464				; MAXIMUM MESSAGE LENGTH IS 36 CHARACTERS :
	1465				;
E6BA	1466	P_MSG	PROC	NEAR	
E6BA E8B118	1467	CALL	DDS		
E6BD 803E120001	1468	CHP	MFG_TST,1		; MFG TEST MODE?
E6C2 7505	1469	JNE	G12		; NO - DISPLAY ERROR MSG
E6C4 B601	1470	MOV	DH,1		; YES - SETUP TO BEEP SPEAKER
E6C6 E906FF	1471	JMP	ERR_BEEP		; YES - BEEP SPEAKER
E6C9	1472	G12:			; WRITE_MSG:
E6C9 2E8A04	1473	MOV	AL,CS:[SI]		; PUT CHAR IN AL
E6CC 46	1474	INC	SI		; POINT TO NEXT CHAR
E6CD 50	1475	PUSH	AX		; SAVE PRINT CHAR
E6CE E865FF	1476	CALL	PRT_HEX		; CALL VIDEO_IO
E6D1 58	1477	POP	AX		; RECOVER PRINT CHAR
E6D2 3C0A	1478	CMP	AL,10		; WAS IT LINE FEED
E6D4 75F3	1479	JNE	G12		; NO,KEEP PRINTING STRING
E6D6 C3	1480	RET			
	1481	P_MSG	ENDP		
	1482				
E6D7 20524F40	1483	F3A	DB	' ROM',13,10	
E6DB 0D					
E6DC 0A					
	1484				
E6DD	1485	D_EOI	PROC	NEAR	
E6DD 50	1486	PUSH	AX		
E6DE B020	1487	MOV	AL,20H		
E6E0 E620	1488	OUT	20H,AL		
E6E2 58	1489	POP	AX		
E6E3 CF	1490	IRET			
	1491	D_EOI	ENDP		
	1492				
	1493				;--- INT 19 -----
	1494				; BOOT STRAP LOADER :
	1495				; IF A 5 1/4" DISKETTE DRIVE IS AVAILABLE ON THE SYSTEM, :
	1496				; TRACK 0, SECTOR 1 IS READ INTO THE BOOT LOCATION :
	1497				; (SEGMENT 0, OFFSET 7C00) AND CONTROL IS TRANSFERRED :
	1498				; THERE. :
	1499				;
	1500				; IF THERE IS NO DISKETTE DRIVE, OR IF THERE IS A :
	1501				; HARDWARE ERROR CONTROL IS TRANSFERRED TO THE RESIDENT :
	1502				; BASIC ENTRY POINT. :
	1503				;
	1504				; IPL ASSUMPTIONS: :
	1505				; 8255 PORT 60H BIT 0 = 1 IF IPL FROM DISKETTE :
	1506				;
	1507				-----
					ASSUME CS:CODE,DS:ABS0



LOC OBJ

LINE SOURCE

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1585 ; OCCURRED. ;
1586 ; (AH)=3 RETURN THE COMMO PORT STATUS IN (AX) ;
1587 ; AH CONTAINS THE LINE STATUS ;
1588 ; BIT 7 = TIME OUT ;
1589 ; BIT 6 = TRANS SHIFT REGISTER EMPTY ;
1590 ; BIT 5 = TRAN HOLDING REGISTER EMPTY ;
1591 ; BIT 4 = BREAK DETECT ;
1592 ; BIT 3 = FRAMING ERROR ;
1593 ; BIT 2 = PARITY ERROR ;
1594 ; BIT 1 = OVERRUN ERROR ;
1595 ; BIT 0 = DATA READY ;
1596 ; AL CONTAINS THE MODEM STATUS ;
1597 ; BIT 7 = RECEIVED LINE SIGNAL DETECT ;
1598 ; BIT 6 = RING INDICATOR ;
1599 ; BIT 5 = DATA SET READY ;
1600 ; BIT 4 = CLEAR TO SEND ;
1601 ; BIT 3 = DELTA RECEIVE LINE SIGNAL DETECT ;
1602 ; BIT 2 = TRAILING EDGE RING DETECTOR ;
1603 ; BIT 1 = DELTA DATA SET READY ;
1604 ; BIT 0 = DELTA CLEAR TO SEND ;
1605 ;
1606 ; (DX) = PARAMETER INDICATING WHICH RS232 CARD (0,1 ALLOWED) ;
1607 ;
1608 ; DATA AREA RS232_BASE CONTAINS THE BASE ADDRESS OF THE 8250 ON THE ;
1609 ; CARD LOCATION 400H CONTAINS UP TO 4 RS232 ADDRESSES POSSIBLE ;
1610 ; DATA AREA LABEL RS232_TIM_OUT (BYTE) CONTAINS OUTER LOOP COUNT ;
1611 ; VALUE FOR TIMEOUT (DEFAULT=1) ;
1612 ; OUTPUT ;
1613 ; AX MODIFIED ACCORDING TO PARMS OF CALL ;
1614 ; ALL OTHERS UNCHANGED ;
1615 ;-----;
1616 ASSUME CS:CODE,DS:DATA
E729 1617 ORG 0E729H
E729 1618 A1 LABEL WORD ; TABLE OF INIT VALUE
E729 1704 1619 DW 1047 ; 110 BAUD
E72B 0003 1620 DW 768 ; 150
E72D 8001 1621 DW 384 ; 300
E72F C000 1622 DW 192 ; 600
E731 6000 1623 DW 96 ; 1200
E733 3000 1624 DW 48 ; 2400
E735 1800 1625 DW 24 ; 4800
E737 0C00 1626 DW 12 ; 9600
1627
E739 1628 RS232_ID PROC FAR
1629
1630 ;----- VECTOR TO APPROPRIATE ROUTINE
1631
E739 FB 1632 STI ; INTERRUPTS BACK ON
E73A 1E 1633 PUSH DS ; SAVE SEGMENT
E73B 52 1634 PUSH DX
E73C 56 1635 PUSH SI
E73D 57 1636 PUSH DI
E73E 51 1637 PUSH CX
E73F 53 1638 PUSH BX
E740 8BF2 1639 MOV SI,DX ; RS232 VALUE TO SI
E742 8BFA 1640 MOV DI,DX
E744 D1E6 1641 SHL SI,1 ; WORD OFFSET
E746 E8F517 1642 CALL DDS
E749 8B14 1643 MOV DX,RS232_BASE[SI] ; GET BASE ADDRESS
E74B 0BD2 1644 OR DX,DX ; TEST FOR 0 BASE ADDRESS
E74D 7413 1645 JZ A3 ; RETURN
E74F 0AE4 1646 OR AH,AH ; TEST FOR (AH)=0
E751 7416 1647 JZ A4 ; COMMUN INIT
E753 FECC 1648 DEC AH ; TEST FOR (AH)=1
E755 7445 1649 JZ A5 ; SEND AL
E757 FECC 1650 DEC AH ; TEST FOR (AH)=2
E759 746A 1651 JZ A12 ; RECEIVE INTO AL
E75B 1652 A2:
E75B FECC 1653 DEC AH ; TEST FOR (AH)=3
E75D 7503 1654 JNZ A3
E75F E98300 1655 JMP A18 ; COMMUNICATION STATUS
E762 1656 A3:
E762 5B 1657 POP BX
E763 59 1658 POP CX
E764 5F 1659 POP DI
E765 5E 1660 POP SI
E766 5A 1661 POP DX

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Appendix A

LOC OBJ	LINE	SOURCE			
E767 1F	1662	POP	DS		
E768 CF	1663	IRET			; RETURN TO CALLER, NO ACTION
	1664				
	1665	;----- INITIALIZE THE COMMUNICATIONS PORT			
	1666				
E769	1667	A4:			
E769 8AE0	1668	MOV	AH,AL		; SAVE INIT PARMS IN AH
E76B 83C203	1669	ADD	DX,3		; POINT TO 8250 CONTROL REGISTER
E76E B0B0	1670	MOV	AL,80H		
E770 EE	1671	OUT	DX,AL		; SET DLAB=1
	1672				
	1673	;----- DETERMINE BAUD RATE DIVISOR			
	1674				
E771 8AD4	1675	MOV	DL,AH		; GET PARMS TO DL
E773 B104	1676	MOV	CL,4		
E775 D2C2	1677	ROL	DL,CL		
E777 81E20E00	1678	AND	DX,0EH		; ISOLATE THEM
E77B BF29E7	1679	MOV	DI,OFFSET A1		; BASE OF TABLE
E77E 03FA	1680	ADD	DI,DX		; PUT INTO INDEX REGISTER
E780 8B14	1681	MOV	DX,RS232_BASE[SI]		; POINT TO HIGH ORDER OF DIVISOR
E782 42	1682	INC	DX		
E783 2E8A4501	1683	MOV	AL,CS:[DI]*1		; GET HIGH ORDER OF DIVISOR
E787 EE	1684	OUT	DX,AL		; SET HS OF DIV TO 0
E788 4A	1685	DEC	DX		
E789 2E8A05	1686	MOV	AL,CS:[DI]		; GET LOW ORDER OF DIVISOR
E78C EE	1687	OUT	DX,AL		; SET LOW OF DIVISOR
E78D 83C203	1688	ADD	DX,3		
E790 8AC4	1689	MOV	AL,AH		; GET PARMS BACK
E792 241F	1690	AND	AL,01FH		; STRIP OFF THE BAUD BITS
E794 EE	1691	OUT	DX,AL		; LINE CONTROL TO 8 BITS
E795 4A	1692	DEC	DX		
E796 4A	1693	DEC	DX		
E797 B000	1694	MOV	AL,0		
E799 EE	1695	OUT	DX,AL		; INTERRUPT ENABLES ALL OFF
E79A EB49	1696	JMP	SHORT A16		; COM_STATUS
	1697				
	1698	;----- SEND CHARACTER IN (AL) OVER COMMO LINE			
	1699				
E79C	1700	A5:			
E79C 50	1701	PUSH	AX		; SAVE CHAR TO SEND
E79D 83C204	1702	ADD	DX,4		; MODEM CONTROL REGISTER
E7A0 B003	1703	MOV	AL,3		; DTR AND RTS
E7A2 EE	1704	OUT	DX,AL		; DATA TERMINAL READY, REQUEST TO SEND
E7A3 42	1705	INC	DX		; MODEM STATUS REGISTER
E7A4 42	1706	INC	DX		
E7A5 B730	1707	MOV	BH,30H		; DATA SET READY & CLEAR TO SEND
E7A7 E84800	1708	CALL	WAIT_FOR_STATUS		; ARE BOTH TRUE
E7AA 7408	1709	JE	A9		; YES, READY TO TRANSMIT CHAR
E7AC	1710	A7:			
E7AC 59	1711	POP	CX		
E7AD 8AC1	1712	MOV	AL,CL		; RELOAD DATA BYTE
E7AF	1713	A8:			
E7AF 80CC80	1714	OR	AH,80H		; INDICATE TIME OUT
E7B2 EBAE	1715	JMP	A3		; RETURN
E7B4	1716	A9:			; CLEAR_TO_SEND
E7B4 4A	1717	DEC	DX		; LINE STATUS REGISTER
E7B5	1718	A10:			; WAIT_SEND
E7B5 B720	1719	MOV	BH,20H		; IS TRANSMITTER READY
E7B7 E83800	1720	CALL	WAIT_FOR_STATUS		; TEST FOR TRANSMITTER READY
E7BA 75F0	1721	JNZ	A7		; RETURN WITH TIME OUT SET
E7BC	1722	A11:			; OUT_CHAR
E7BC 83EA05	1723	SUB	DX,5		; DATA PORT
E7BF 59	1724	POP	CX		; RECOVER IN CX TEMPORARILY
E7C0 8AC1	1725	MOV	AL,CL		; MOVE CHAR TO AL FOR OUT, STATUS IN AH
E7C2 EE	1726	OUT	DX,AL		; OUTPUT CHARACTER
E7C3 EB9D	1727	JMP	A3		; RETURN
	1728				
	1729	;----- RECEIVE CHARACTER FROM COMMO LINE			
	1730				
E7C5	1731	A12:			
E7C5 83C204	1732	ADD	DX,4		; MODEM CONTROL REGISTER
E7C8 B001	1733	MOV	AL,1		; DATA TERMINAL READY
E7CA EE	1734	OUT	DX,AL		
E7CB 42	1735	INC	DX		; MODEM STATUS REGISTER
E7CC 42	1736	INC	DX		
E7CD	1737	A13:			; WAIT_DSR
E7CD B720	1738	MOV	BH,20H		; DATA SET READY

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LOC OBJ          LINE    SOURCE
E7CF E82000      1739      CALL    WAIT_FOR_STATUS      ; TEST FOR DSR
E7D2 75DB        1740      JNZ     A8                    ; RETURN WITH ERROR
E7D4             1741      A15:    WAIT_DSR_END                ; WAIT_DSR_END
E7D4 4A          1742      DEC     DX                    ; LINE STATUS REGISTER
E7D5             1743      A16:    WAIT_RECV                   ; WAIT_RECV
E7D5 B701        1744      MOV     BH,1                  ; RECEIVE BUFFER FULL
E7D7 E81800      1745      CALL    WAIT_FOR_STATUS      ; TEST FOR REC. BUFF. FULL
E7DA 75D3        1746      JNZ     A8                    ; SET TIME OUT ERROR
E7DC             1747      A17:    GET_CHAR                     ; GET_CHAR
E7DC 80E41E      1748      AND     AH,00011110B         ; TEST FOR ERR CONDITIONS ON RECV CHAR
E7DF 8B14        1749      MOV     DX,RS232_BASE[SI]    ; DATA PORT
E7E1 EC          1750      IN      AL,DX                 ; GET CHARACTER FROM LINE
E7E2 E97DFF      1751      JMP     A3                    ; RETURN
1752
1753      I----- COMMO PORT STATUS ROUTINE
1754
E7E5             1755      A18:
E7E5 8B14        1756      MOV     DX,RS232_BASE[SI]
E7E7 83C205      1757      ADD     DX,5                  ; CONTROL PORT
E7EA EC          1758      IN      AL,DX                 ; GET LINE CONTROL STATUS
E7EB 8AE0        1759      MOV     AH,AL                 ; PUT IN AH FOR RETURN
E7ED 42          1760      INC     DX                    ; POINT TO MODEM STATUS REGISTER
E7EE EC          1761      IN      AL,DX                 ; GET MODEM CONTROL STATUS
E7EF E970FF      1762      JMP     A3                    ; RETURN
1763
1764      I-----
1764      ; WAIT FOR STATUS ROUTINE
1765      ;
1766      ; ENTRY:
1767      ;   BH=STATUS BIT(S) TO LOOK FOR,
1768      ;   DX=ADDR. OF STATUS REG
1769      ; EXIT:
1770      ;   ZERO FLAG ON = STATUS FOUND
1771      ;   ZERO FLAG OFF = TIMEOUT.
1772      ;   AH=LAST STATUS READ
1773      I-----
E7F2             1774      WAIT_FOR_STATUS PROC NEAR
E7F2 8A5D7C      1775      MOV     BL,RS232_TIM_OUT[DI] ; LOAD OUTER LOOP COUNT
E7F5             1776      WFS0:
E7F5 2BC9        1777      SUB     CX,CX
E7F7             1778      WFS1:
E7F7 EC          1779      IN      AL,DX                 ; GET STATUS
E7F8 8AE0        1780      MOV     AH,AL                 ; MOVE TO AH
E7FA 22C7        1781      AND     AL,BH                 ; ISOLATE BITS TO TEST
E7FC 3AC7        1782      CMP     AL,BH                 ; EXACTLY = TO MASK
E7FE 7A08        1783      JE      WFS_END               ; RETURN WITH ZERO FLAG ON
E800 E2F5        1784      LOOP   WFS1                  ; TRY AGAIN
E802 FECB        1785      DEC     BL
E804 75EF        1786      JNZ     WFS0
E806 0AFF        1787      OR      BH,BH                 ; SET ZERO FLAG OFF
E808             1788      WFS_END:
E808 C3          1789      RET
1790      WAIT_FOR_STATUS ENDP
1791      RS232_IO      ENDP
1792
1793
1794      I-----
1794      ; PRINT ADDRESS AND ERROR MESSAGE FOR ROM CHECKSUM ERRORS
1795      ;
1796      I-----
E809             1796      ROM_ERR PROC NEAR
E809 52          1797      PUSH   DX                    ; SAVE POINTER
E80A 50          1798      PUSH   AX
E80B 8CDA        1799      MOV     DX,DS                ; GET ADDRESS POINTER
E80D 81FA00CB    1800      CMP     DX,0C800H
E811 7E13        1801      JLE    ROM_ERR_BEEP          ; SPECIAL ERROR INDICATION
E813 8AC6        1802      MOV     AL,DH
E815 E80DFE      1803      CALL   XPC_BYTE              ; DISPLAY ADDRESS
E818 8AC2        1804      MOV     AL,DL
E81A E808FE      1805      CALL   XPC_BYTE              ; DISPLAY ADDRESS
E81D BED7E6      1806      MOV     SI,OFFSET F3A        ; DISPLAY ERROR MSG
E820 E897FE      1807      CALL   P_HSG
E823             1808      ROM_ERR_END:
E823 58          1809      POP     AX
E824 5A          1810      POP     DX
E825 C3          1811      RET
E826             1812      ROM_ERR_BEEP:
E826 BA0201      1813      MOV     DX,0102H            ; BEEP 1 LONG, 2 SHORT
E829 E8A3FD      1814      CALL   ERR_BEEP
E82C EBF5        1815      JMP     SHORT ROM_ERR_END

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```

1816 ROM_ERR ENDP
1817
1818 ;---- INT 16 -----
1819 ; KEYBOARD I/O
1820 ; THESE ROUTINES PROVIDE KEYBOARD SUPPORT
1821 ; INPUT
1822 ; (AH)=0 READ THE NEXT ASCII CHARACTER STRUCK FROM THE KEYBOARD
1823 ; RETURN THE RESULT IN (AL), SCAN CODE IN (AH)
1824 ; (AH)=1 SET THE Z FLAG TO INDICATE IF AN ASCII CHARACTER IS
1825 ; AVAILABLE TO BE READ.
1826 ; (ZF)=1 -- NO CODE AVAILABLE
1827 ; (ZF)=0 -- CODE IS AVAILABLE
1828 ; IF ZF = 0, THE NEXT CHARACTER IN THE BUFFER TO BE READ
1829 ; IS IN AX, AND THE ENTRY REMAINS IN THE BUFFER
1830 ; (AH)=2 RETURN THE CURRENT SHIFT STATUS IN AL REGISTER
1831 ; THE BIT SETTINGS FOR THIS CODE ARE INDICATED IN THE
1832 ; THE EQUATES FOR KB_FLAG
1833 ; OUTPUT
1834 ; AS NOTED ABOVE, ONLY AX AND FLAGS CHANGED
1835 ; ALL REGISTERS PRESERVED
1836 ;-----
1837 ASSUME CS:CODE,DS:DATA
1838 ORG 0E82EH
E82E 1839 KEYBOARD_IO PROC FAR
E82E FB 1840 STI ; INTERRUPTS BACK ON
E82F 1E 1841 PUSH DS ; SAVE CURRENT DS
E830 53 1842 PUSH BX ; SAVE BX TEMPORARILY
E831 E80A17 1843 CALL DDS
E834 0AE4 1844 OR AH,AH ; AH=0
E836 740A 1845 JZ K1 ; ASCII_READ
E838 FECC 1846 DEC AH ; AH=1
E83A 741E 1847 JZ K2 ; ASCII_STATUS
E83C FECC 1848 DEC AH ; AH=2
E83E 742B 1849 JZ K3 ; SHIFT_STATUS
E840 EB2C 1850 JMP SHORT INT10_END ; EXIT
1851
1852 ;---- READ THE KEY TO FIGURE OUT WHAT TO DO
1853
E842 1854 K1: ; ASCII READ
E842 FB 1855 STI ; INTERRUPTS BACK ON DURING LOOP
E843 9D 1856 NOP ; ALLOW AN INTERRUPT TO OCCUR
E844 FA 1857 CLI ; INTERRUPTS BACK OFF
E845 8B1E1A00 1858 MOV BX,BUFFER_HEAD ; GET POINTER TO HEAD OF BUFFER
E849 3B1E1C00 1859 CMP BX,BUFFER_TAIL ; TEST END OF BUFFER
E84D 74F3 1860 JZ K1 ; LOOP UNTIL SOMETHING IN BUFFER
E84F 8B07 1861 MOV AX,[BX] ; GET SCAN CODE AND ASCII CODE
E851 E81D00 1862 CALL K4 ; MOVE POINTER TO NEXT POSITION
E854 891E1A00 1863 MOV BUFFER_HEAD,BX ; STORE VALUE IN VARIABLE
E858 EB14 1864 JMP SHORT INT10_END ; RETURN
1865
1866 ;---- ASCII STATUS
1867
E85A 1868 K2:
E85A FA 1869 CLI ; INTERRUPTS OFF
E85B 8B1E1A00 1870 MOV BX,BUFFER_HEAD ; GET HEAD POINTER
E85F 3B1E1C00 1871 CMP BX,BUFFER_TAIL ; IF EQUAL (Z=1) THEN NOTHING THERE
E863 8B07 1872 MOV AX,[BX]
E865 FB 1873 STI ; INTERRUPTS BACK ON
E866 5B 1874 POP BX ; RECOVER REGISTER
E867 1F 1875 POP DS ; RECOVER SEGMENT
E868 CA0200 1876 RET 2 ; THROW AWAY FLAGS
1877
1878 ;---- SHIFT STATUS
1879
E86B 1880 K3:
E86B A01700 1881 MOV AL,KB_FLAG ; GET THE SHIFT STATUS FLAGS
E86E INT10_END:
E86E 5B 1883 POP BX ; RECOVER REGISTER
E86F 1F 1884 POP DS ; RECOVER REGISTERS
E870 CF 1885 IRET ; RETURN TO CALLER
1886 KEYBOARD_IO ENDP
1887
1888 ;---- INCREMENT A BUFFER POINTER
1889
E871 1890 K4 PROC NEAR
E871 43 1891 INC BX ; MOVE TO NEXT WORD IN LIST
E872 43 1892 INC BX

```

LOC OBJ	LINE	SOURCE		
E873 301E0200	1893	CHP	BX,BUFFER_END	; AT END OF BUFFER?
E877 7504	1894	JNE	K5	; NO, CONTINUE
E879 081E0000	1895	MOV	BX,BUFFER_START	; YES, RESET TO BUFFER BEGINNING
E87D	1896	K5:		
E87D C3	1897	RET		
	1898	K4	ENDP	
	1899			
	1900		;----- TABLE OF SHIFT KEYS AND MASK VALUES	
	1901			
E87E	1902	K6	LABEL	BYTE
E87E 52	1903	DB	INS_KEY	; INSERT KEY
E87F 3A	1904	DB	CAPS_KEY,NUM_KEY,SCROLL_KEY,ALT_KEY,CTL_KEY	
E880 45				
E881 46				
E882 38				
E883 1D				
E884 2A	1905	DB	LEFT_KEY,RIGHT_KEY	
E885 36				
0008	1906	K6L	EQU	\$-K6
	1907			
	1908		;----- SHIFT MASK TABLE	
	1909			
E886	1910	K7	LABEL	BYTE
E886 80	1911	DB	INS_SHIFT	; INSERT MODE SHIFT
E887 40	1912	DB	CAPS_SHIFT,NUM_SHIFT,SCROLL_SHIFT,ALT_SHIFT,CTL_SHIFT	
E888 20				
E889 10				
E88A 08				
E88B 04				
E88C 02	1913	DB	LEFT_SHIFT,RIGHT_SHIFT	
E88D 01				
	1914			
	1915		;----- SCAN CODE TABLES	
	1916			
E88E 1B	1917	K8	DB	27,-1,0,-1,-1,-1,30,-1
E88F FF				
E890 00				
E891 FF				
E892 FF				
E893 FF				
E894 1E				
E895 FF				
E896 FF	1918	DB	-1,-1,-1,31,-1,127,-1,17	
E897 FF				
E898 FF				
E899 1F				
E89A FF				
E89B 7F				
E89C FF				
E89D 11				
E89E 17	1919	DB	23,5,18,20,25,21,9,15	
E89F 05				
E8A0 12				
E8A1 14				
E8A2 19				
E8A3 15				
E8A4 09				
E8A5 0F				
E8A6 10	1920	DB	16,27,29,10,-1,1,19	
E8A7 1B				
E8A8 10				
E8A9 0A				
E8AA FF				
E8AB 01				
E8AC 13				
E8AD 04	1921	DB	4,6,7,8,10,11,12,-1,-1	
E8AE 06				
E8AF 07				
E8B0 08				
E8B1 0A				
E8B2 0B				
E8B3 0C				
E8B4 FF				
E8B5 FF				
E8B6 FF	1922	DB	-1,-1,28,26,24,3,22,2	
E8B7 FF				
E8B8 1C				

LOC OBJ	LINE	SOURCE				
E8B9 1A						
E8BA 18						
E8BB 03						
E8BC 16						
E8BD 02						
E8BE 0E	1923		DB	14,13,-1,-1,-1,-1,-1		
E8BF 0D						
E8C0 FF						
E8C1 FF						
E8C2 FF						
E8C3 FF						
E8C4 FF						
E8C5 FF						
E8C6 20	1924		DB	' ', -1		
E8C7 FF						
E8C8	1925	;----- CTL TABLE SCAN				
E8C8 5E	1926	K9	LABEL	BYTE		
E8C9 5F	1927		DB	94,95,96,97,98,99,100,101		
E8CA 60						
E8CB 61						
E8CC 62						
E8CD 63						
E8CE 64						
E8CF 65						
E8D0 66	1928		DB	102,103,-1,-1,119,-1,132,-1		
E8D1 67						
E8D2 FF						
E8D3 FF						
E8D4 77						
E8D5 FF						
E8D6 84						
E8D7 FF						
E8D8 73	1929		DB	115,-1,116,-1,117,-1,118,-1		
E8D9 FF						
E8DA 74						
E8DB FF						
E8DC 75						
E8DD FF						
E8DE 76						
E8DF FF						
E8E0 FF	1930		DB	-1		
E8E1	1931	;----- LC TABLE				
E8E1 1B	1932	K10	LABEL	BYTE		
E8E2 31323334353637	1933		DB	01BH, '1234567890-' , 08H, 09H		
3839302D3D						
E8EE 08						
E8EF 09						
E8F0 71776572747975	1934		DB	'qwertyuiop[]', 0DH, -1, 'asdfghjkl;', 027H		
696F705B5D						
E8FC 0D						
E8FD FF						
E8FE 6173646667686A						
6B6C3B						
E908 27						
E909 60	1935		DB	60H, -1, 5CH, 'zxcvbnm,.,/ ', -1, 'M', -1, ' '		
E90A FF						
E90B 5C						
E90C 7A786376626E6D						
2C2E2F						
E916 FF						
E917 2A						
E918 FF						
E919 20						
E91A FF	1936		DB	-1		
E91B	1937	;----- UC TABLE				
E91B 1B	1938	K11	LABEL	BYTE		
E91C 21402324	1939		DB	27, '085', 37, 05EH, '&M( )_+', 08H, 0		
E920 25						
E921 5E						
E922 262A28295F2B						
E928 08						
E929 00						
E92A 51574552545955	1940		DB	'QWERTYUIOP[]', 0DH, -1, 'ASDFGHJKL:'''		
494F507B7D						

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LOC OBJ                LINE   SOURCE

E936 0D
E937 FF
E938 415344647484A
      4B4C5A22
E943 7E                1941          DB      07EH,-1,'|ZXCVBHM<>?',-1,0,-1,' ',-1
E944 FF
E945 7C5A504356424E
      4D3C3E3F
E950 FF
E951 00
E952 FF
E953 20
E954 FF

                1942      ;----- UC TABLE SCAN
E955                1943      K12     LABEL  BYTE
                1944          DB      86,85,86,87,88,89,90
E956 55
E957 56
E958 57
E959 58
E95A 59
E95B 5A
E95C 5B                1945          DB      91,92,93
E95D 5C
E95E 5D

                1946      ;----- ALT TABLE SCAN
E95F                1947      K13     LABEL  BYTE
                1948          DB      104,105,106,107,108
E960 69
E961 6A
E962 6B
E963 6C
E964 6D                1949          DB      109,110,111,112,113
E965 6E
E966 6F
E967 70
E968 71

                1950      ;----- MM STATE TABLE
E969                1951      K14     LABEL  BYTE
E969 3738392D343536  1952          DB      '789-456+1230.'
      2B313233302E

                1953      ;----- BASE CASE TABLE
E976                1954      K15     LABEL  BYTE
E976 47                1955          DB      71,72,73,-1,75,-1,77
E977 48
E978 49
E979 FF
E97A 4B
E97B FF
E97C 4D
E97D 4F                1956          DB      -1,79,80,81,82,83
E97E 4F
E97F 50
E980 51
E981 52
E982 53

                1957
E987                1958      ;----- KEYBOARD INTERRUPT ROUTINE
                1959
                1960          ORG      0E987H
E987                1961      KB_INT  PROC  FAR
                1962          STI
                1963          PUSH  AX          ; ALLOW FURTHER INTERRUPTS
E987 FB                1964          PUSH  BX
E988 50                1965          PUSH  CX
E989 53                1966          PUSH  DX
E98A 51                1967          PUSH  SI
E98B 52                1968          PUSH  DI
E98C 56                1969          PUSH  DS
E98D 57                1970          PUSH  ES
E98E 1E                1971          CLD          ; FORWARD DIRECTION
E98F 06                1972          CALL  DDS
E990 FC                1973          IN    AL,KB_DATA  ; READ IN THE CHARACTER
E991 E8AA15           1974          PUSH  AX          ; SAVE IT
E994 E460           1975          IN    AL,KB_CTL   ; GET THE CONTROL PORT
E996 50           1976          MOV  AH,AL        ; SAVE VALUE
E997 E461           1977          OR   AL,80H     ; RESET BIT FOR KEYBOARD
E999 8AE0
E99B 0C80

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LOC OBJ	LINE	SOURCE	
E990 E661	1978	OUT	KB_CTL,AL
E99F 86E0	1979	XCHG	AH,AL ; GET BACK ORIGINAL CONTROL
E9A1 E661	1980	OUT	KB_CTL,AL ; KB HAS BEEN RESET
E9A3 58	1981	POP	AX ; RECOVER SCAN CODE
E9A4 8AE0	1982	MOV	AH,AL ; SAVE SCAN CODE IN AH ALSO
	1983		
	1984	;----- TEST FOR OVERRUN SCAN CODE FROM KEYBOARD	
	1985		
E9A6 3CFF	1986	CMF	AL,0FFH ; IS THIS AN OVERRUN CHAR
E9A8 7503	1987	JNZ	K16 ; NO, TEST FOR SHIFT KEY
E9AA E97A02	1988	JMP	K62 ; BUFFER_FULL_BEEP
	1989		
	1990	;----- TEST FOR SHIFT KEYS	
	1991		
E9AD	1992	K16:	; TEST_SHIFT
E9AD 247F	1993	AND	AL,07FH ; TURN OFF THE BREAK BIT
E9AF 0E	1994	PUSH	CS
E9B0 07	1995	POP	ES ; ESTABLISH ADDRESS OF SHIFT TABLE
E9B1 BF7EE8	1996	MOV	DI,OFFSET K6 ; SHIFT KEY TABLE
E9B6 B90800	1997	MOV	CX,K6L ; LENGTH
E9B7 F2	1998	REPNE	SCASB ; LOOK THROUGH THE TABLE FOR A MATCH
E9B8 AE			
E9B9 8AC4	1999	MOV	AL,AH ; RECOVER SCAN CODE
E9BB 7403	2000	JE	K17 ; JUMP IF MATCH FOUND
E9BD E98500	2001	JMP	K25 ; IF NO MATCH, THEN SHIFT NOT FOUND
	2002		
	2003	;----- SHIFT KEY FOUND	
	2004		
E9CD 81E7FE8	2005	K17: SUB	DI,OFFSET K6+1 ; ADJUST PTR TO SCAN CODE MTCX
E9C4 2E8AAS86E8	2006	MOV	AH,CS+K7(DI) ; GET MASK INTO AH
E9C9 A880	2007	TEST	AL,80H ; TEST FOR BREAK KEY
E9CB 7551	2008	JNZ	K23 ; BREAK_SHIFT_FOUND
	2009		
	2010	;----- SHIFT MAKE FOUND, DETERMINE SET OR TOGGLE	
	2011		
E9CD 80FC10	2012	CMF	AH,SCROLL_SHIFT
E9DD 7307	2013	JAE	K18 ; IF SCROLL SHIFT OR ABOVE, TOGGLE KEY
	2014		
	2015	;----- PLAIN SHIFT KEY, SET SHIFT ON	
	2016		
E9D2 08261700	2017	OR	KB_FLAG,AH ; TURN ON SHIFT BIT
E9D6 E98000	2018	JMP	K26 ; INTERRUPT_RETURN
	2019		
	2020	;----- TOGGLED SHIFT KEY, TEST FOR 1ST MAKE OR NOT	
	2021		
E9D9	2022	K18:	; SHIFT-TOGGLE
E9D9 F606170004	2023	TEST	KB_FLAG, CTL_SHIFT ; CHECK CTL SHIFT STATE
E9DE 7565	2024	JNZ	K25 ; JUMP IF CTL STATE
E9E0 3C52	2025	CMF	AL, INS_KEY ; CHECK FOR INSERT KEY
E9E2 7522	2026	JNZ	K22 ; JUMP IF NOT INSERT KEY
E9E4 F606170008	2027	TEST	KB_FLAG, ALT_SHIFT ; CHECK FOR ALTERNATE SHIFT
E9E9 755A	2028	JNZ	K25 ; JUMP IF ALTERNATE SHIFT
E9EB F606170020	2029	K19: TEST	KB_FLAG, NUM_STATE ; CHECK FOR BASE STATE
E9F0 750D	2030	JNZ	K21 ; JUMP IF NUM LOCK IS ON
E9F2 F606170003	2031	TEST	KB_FLAG, LEFT_SHIFT+ RIGHT_SHIFT
E9F7 740D	2032	JZ	K22 ; JUMP IF BASE STATE
	2033		
E9F9	2034	K20:	; NUMERIC ZERO, NOT INSERT KEY
E9F9 B83052	2035	MOV	AX, 5230H ; PUT OUT AN ASCII ZERO
E9FC E9D601	2036	JMP	K57 ; BUFFER_FILL
E9FF	2037	K21:	; MIGHT BE NUMERIC
E9FF F606170003	2038	TEST	KB_FLAG, LEFT_SHIFT+ RIGHT_SHIFT
EA04 74F3	2039	JZ	K20 ; JUMP NUMERIC, NOT INSERT
	2040		
EA06	2041	K22:	; SHIFT TOGGLE KEY HIT; PROCESS IT
EA06 84261800	2042	TEST	AH,KB_FLAG_1 ; IS KEY ALREADY DEPRESSED
EA0A 754D	2043	JNZ	K26 ; JUMP IF KEY ALREADY DEPRESSED
EA0C 08261800	2044	OR	KB_FLAG_1,AH ; INDICATE THAT THE KEY IS DEPRESSED
EA10 30261700	2045	XOR	KB_FLAG,AH ; TOGGLE THE SHIFT STATE
EA14 3C52	2046	CMF	AL,INS_KEY ; TEST FOR 1ST MAKE OF INSERT KEY
EA16 7541	2047	JNE	K26 ; JUMP IF NOT INSERT KEY
EA18 B80052	2048	MOV	AX,INS_KEY*256 ; SET SCAN CODE INTO AH, 0 INTO AL
EA1B E98701	2049	JMP	K57 ; PUT INTO OUTPUT BUFFER
	2050		
	2051	;----- BREAK SHIFT FOUND	
	2052		
EA1E	2053	K23:	; BREAK-SHIFT-FOUND

LOC OBJ	LINE	SOURCE	
EA1E 80FC10	2054	CHP AH,SCROLL_SHIFT	; IS THIS A TOGGLE KEY
EA21 731A	2055	JAE K24	; YES, HANDLE BREAK TOGGLE
EA23 F6D4	2056	NOT AH	; INVERT MASK
EA25 20261700	2057	AND KB_FLAG,AH	; TURN OFF SHIFT BIT
EA29 3CB8	2058	CHP AL,ALT_KEY+80H	; IS THIS ALTERNATE SHIFT RELEASE
EA2B 752C	2059	JNE K26	; INTERRUPT_RETURN
	2060		
	2061	;----- ALTERNATE SHIFT KEY RELEASED, GET THE VALUE INTO BUFFER	
	2062		
EA2D A01900	2063	MOV AL,ALT_INPUT	
EA30 B400	2064	MOV AH,0	; SCAN CODE OF 0
EA32 88261900	2065	MOV ALT_INPUT,AH	; ZERO OUT THE FIELD
EA36 3C00	2066	CHP AL,0	; WAS THE INPUT=0
EA38 741F	2067	JE K26	; INTERRUPT_RETURN
EA3A E9A101	2068	JMP K58	; IT WASN'T, SO PUT IN BUFFER
EA3D	2069	K24:	; BREAK-TOGGLE
EA3D F6D4	2070	NOT AH	; INVERT MASK
EA3F 20261800	2071	AND KB_FLAG_1,AH	; INDICATE NO LONGER DEPRESSED
EA43 EB14	2072	JMP SHORT K26	; INTERRUPT_RETURN
	2073		
	2074	;----- TEST FOR HOLD STATE	
	2075		
EA45	2076	K25:	; NO-SHIFT-FOUND
EA45 3C80	2077	CHP AL,80H	; TEST FOR BREAK KEY
EA47 7310	2078	JAE K26	; NOTHING FOR BREAK CHARS FROM HERE ON
EA49 F606180008	2079	TEST KB_FLAG_1,HOLD_STATE	; ARE WE IN HOLD STATE
EA4E 7417	2080	JZ K28	; BRANCH AROUND TEST IF NOT
EA50 3C45	2081	CHP AL,NUM_KEY	
EA52 7405	2082	JE K26	; CAN'T END HOLD ON NUM_LOCK
EA54 80261800F7	2083	AND KB_FLAG_1,NOT_HOLD_STATE	; TURN OFF THE HOLD STATE BIT
EA59	2084	K26:	; INTERRUPT_RETURN
EA59 FA	2085	CLI	; TURN OFF INTERRUPTS
EA5A B020	2086	MOV AL,EOI	; END OF INTERRUPT COMMAND
EA5C E620	2087	OUT 020H,AL	; SEND COMMAND TO INT CONTROL PORT
EA5E	2088	K27:	; INTERRUPT_RETURN-NO-EOI
EA5E 07	2089	POP ES	
EA5F 1F	2090	POP DS	
EA60 5F	2091	POP DI	
EA61 5E	2092	POP SI	
EA62 5A	2093	POP DX	
EA63 59	2094	POP CX	
EA64 5B	2095	POP BX	
EA65 5B	2096	POP AX	; RESTORE STATE
EA66 CF	2097	IRET	; RETURN, INTERRUPTS BACK ON
	2098		; WITH FLAG CHANGE
	2099		
	2100	;----- NOT IN HOLD STATE, TEST FOR SPECIAL CHARS	
	2101		
EA67	2102	K28:	; NO-HOLD-STATE
EA67 F606170008	2103	TEST KB_FLAG,ALT_SHIFT	; ARE WE IN ALTERNATE SHIFT
EA6C 7503	2104	JNZ K29	; JUMP IF ALTERNATE SHIFT
EA6E E99100	2105	JMP K38	; JUMP IF NOT ALTERNATE
	2106		
	2107	;----- TEST FOR RESET KEY SEQUENCE (CTL ALT DEL)	
	2108		
EA71	2109	K29:	; TEST-RESET
EA71 F606170004	2110	TEST KB_FLAG,CTL_SHIFT	; ARE WE IN CONTROL SHIFT ALSO
EA76 7433	2111	JZ K31	; NO_RESET
EA78 3C53	2112	CHP AL,DEL_KEY	; SHIFT STATE IS THERE, TEST KEY
EA7A 752F	2113	JNE K31	; NO_RESET
	2114		
	2115	;----- CTL-ALT-DEL HAS BEEN FOUND, DD I/O CLEARUP	
	2116		
EA7C C70672003412	2117	MOV RESET_FLAG,1234H	; SET FLAG FOR RESET FUNCTION
EA82 EASBE000F0	2118	JMP RESET	; JUMP TO POWER ON DIAGNOSTICS
	2119		
	2120	;----- ALT-INPUT-TABLE	
EA87	2121	K30 LABEL BYTE	
EA87 52	2122	DB 82,79,80,81,75,76,77	
EA88 4F			
EA89 50			
EA8A 51			
EA8B 4B			
EA8C 4C			
EA8D 4D			
EA8E 47	2123	DB 71,72,73	; 10 NUMBERS ON KEYPAD
EA8F 48			

LOC OBJ	LINE	SOURCE
EA90 49		
	2124	;----- SUPER-SHIFT-TABLE
EA91 10	2125	DB 16,17,18,19,20,21,22,23 ; A-Z TYPEWRITER CHARS
EA92 11		
EA93 12		
EA94 13		
EA95 14		
EA96 15		
EA97 16		
EA98 17		
EA99 18	2126	DB 24,25,30,31,32,33,34,35
EA9A 19		
EA9B 1E		
EA9C 1F		
EA9D 20		
EA9E 21		
EA9F 22		
EAA0 23		
EAA1 24	2127	DB 36,37,38,44,45,46,47,48
EAA2 25		
EAA3 26		
EAA4 2C		
EAA5 2D		
EAA6 2E		
EAA7 2F		
EAA8 30		
EAA9 31	2128	DB 49,50
EAAA 32		
	2129	
	2130	;----- IN ALTERNATE SHIFT, RESET NOT FOUND
	2131	
EAB8	2132	K31: ; NO-RESET
EAB8 3C39	2133	CHP AL,57 ; TEST FOR SPACE KEY
EAD0 7505	2134	JNE K32 ; NOT THERE
EAAF B020	2135	MOV AL,' ' ; SET SPACE CHAR
EAB1 E92101	2136	JMP K57 ; BUFFER_FILL
	2137	
	2138	;----- LOOK FOR KEY PAD ENTRY
	2139	
EAB4	2140	K32: ; ALT-KEY-PAD
EAB4 BF87EA	2141	MOV DI,OFFSET K30 ; ALT-INPUT-TABLE
EAB7 B90A00	2142	MOV CX,10 ; LOOK FOR ENTRY USING KEYPAD
EABA F2	2143	REPNE SCASB ; LOOK FOR MATCH
EABB AE		
EABC 7512	2144	JNE K33 ; NO_ALT_KEYPAD
EABE 81EF88EA	2145	SUB DI,OFFSET K30+1 ; DI NOW HAS ENTRY VALUE
EAC2 A01900	2146	MOV AL,ALT_INPUT ; GET THE CURRENT BYTE
EAC5 B40A	2147	MOV AH,10 ; MULTIPLY BY 10
EAC7 F6E4	2148	MUL AH
EAC9 03C7	2149	ADD AX,DI ; ADD IN THE LATEST ENTRY
EACB A21900	2150	MOV ALT_INPUT,AL ; STORE IT AWAY
EACE EB89	2151	JMP K26 ; THROW AWAY THAT KEYSTROKE
	2152	
	2153	;----- LOOK FOR SUPERSHIFT ENTRY
	2154	
EAD0	2155	K33: ; NO-ALT-KEYPAD
EAD0 C606190000	2156	MOV ALT_INPUT,0 ; ZERO ANY PREVIOUS ENTRY INTO INPUT
EAD5 B91A00	2157	MOV CX,26 ; DI,ES ALREADY POINTING
EAD8 F2	2158	REPNE SCASB ; LOOK FOR MATCH IN ALPHABET
EAD9 AE		
EADA 7505	2159	JNE K34 ; NOT FOUND, FUNCTION KEY OR OTHER
EADC 8000	2160	MOV AL,0 ; ASCII CODE OF ZERO
EADE E9F400	2161	JMP K57 ; PUT IT IN THE BUFFER
	2162	
	2163	;----- LOOK FOR TOP ROW OF ALTERNATE SHIFT
	2164	
EAE1	2165	K34: ; ALT-TOP-ROW
EAE1 3C02	2166	CHP AL,2 ; KEY WITH '1' ON IT
EAE3 720C	2167	JB K35 ; NOT ONE OF INTERESTING KEYS
EAE5 3C0E	2168	CHP AL,14 ; IS IT IN THE REGION
EAE7 7308	2169	JAE K35 ; ALT-FUNCTION
EAE9 80C476	2170	ADD AH,118 ; CONVERT PSEUDO SCAN CODE TO RANGE
EAEC B000	2171	MOV AL,0 ; INDICATE AS SUCH
EAE8 E9E400	2172	JMP K57 ; BUFFER_FILL
	2173	
	2174	;----- TRANSLATE ALTERNATE SHIFT PSEUDO SCAN CODES
	2175	

LOC OBJ	LINE	SOURCE	
EAF1	2176	K35:	; ALT-FUNCTION
EAF1 3C3B	2177	CMP	AL,59 ; TEST FOR IN TABLE
EAF3 7303	2178	JAE	K37 ; ALT-CONTINUE
EAF5	2179	K36:	; CLOSE-RETURN
EAF5 E961FF	2180	JMP	K26 ; IGNORE THE KEY
EAF6	2181	K37:	; ALT-CONTINUE
EAF8 3C47	2182	CMP	AL,71 ; IN KEYPAD REGION
EAF8 73F9	2183	JAE	K36 ; IF SO, IGNORE
EAF8 BB5FE9	2184	MOV	BX,OFFSET K13 ; ALT SHIFT PSEUDO SCAN TABLE
EAF8 E91B01	2185	JMP	K63 ; TRANSLATE THAT
	2186		
	2187		;----- NOT IN ALTERNATE SHIFT
	2188		
EB02	2189	K38:	; NOT-ALT-SHIFT
EB02 F606170004	2190	TEST	KB_FLAG,CTL_SHIFT ; ARE WE IN CONTROL SHIFT
EB07 7458	2191	JZ	K44 ; NOT-CTL-SHIFT
	2192		
	2193		;----- CONTROL SHIFT, TEST SPECIAL CHARACTERS
	2194		;----- TEST FOR BREAK AND PAUSE KEYS
	2195		
EB09 3C46	2196	CMP	AL,SCROLL_KEY ; TEST FOR BREAK
EB0B 7518	2197	JNE	K39 ; NO-BREAK
EB0D 8B1E8000	2198	MOV	BX,BUFFER_START ; RESET BUFFER TO EMPTY
EB11 891E1A00	2199	MOV	BUFFER_HEAD,BX
EB15 891E1C00	2200	MOV	BUFFER_TAIL,BX
EB19 C606710080	2201	MOV	BIOS_BREAK,80H ; TURN ON BIOS_BREAK BIT
EB1E CD18	2202	INT	1BH ; BREAK INTERRUPT VECTOR
EB20 2BC0	2203	SUB	AX,AX ; PUT OUT DUMMY CHARACTER
EB22 E9B000	2204	JMP	K57 ; BUFFER_FILL
EB25	2205	K39:	; NO-BREAK
EB25 3C45	2206	CMP	AL,NUM_KEY ; LOOK FOR PAUSE KEY
EB27 7521	2207	JNE	K41 ; NO-PAUSE
EB29 800E180000	2208	OR	KB_FLAG_1,HOLD_STATE ; TURN ON THE HOLD FLAG
EB2E B020	2209	MOV	AL,EOI ; END OF INTERRUPT TO CONTROL PORT
EB30 E620	2210	OUT	020H,AL ; ALLOW FURTHER KEYSTROKE INTS
	2211		
	2212		;----- DURING PAUSE INTERVAL, TURN CRT BACK ON
	2213		
EB32 803E490007	2214	CMP	CRT_MODE,7 ; IS THIS BLACK AND WHITE CARD
EB37 7407	2215	JE	K40 ; YES, NOTHING TO DO
EB39 BAD803	2216	MOV	DX,030BH ; PORT FOR COLOR CARD
EB3C A06500	2217	MOV	AL,CRT_MODE_SET ; GET THE VALUE OF THE CURRENT MODE
EB3F EE	2218	OUT	DX,AL ; SET THE CRT MODE, SO THAT CRT IS ON
EB40	2219	K40:	; PAUSE-LOOP
EB40 F606180000	2220	TEST	KB_FLAG_1,HOLD_STATE
EB45 75F9	2221	JNZ	K40 ; LOOP UNTIL FLAG TURNED OFF
EB47 E914FF	2222	JMP	K27 ; INTERRUPT_RETURN_NO_EOI
EB4A	2223	K41:	; NO-PAUSE
	2224		
	2225		;----- TEST SPECIAL CASE KEY 55
	2226		
EB4A 3C37	2227	CMP	AL,55
EB4C 7506	2228	JNE	K42 ; NOT-KEY-55
EB4E B80072	2229	MOV	AX,114*256 ; START/STOP PRINTING SWITCH
EB51 E98100	2230	JMP	K57 ; BUFFER_FILL
	2231		
	2232		;----- SET UP TO TRANSLATE CONTROL SHIFT
	2233		
EB54	2234	K42:	; NOT-KEY-55
EB54 BB8EE8	2235	MOV	BX,OFFSET K8 ; SET UP TO TRANSLATE CTL
EB57 3C3B	2236	CMP	AL,59 ; IS IT IN TABLE
	2237		; CTL-TABLE-TRANSLATE
EB59 7276	2238	JB	K56 ; YES, GO TRANSLATE CHAR
EB5B	2239	K43:	; CTL-TABLE-TRANSLATE
EB5B BBC8E8	2240	MOV	BX,OFFSET K9 ; CTL TABLE SCAN
EB5E E9BC00	2241	JMP	K63 ; TRANSLATE_SCAN
	2242		
	2243		;----- NOT IN CONTROL SHIFT
	2244		
EB61	2245	K44:	; NOT-CTL-SHIFT
EB61 3C47	2246	CMP	AL,71 ; TEST FOR KEYPAD REGION
EB63 732C	2247	JAE	K48 ; HANDLE KEYPAD REGION
EB65 F606170003	2248	TEST	KB_FLAG,LEFT_SHIFT+RIGHT_SHIFT
EB6A 745A	2249	JZ	K54 ; TEST FOR SHIFT STATE
	2250		
	2251		;----- UPPER CASE, HANDLE SPECIAL CASES
	2252		

LOC OBJ	LINE	SOURCE
EB6C 3C0F	2253	CHP AL,15 ; BACK TAB KEY
EB6E 7505	2254	JNE K45 ; NOT-BACK-TAB
EB70 B8000F	2255	MOV AX,15*256 ; SET PSEUDO SCAN CODE
EB73 EB60	2256	JMP SHORT K57 ; BUFFER_FILL
EB75	2257	K45: ; NOT-BACK-TAB
EB75 3C37	2258	CHP AL,55 ; PRINT SCREEN KEY
EB77 7509	2259	JNE K46 ; NOT-PRINT-SCREEN
	2260	
	2261	;----- ISSUE INTERRUPT TO INDICATE PRINT SCREEN FUNCTION
	2262	
EB79 B020	2263	MOV AL,EOI ; END OF CURRENT INTERRUPT
EB7B E620	2264	OUT 020H,AL ; SO FURTHER THINGS CAN HAPPEN
EB7D C005	2265	INT 5H ; ISSUE PRINT SCREEN INTERRUPT
EB7F E90CFE	2266	JMP K27 ; GO BACK WITHOUT EOI OCCURRING
EB82	2267	K46: ; NOT-PRINT-SCREEN
EB82 3C3B	2268	CHP AL,59 ; FUNCTION KEYS
EB84 7206	2269	JB K47 ; NOT-UPPER-FUNCTION
EB86 B855E9	2270	MOV BX,OFFSET K12 ; UPPER CASE PSEUDO SCAN CODES
EB89 E99100	2271	JMP K63 ; TRANSLATE_SCAN
EB8C	2272	K47: ; NOT-UPPER-FUNCTION
EB8C BB1BE9	2273	MOV BX,OFFSET K11 ; POINT TO UPPER CASE TABLE
EB8F EB40	2274	JMP SHORT K56 ; OK, TRANSLATE THE CHAR
	2275	
	2276	;----- KEYPAD KEYS, MUST TEST NUM LOCK FOR DETERMINATION
	2277	
EB91	2278	K48: ; KEYPAD-REGION
EB91 F606170020	2279	TEST KB_FLAG,NUM_STATE ; ARE WE IN NUM_LOCK
EB96 7520	2280	JNZ K52 ; TEST FOR SURE
EB98 F606170003	2281	TEST KB_FLAG,LEFT_SHIFT+RIGHT_SHIFT ; ARE WE IN SHIFT STATE
EB9D 7520	2282	JNZ K53 ; IF SHIFTED, REALLY NUM STATE
	2283	
	2284	;----- BASE CASE FOR KEYPAD
	2285	
EB9F	2286	K49: ; BASE-CASE
EB9F 3C4A	2287	CHP AL,74 ; SPECIAL CASE FOR A COUPLE OF KEYS
EBA1 740B	2288	JE K50 ; MINUS
EBA3 3C4E	2289	CHP AL,78
EBA5 740C	2290	JE K51
EBA7 2C47	2291	SUB AL,71 ; CONVERT ORIGIN
EBA9 B876E9	2292	MOV BX,OFFSET K15 ; BASE CASE TABLE
EBAE EB71	2293	JMP SHORT K64 ; CONVERT TO PSEUDO SCAN
EBAE	2294	K50: ;
EBAE B82D4A	2295	MOV AX,74*256+'-' ; MINUS
EBB1 EB22	2296	JMP SHORT K57 ; BUFFER_FILL
EBB3	2297	K51: ;
EBB3 B82B4E	2298	MOV AX,78*256+'+' ; PLUS
EBB6 EB1D	2299	JMP SHORT K57 ; BUFFER_FILL
	2300	
	2301	;----- MIGHT BE NUM LOCK, TEST SHIFT STATUS
	2302	
EBB8	2303	K52: ; ALMOST-NUM-STATE
EBB8 F606170003	2304	TEST KB_FLAG,LEFT_SHIFT+RIGHT_SHIFT
EBBD 75E0	2305	JNZ K49 ; SHIFTED TEMP OUT OF NUM STATE
EBBF	2306	K53: ; REALLY_NUM_STATE
EBBF 2C46	2307	SUB AL,70 ; CONVERT ORIGIN
EBC1 B869E9	2308	MOV BX,OFFSET K14 ; NUM STATE TABLE
EBC4 E80B	2309	JMP SHORT K56 ; TRANSLATE_CHAR
	2310	
	2311	;----- PLAIN OLD LOWER CASE
	2312	
EBC6	2313	K54: ; NOT-SHIFT
EBC6 3C3B	2314	CHP AL,59 ; TEST FOR FUNCTION KEYS
EBC8 7204	2315	JB K55 ; NOT-LOWER-FUNCTION
EBCA B000	2316	MOV AL,0 ; SCAN CODE IN AH ALREADY
EBCD EB07	2317	JMP SHORT K57 ; BUFFER_FILL
EBCD	2318	K55: ; NOT-LOWER-FUNCTION
EBCD BBE1E8	2319	MOV BX,OFFSET K10 ; LC TABLE
	2320	
	2321	;----- TRANSLATE THE CHARACTER
	2322	
EBD1	2323	K56: ; TRANSLATE-CHAR
EBD1 FEC8	2324	DEC AL ; CONVERT ORIGIN
EBD3 2E07	2325	XLAT CS:K11 ; CONVERT THE SCAN CODE TO ASCII
	2326	
	2327	;----- PUT CHARACTER INTO BUFFER
	2328	
EBD5	2329	K57: ; BUFFER-FILL

LOC OBJ	LINE	SOURCE	
EBD5 3CFF	2330	CMP AL,-1	; IS THIS AN IGNORE CHAR
EBD7 741F	2331	JE K59	; YES, DO NOTHING WITH IT
EBD9 80FCFF	2332	CMP AH,-1	; LOOK FOR -1 PSEUDO SCAN
EBDC 741A	2333	JE K59	; NEAR_INTERRUPT_RETURN
	2334		
	2335	;----- HANDLE THE CAPS LOCK PROBLEM	
	2336		
EBDE	2337	K58:	; BUFFER-FILL-NOTEST
EBDE F606170040	2338	TEST KB_FLAG,CAPS_STATE	; ARE WE IN CAPS LOCK STATE
EBE3 7420	2339	JZ K61	; SKIP IF NOT
	2340		
	2341	;----- IN CAPS LOCK STATE	
	2342		
EBE5 F606170003	2343	TEST KB_FLAG,LEFT_SHIFT+RIGHT_SHIFT	; TEST FOR SHIFT STATE
EBEA 740F	2344	JZ K60	; IF NOT SHIFT, CONVERT LOWER TO UPPER
	2345		
	2346	;----- CONVERT ANY UPPER CASE TO LOWER CASE	
	2347		
EBEC 3C41	2348	CMP AL,'A'	; FIND OUT IF ALPHABETIC
EBEE 7215	2349	JB K61	; NOT_CAPS_STATE
EBF0 3C5A	2350	CMP AL,'Z'	
EBF2 7711	2351	JA K61	; NOT_CAPS_STATE
EBF4 0420	2352	ADD AL,'a'-'A'	; CONVERT TO LOWER CASE
EBF6 EB0D	2353	JMP SHORT K61	; NOT_CAPS_STATE
EBF8	2354	K59:	; NEAR_INTERRUPT_RETURN
EBF8 E95EFE	2355	JMP K26	; INTERRUPT_RETURN
	2356		
	2357	;----- CONVERT ANY LOWER CASE TO UPPER CASE	
	2358		
EBFB	2359	K60:	; LOWER-TO-UPPER
EBFB 3C61	2360	CMP AL,'a'	; FIND OUT IF ALPHABETIC
EBFD 7206	2361	JB K61	; NOT_CAPS_STATE
EBFF 3C7A	2362	CMP AL,'z'	
EC01 7702	2363	JA K61	; NOT_CAPS_STATE
EC03 EC20	2364	SUB AL,'a'-'A'	; CONVERT TO UPPER CASE
EC05	2365	K61:	; NOT-CAPS_STATE
EC05 8B1E1C00	2366	MOV BX,BUFFER_TAIL	; GET THE END POINTER TO THE BUFFER
EC09 8BF3	2367	MOV SI,BX	; SAVE THE VALUE
EC0B E83FC	2368	CALL K4	; ADVANCE THE TAIL
EC0E 3D1E1A00	2369	CMP BX,BUFFER_HEAD	; HAS THE BUFFER WRAPPED AROUND
EC12 7413	2370	JE K62	; BUFFER_FULL_BEEP
EC14 8904	2371	MOV [SI],AX	; STORE THE VALUE
EC16 891E1C00	2372	MOV BUFFER_TAIL,BX	; MOVE THE POINTER UP
EC1A E93CFE	2373	JMP K26	; INTERRUPT_RETURN
	2374		
	2375	;----- TRANSLATE SCAN FOR PSEUDO SCAN CODES	
	2376		
EC1D	2377	K63:	; TRANSLATE-SCAN
EC1D 2C3B	2378	SUB AL,59	; CONVERT ORIGIN TO FUNCTION KEYS
EC1F	2379	K64:	; TRANSLATE-SCAN-ORGD
EC1F 2E07	2380	XLAT CS:K9	; CTL TABLE SCAN
EC21 8AE0	2381	MOV AH,AL	; NUMBER OF CYCLES FOR 1/12 SECOND TONE
EC23 B000	2382	MOV AL,0	; ZERO ASCII CODE
EC25 EBAE	2383	JMP K57	; PUT IT INTO THE BUFFER
	2384		
	2385	KB_INT ENDP	
	2386		
	2387	;----- BUFFER IS FULL, SOUND THE BEEPER	
	2388		
EC27	2389	K62:	; BUFFER-FULL-BEEP
EC27 B020	2390	MOV AL,EOI	; END OF INTERRUPT COMMAND
EC29 E620	2391	OUT 20H,AL	; SEND COMMAND TO INT CONTROL PORT
EC2B BB8000	2392	MOV BX,080H	; NUMBER OF CYCLES FOR 1/12 SECOND TONE
EC2E E461	2393	IN AL,KB_CTL	; GET CONTROL INFORMATION
EC30 50	2394	PUSH AX	; SAVE
EC31	2395	K65:	; BEEP-CYCLE
EC31 24FC	2396	AND AL,0FCH	; TURN OFF TIMER GATE AND SPEAKER DATA
EC33 E661	2397	OUT KB_CTL,AL	; OUTPUT TO CONTROL
EC35 B94800	2398	MOV CX,48H	; HALF CYCLE TIME FOR TONE
EC38	2399	K66:	
EC38 E2FE	2400	LOOP K66	; SPEAKER OFF
EC3A 0C02	2401	OR AL,2	; TURN ON SPEAKER BIT
EC3C E661	2402	OUT KB_CTL,AL	; OUTPUT TO CONTROL
EC3E B94800	2403	MOV CX,48H	; SET UP COUNT
EC41	2404	K67:	
EC41 E2FE	2405	LOOP K67	; ANOTHER HALF CYCLE
EC43 4B	2406	DEC BX	; TOTAL TIME COUNT

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LOC OBJ          LINE    SOURCE
EC44 75EB       2407          JNZ    K65                ; DO ANOTHER CYCLE
EC46 58         2408          POP    AX                ; RECOVER CONTROL
EC47 E661       2409          OUT    KB_CTL,AL        ; OUTPUT THE CONTROL
EC49 E912FE     2410          JMP    K27
2411          ;-----
2412          ;   ROS CHECKSUM SUBROUTINE   :
2413          ;-----
EC4C           2414  ROS_CHECKSUM  PROC  NEAR    ; NEXT_ROS_MODULE
EC4C B90020     2415          MOV    CX,8192          ; NUMBER OF BYTES TO ADD
EC4F           2416  ROS_CHECKSUM_CNT:      ; ENTRY FOR OPTIONAL ROS TEST
EC4F 32C0       2417          XOR    AL,AL
EC51           2418  C26:
EC51 0207       2419          ADD    AL,DS:[BX]
EC53 43        2420          INC    BX                ; POINT TO NEXT BYTE
EC54 E2FB       2421          LOOP  C26              ; ADD ALL BYTES IN ROS MODULE
EC56 0A0C       2422          OR     AL,AL           ; SUM = 0?
EC58 C3        2423          RET
2424  ROS_CHECKSUM  ENDP
2425
2426          ;-- INT 13 -----
2427          ; DISKETTE I/O :
2428          ; THIS INTERFACE PROVIDES ACCESS TO THE 5 1/4" DISKETTE DRIVES :
2429          ; INPUT :
2430          ; (AH)=0 RESET DISKETTE SYSTEM :
2431          ; HARD RESET TO NEC, PREPARE COMMAND, RECAL REQUIRED :
2432          ; ON ALL DRIVES :
2433          ; (AH)=1 READ THE STATUS OF THE SYSTEM INTO (AL) :
2434          ; DISKETTE_STATUS FROM LAST OPERATION IS USED :
2435          ;
2436          ; REGISTERS FOR READ/WRITE/VERIFY/FORMAT :
2437          ; (DL) - DRIVE NUMBER (0-3 ALLOWED, VALUE CHECKED) :
2438          ; (DH) - HEAD NUMBER (0-1 ALLOWED, NOT VALUE CHECKED) :
2439          ; (CH) - TRACK NUMBER (0-39, NOT VALUE CHECKED) :
2440          ; (CL) - SECTOR NUMBER (1-8, NOT VALUE CHECKED, :
2441          ; NOT USED FOR FORMAT) :
2442          ; (AL) - NUMBER OF SECTORS ( MAX = 8, NOT VALUE CHECKED, NOT USED :
2443          ; FOR FORMAT) :
2444          ; (ES:BX) - ADDRESS OF BUFFER ( NOT REQUIRED FOR VERIFY) :
2445          ;
2446          ; (AH)=2 READ THE DESIRED SECTORS INTO MEMORY :
2447          ; (AH)=3 WRITE THE DESIRED SECTORS FROM MEMORY :
2448          ; (AH)=4 VERIFY THE DESIRED SECTORS :
2449          ; (AH)=5 FORMAT THE DESIRED TRACK :
2450          ; FOR THE FORMAT OPERATION, THE BUFFER POINTER (ES,BX) :
2451          ; MUST POINT TO THE COLLECTION OF DESIRED ADDRESS FIELDS :
2452          ; FOR THE TRACK. EACH FIELD IS COMPOSED OF 4 BYTES, :
2453          ; (C,H,R,N), WHERE C = TRACK NUMBER, H=HEAD NUMBER, :
2454          ; R = SECTOR NUMBER, N= NUMBER OF BYTES PER SECTOR :
2455          ; (00=128, 01=256, 02=512, 03=1024). THERE MUST BE ONE :
2456          ; ENTRY FOR EVERY SECTOR ON THE TRACK. THIS INFORMATION :
2457          ; IS USED TO FIND THE REQUESTED SECTOR DURING READ/WRITE :
2458          ; ACCESS. :
2459          ;
2460          ; DATA VARIABLE -- DISK_POINTER :
2461          ; DOUBLE WORD POINTER TO THE CURRENT SET OF DISKETTE PARAMETERS :
2462          ; OUTPUT :
2463          ; AH = STATUS OF OPERATION :
2464          ; STATUS BITS ARE DEFINED IN THE EQUATES FOR :
2465          ; DISKETTE_STATUS VARIABLE IN THE DATA SEGMENT OF THIS :
2466          ; MODULE. :
2467          ; CY = 0 SUCCESSFUL OPERATION (AH=0 ON RETURN) :
2468          ; CY = 1 FAILED OPERATION (AH HAS ERROR REASON) :
2469          ; FOR READ/WRITE/VERIFY :
2470          ; DS,BX,DX,CH,CL PRESERVED :
2471          ; AL = NUMBER OF SECTORS ACTUALLY READ :
2472          ; ***** AL MAY NOT BE CORRECT IF TIME OUT ERROR OCCURS :
2473          ; NOTE: IF AN ERROR IS REPORTED BY THE DISKETTE CODE, THE :
2474          ; APPROPRIATE ACTION IS TO RESET THE DISKETTE, THEN RETRY :
2475          ; THE OPERATION. ON READ ACCESSES, NO MOTOR START DELAY :
2476          ; IS TAKEN, SO THAT THREE RETRIES ARE REQUIRED ON READS :
2477          ; TO ENSURE THAT THE PROBLEM IS NOT DUE TO MOTOR :
2478          ; START-UP. :
2479          ;-----
2480          ASSUME  CS:CODE,DS:DATA,ES:DATA
2481          ORG    0EC59H
EC59           2482  DISKETTE_IO  PROC  FAR
EC59 FB        2483          STI                ; INTERRUPTS BACK ON

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LOC OBJ	LINE	SOURCE			
EC5A 53	2484	PUSH	BX		; SAVE ADDRESS
EC5B 51	2485	PUSH	CX		
EC5C 1E	2486	PUSH	DS		; SAVE SEGMENT REGISTER VALUE
EC5D 56	2487	PUSH	SI		; SAVE ALL REGISTERS DURING OPERATION
EC5E 57	2488	PUSH	DI		
EC5F 55	2489	PUSH	BP		
EC60 52	2490	PUSH	DX		
EC61 8BEC	2491	MOV	BP,SP		; SET UP POINTER TO HEAD PARM
EC63 E8D812	2492	CALL	DDS		
EC66 E81C00	2493	CALL	J1		; CALL THE REST TO ENSURE DS RESTORED
EC69 8B0400	2494	MOV	BX,4		; GET THE MOTOR WAIT PARAMETER
EC6C E8FD01	2495	CALL	GET_PARM		
EC6F 80264000	2496	MOV	MOTOR_COUNT,AH		; SET THE TIMER COUNT FOR THE MOTOR
EC73 8A264100	2497	MOV	AH,DISKETTE_STATUS		; GET STATUS OF OPERATION
EC77 80FC01	2498	CMP	AH,1		; SET THE CARRY FLAG TO INDICATE
EC7A F5	2499	CHC			; SUCCESS OR FAILURE
EC7B 5A	2500	POP	DX		; RESTORE ALL REGISTERS
EC7C 5D	2501	POP	BP		
EC7D 5F	2502	POP	DI		
EC7E 5E	2503	POP	SI		
EC7F 1F	2504	POP	DS		
EC80 59	2505	POP	CX		
EC81 5B	2506	POP	BX		; RECOVER ADDRESS
EC82 CA0200	2507	RET	2		; THROW AWAY SAVED FLAGS
	2508	DISKETTE_IO	ENDP		
	2509				
EC85	2510	J1	PROC	NEAR	
EC85 8AF0	2511	MOV	DH,AL		; SAVE # SECTORS IN DH
EC87 80263F007F	2512	AND	MOTOR_STATUS,07FH		; INDICATE A READ OPERATION
EC8C 0AE4	2513	OR	AH,AH		; AH=0
EC8E 7427	2514	JZ	DISK_RESET		
EC90 FECC	2515	DEC	AH		; AH=1
EC92 7473	2516	JZ	DISK_STATUS		
EC94 C606410000	2517	MOV	DISKETTE_STATUS,0		; RESET THE STATUS INDICATOR
EC99 80FA04	2518	CMP	DL,4		; TEST FOR DRIVE IN 0-3 RANGE
EC9C 7313	2519	JAE	J3		; ERROR IF ABOVE
EC9E FECC	2520	DEC	AH		; AH=2
ECA0 7469	2521	JZ	DISK_READ		
ECA2 FECC	2522	DEC	AH		; AH=3
ECA4 7503	2523	JNZ	J2		; TEST_DISK_VERF
ECA6 E99500	2524	JMP	DISK_WRITE		
ECA9	2525	J2:			; TEST_DISK_VERF
ECA9 FECC	2526	DEC	AH		; AH=4
ECAB 7467	2527	JZ	DISK_VERF		
ECAD FECC	2528	DEC	AH		; AH=5
ECAF 7467	2529	JZ	DISK_FORMAT		
ECB1	2530	J3:			; BAD_COMMAND
ECB1 C606410001	2531	MOV	DISKETTE_STATUS,BAD_CMD		; ERROR CODE, NO SECTORS TRANSFERRED
ECB6 C3	2532	RET			; UNDEFINED OPERATION
	2533	J1	ENDP		
	2534				
	2535				;----- RESET THE DISKETTE SYSTEM
	2536				
ECB7	2537	DISK_RESET	PROC	NEAR	
ECB7 BAF203	2538	MOV	DX,03F2H		; ADAPTER CONTROL PORT
ECBA FA	2539	CLI			; NO INTERRUPTS
ECBB A03F00	2540	MOV	AL,MOTOR_STATUS		; WHICH MOTOR IS ON
ECBE B104	2541	MOV	CL,4		; SHIFT COUNT
ECC0 D2E0	2542	SAL	AL,CL		; MOVE MOTOR VALUE TO HIGH NYBBLE
ECC2 A820	2543	TEST	AL, 20H		; SELECT CORRESPONDING DRIVE
ECC4 750C	2544	JNZ	J5		; JUMP IF MOTOR ONE IS ON
ECC6 A840	2545	TEST	AL, 40H		
ECC8 7506	2546	JNZ	J4		; JUMP IF MOTOR TWO IS ON
ECCA A880	2547	TEST	AL, 80H		
ECCC 7406	2548	JZ	J6		; JUMP IF MOTOR ZERO IS ON
ECCE FECC	2549	INC	AL		
ECD0	2550	J4:			
ECD0 FECC	2551	INC	AL		
ECD2	2552	J5:			
ECD2 FECC	2553	INC	AL		
ECD4	2554	J6:			
ECD4 0C08	2555	OR	AL,8		; TURN ON INTERRUPT ENABLE
ECD6 EE	2556	OUT	DX,AL		; RESET THE ADAPTER
ECD7 C6063E0000	2557	MOV	SEEK_STATUS,0		; SET RECAL REQUIRED ON ALL DRIVES
ECDC C606410000	2558	MOV	DISKETTE_STATUS,0		; SET OK STATUS FOR DISKETTE
ECE1 0C04	2559	OR	AL,4		; TURN OFF RESET
ECE3 EE	2560	OUT	DX,AL		; TURN OFF THE RESET

LOC OBJ	LINE	SOURCE	
ECE4 FB	2561	STI	; REENABLE THE INTERRUPTS
ECE5 E82A02	2562	CALL	CHK_STAT_2 ; DO SENSE INTERRUPT STATUS
	2563		; FOLLOWING RESET
ECE8 A04200	2564	MOV	AL,NEC_STATUS ; IGNORE ERROR RETURN AND DO OWN TEST
ECEB 3CC0	2565	CHP	AL,0C0H ; TEST FOR DRIVE READY TRANSITION
ECED 7406	2566	JZ	J7 ; EVERYTHING OK
ECEF 80DE410020	2567	OR	DISKETTE_STATUS,BAD_NEC ; SET ERROR CODE
ECF4 C3	2568	RET	
	2569		
	2570		;----- SEND SPECIFY COMMAND TO NEC
	2571		
ECF5	2572	J7:	; DRIVE_READY
ECF5 B403	2573	MOV	AH,03H ; SPECIFY COMMAND
ECF7 E84701	2574	CALL	NEC_OUTPUT ; OUTPUT THE COMMAND
ECFA BB0100	2575	MOV	BX,1 ; FIRST BYTE PARM IN BLOCK
ECFD E86C01	2576	CALL	GET_PARM ; TO THE NEC CONTROLLER
ED00 BB0300	2577	MOV	BX,3 ; SECOND BYTE PARM IN BLOCK
ED03 E86601	2578	CALL	GET_PARM ; TO THE NEC CONTROLLER
ED06	2579	J8:	; RESET_RET
ED06 C3	2580	RET	; RETURN TO CALLER
	2581	DISK_RESET	ENDP
	2582		
	2583		;----- DISKETTE STATUS ROUTINE
	2584		
ED07	2585	DISK_STATUS	PROC NEAR
ED07 A04100	2586	MOV	AL,DISKETTE_STATUS
ED0A C3	2587	RET	
	2588	DISK_STATUS	ENDP
	2589		
	2590		;----- DISKETTE READ
	2591		
ED0B	2592	DISK_READ	PROC NEAR
ED0B B046	2593	MOV	AL,046H ; READ COMMAND FOR DMA
ED0D	2594	J9:	; DISK_READ_CONT
ED0D E8B801	2595	CALL	DMA_SETUP ; SET UP THE DMA
ED10 B4E6	2596	MOV	AH,0E6H ; SET UP RD COMMAND FOR NEC CONTROLLER
ED12 EB36	2597	JMP	SHORT RM_OPN ; GO DO THE OPERATION
	2598	DISK_READ	ENDP
	2599		
	2600		;----- DISKETTE VERIFY
	2601		
ED14	2602	DISK_VERF	PROC NEAR
ED14 B042	2603	MOV	AL,042H ; VERIFY COMMAND FOR DMA
ED16 EBF5	2604	JMP	J9 ; DO AS IF DISK READ
	2605	DISK_VERF	ENDP
	2606		
	2607		;----- DISKETTE FORMAT
	2608		
ED18	2609	DISK_FORMAT	PROC NEAR
ED18 800E3F0080	2610	OR	MOTOR_STATUS,80H ; INDICATE WRITE OPERATION
ED1D B04A	2611	MOV	AL,04AH ; WILL WRITE TO THE DISKETTE
ED1F E8A601	2612	CALL	DMA_SETUP ; SET UP THE DMA
ED22 B44D	2613	MOV	AH,04DH ; ESTABLISH THE FORMAT COMMAND
ED24 EB24	2614	JMP	SHORT RM_OPN ; DO THE OPERATION
ED26	2615	J10:	; CONTINUATION OF RM_OPN FOR FMT
ED26 BB0700	2616	MOV	BX,7 ; GET THE
ED29 E84001	2617	CALL	GET_PARM ; BYTES/SECTOR VALUE TO NEC
ED2C BB0900	2618	MOV	BX,9 ; GET THE
ED2F E83A01	2619	CALL	GET_PARM ; SECTORS/TRACK VALUE TO NEC
ED32 BB0F00	2620	MOV	BX,15 ; GET THE
ED35 E83401	2621	CALL	GET_PARM ; GAP LENGTH VALUE TO NEC
ED38 BB1100	2622	MOV	BX,17 ; GET THE FILLER BYTE
ED3B E9AB00	2623	JMP	J16 ; TO THE CONTROLLER
	2624	DISK_FORMAT	ENDP
	2625		
	2626		;----- DISKETTE WRITE ROUTINE
	2627		
ED3E	2628	DISK_WRITE	PROC NEAR
ED3E 800E3F0080	2629	OR	MOTOR_STATUS,80H ; INDICATE WRITE OPERATION
ED43 B04A	2630	MOV	AL,04AH ; DMA WRITE COMMAND
ED45 E88001	2631	CALL	DMA_SETUP
ED48 B4C5	2632	MOV	AH,0C5H ; NEC COMMAND TO WRITE TO DISKETTE
	2633	DISK_WRITE	ENDP
	2634		
	2635		;----- ALLOW WRITE ROUTINE TO FALL INTO RM_OPN
	2636		
	2637		-----

LOC OBJ	LINE	SOURCE	
	2638	; RM_OPN	:
	2639	; THIS ROUTINE PERFORMS THE READ/WRITE/VERIFY OPERATION	:
	2640	;	:
ED4A	2641	RM_OPN PROC NEAR	
ED4A 7308	2642	JNC J11	; TEST FOR DMA ERROR
ED4C C606410009	2643	MOV DISKETTE_STATUS,DMA_BOUNDARY	; SET ERROR
ED51 B000	2644	MOV AL,0	; NO SECTORS TRANSFERRED
ED53 C3	2645	RET	; RETURN TO MAIN ROUTINE
ED54	2646	J11:	; DO_RM_OPN
ED54 50	2647	PUSH AX	; SAVE THE COMMAND
	2648		
	2649	;----- TURN ON THE MOTOR AND SELECT THE DRIVE	
	2650		
ED55 51	2651	PUSH CX	; SAVE THE T/S PARMS
ED56 8ACA	2652	MOV CL,DL	; GET DRIVE NUMBER AS SHIFT COUNT
ED58 B001	2653	MOV AL,1	; MASK FOR DETERMINING MOTOR BIT
ED5A D2E0	2654	SAL AL,CL	; SHIFT THE MASK BIT
ED5C FA	2655	CLI	; NO INTERRUPTS WHILE DETERMINING
	2656		; MOTOR STATUS
ED5D C6064000FF	2657	MOV MOTOR_COUNT,0FFH	; SET LARGE COUNT DURING OPERATION
ED62 84063F00	2658	TEST AL,MOTOR_STATUS	; TEST THAT MOTOR FOR OPERATING
ED66 7531	2659	JNZ J14	; IF RUNNING, SKIP THE WAIT
ED68 80263F00F0	2660	AND MOTOR_STATUS,0F0H	; TURN OFF ALL MOTOR BITS
ED6D 08063F00	2661	OR MOTOR_STATUS,AL	; TURN ON THE CURRENT MOTOR
ED71 FB	2662	STI	; INTERRUPTS BACK ON
ED72 B010	2663	MOV AL,10H	; MASK BIT
ED74 D2E0	2664	SAL AL,CL	; DEVELOP BIT MASK FOR MOTOR ENABLE
ED76 0AC2	2665	OR AL,DL	; GET DRIVE SELECT BITS IN
ED78 0C0C	2666	OR AL,0CH	; NO RESET, ENABLE DMA/INT
ED7A 52	2667	PUSH DX	; SAVE REG
ED7B BAF203	2668	MOV DX,03F2H	; CONTROL PORT ADDRESS
ED7E EE	2669	OUT DX,AL	
ED7F 5A	2670	POP DX	; RECOVER REGISTERS
	2671		
	2672	;----- WAIT FOR MOTOR IF WRITE OPERATION	
	2673		
ED80 F6063F0080	2674	TEST MOTOR_STATUS,80H	; IS THIS A WRITE
ED85 7412	2675	JZ J14	; NO, CONTINUE WITHOUT WAIT
ED87 BB1400	2676	MOV BX,20	; GET THE MOTOR WAIT
ED8A E8DF00	2677	CALL GET_PARM	; PARAMETER
ED8D 0AE4	2678	OR AH,AH	; TEST FOR NO WAIT
ED8F	2679	J12:	; TEST_WAIT_TIME
ED8F 7408	2680	JZ J14	; EXIT WITH TIME EXPIRED
ED91 2BC9	2681	SUB CX,CX	; SET UP 1/8 SECOND LOOP TIME
ED93	2682	J13:	
ED93 E2FE	2683	LOOP J13	; WAIT FOR THE REQUIRED TIME
ED95 FECC	2684	DEC AH	; DECREMENT TIME VALUE
ED97 EBF6	2685	JMP J12	; ARE WE DONE YET
ED99	2686	J14:	; MOTOR_RUNNING
ED99 FB	2687	STI	; INTERRUPTS BACK ON FOR BYPASS WAIT
ED9A 59	2688	POP CX	
	2689		
	2690	;----- DO THE SEEK OPERATION	
	2691		
ED9B E80F00	2692	CALL SEEK	; MOVE TO CORRECT TRACK
ED9E 58	2693	POP AX	; RECOVER COMMAND
ED9F 8AFC	2694	MOV BH,AH	; SAVE COMMAND IN BH
EDA1 B600	2695	MOV DH,0	; SET NO SECTORS READ IN CASE OF ERROR
EDA3 724B	2696	JC J17	; IF ERROR, THEN EXIT AFTER MOTOR OFF
EDA5 BEF0ED90	2697	MOV SI,OFFSET J17	; DUMMY RETURN ON STACK FOR NEC_OUTPUT
EDA9 56	2698	PUSH SI	; SO THAT IT WILL RETURN TO MOTOR OFF
	2699		; LOCATION
	2700		
	2701	;----- SEND OUT THE PARAMETERS TO THE CONTROLLER	
	2702		
EDAA E89400	2703	CALL NEC_OUTPUT	; OUTPUT THE OPERATION COMMAND
EDAD 8A6601	2704	MOV AH,[BP+1]	; GET THE CURRENT HEAD NUMBER
EDB0 D0E4	2705	SAL AH,1	; MOVE IT TO BIT 2
EDB2 D0E4	2706	SAL AH,1	
EDB4 80E404	2707	AND AH,4	; ISOLATE THAT BIT
EDB7 0AE2	2708	OR AH,DL	; OR IN THE DRIVE NUMBER
EDB9 E88500	2709	CALL NEC_OUTPUT	
	2710		
	2711	;----- TEST FOR FORMAT COMMAND	
	2712		
EDBC 80FF40	2713	CMP BH,04DH	; IS THIS A FORMAT OPERATION
EDBF 7503	2714	JNE J15	; NO. CONTINUE WITH R/W/V

LOC OBJ	LINE	SOURCE	
E0C1 E962FF	2715	JMP J10	; IF SO, HANDLE SPECIAL
E0C4	2716		
E0C4 8AE5	2717	J15: MOV AH,CH	; CYLINDER NUMBER
E0C6 E87800	2718	CALL NEC_OUTPUT	
E0C9 8A6601	2719	MOV AH,[BP+1]	; HEAD NUMBER FROM STACK
E0CC E87200	2720	CALL NEC_OUTPUT	
E0CF 8AE1	2721	MOV AH,CL	; SECTOR NUMBER
E0D1 E86000	2722	CALL NEC_OUTPUT	
E0D4 B80700	2723	MOV BX,7	; BYTES/SECTOR PARM FROM BLOCK
E0D7 E89200	2724	CALL GET_PARM	; TO THE NEC
E0DA B80900	2725	MOV BX,9	; EOT PARM FROM BLOCK
E0DD E88C00	2726	CALL GET_PARM	; TO THE NEC
E0E0 B80800	2727	MOV BX,11	; GAP LENGTH PARM FROM BLOCK
E0E3 E88600	2728	CALL GET_PARM	; TO THE NEC
E0E6 B80000	2729	MOV BX,13	; DTL PARM FROM BLOCK
E0E9	2730	J16: RM_OPN_FINISH	
E0E9 E88000	2731	CALL GET_PARM	; TO THE NEC
E0EC 5E	2732	POP SI	; CAN NOW DISCARD THAT DUMMY
	2733		; RETURN ADDRESS
	2734		
	2735	I----- LET THE OPERATION HAPPEN	
	2736		
E0ED E84301	2737	CALL WAIT_INT	; WAIT FOR THE INTERRUPT
E0F0	2738	J17: JC J21	; MOTOR_OFF
E0F0 7245	2739	JC J21	; LOOK FOR ERROR
E0F2 E87401	2740	CALL RESULTS	; GET THE NEC STATUS
E0F5 723F	2741	JC J20	; LOOK FOR ERROR
	2742		
	2743	I----- CHECK THE RESULTS RETURNED BY THE CONTROLLER	
	2744		
E0F7 FC	2745	CLD	; SET THE CORRECT DIRECTION
E0FB BE4200	2746	MOV SI,OFFSET NEC_STATUS	; POINT TO STATUS FIELD
E0FB AC	2747	LODS NEC_STATUS	; GET ST0
E0FC 24C0	2748	AND AL,0C0H	; TEST FOR NORMAL TERMINATION
E0FE 7438	2749	JZ J22	; OPN_OK
EE00 3C40	2750	CMPL AL,040H	; TEST FOR ABNORMAL TERMINATION
EE02 7529	2751	JNZ J18	; NOT ABNORMAL, BAD NEC
	2752		
	2753	I----- ABNORMAL TERMINATION, FIND OUT WHY	
	2754		
EE04 AC	2755	LODS NEC_STATUS	; GET ST1
EE05 00E0	2756	SAL AL,1	; TEST FOR EOT FOUND
EE07 B404	2757	MOV AH,RECORD_NOT_FND	
EE09 7224	2758	JC J19	; RM_FAIL
EE0B 00E0	2759	SAL AL,1	
EE0D 00E0	2760	SAL AL,1	; TEST FOR CRC ERROR
EE0F 0410	2761	MOV AH,BAD_CRC	
EE11 721C	2762	JC J19	; RM_FAIL
EE13 00E0	2763	SAL AL,1	; TEST FOR DMA OVERRUN
EE15 B408	2764	MOV AH,BAD_DMA	
EE17 7216	2765	JC J19	; RM_FAIL
EE19 00E0	2766	SAL AL,1	
EE1B 00E0	2767	SAL AL,1	; TEST FOR RECORD NOT FOUND
EE1D B404	2768	MOV AH,RECORD_NOT_FND	
EE1F 720E	2769	JC J19	; RM_FAIL
EE21 00E0	2770	SAL AL,1	
EE23 B403	2771	MOV AH,WRITE_PROTECT	; TEST FOR WRITE_PROTECT
EE25 7208	2772	JC J19	; RM_FAIL
EE27 00E0	2773	SAL AL,1	; TEST MISSING ADDRESS MARK
EE29 B402	2774	MOV AH,BAD_ADDR_MARK	
EE2B 7202	2775	JC J19	; RM_FAIL
	2776		
	2777	I----- NEC MUST HAVE FAILED	
	2778		
EE2D	2779	J18: ; RM-NEC-FAIL	
EE2D B420	2780	MOV AH,BAD_NEC	
EE2F	2781	J19: ; RM-FAIL	
EE2F 08264100	2782	OR DISKETTE_STATUS,AH	
EE33 E87801	2783	CALL NUM_TRANS	; HOW MANY WERE REALLY TRANSFERRED
EE36	2784	J20: ; RM_ERR	
EE36 C3	2785	RET	; RETURN TO CALLER
EE37	2786	J21: ; RM_ERR_RES	
EE37 E82F01	2787	CALL RESULTS	; FLUSH THE RESULTS BUFFER
EE3A C3	2788	RET	
	2789		
	2790	I----- OPERATION WAS SUCCESSFUL	
	2791		

LOC OBJ	LINE	SOURCE		
EE3B	2792	J22:		; OPH_OK
EE3B E87001	2793	CALL	NUM_TRANS	; HOW MANY GOT MOVED
EE3E 32E4	2794	XOR	AH,AH	; NO ERRORS
EE40 C3	2795	RET		
	2796	RH_OPN	ENDP	
	2797	;-----		
	2798	; NEC_OUTPUT		;
	2799	; THIS ROUTINE SENDS A BYTE TO THE NEC CONTROLLER AFTER TESTING		;
	2800	; FOR CORRECT DIRECTION AND CONTROLLER READY THIS ROUTINE WILL		;
	2801	; TIME OUT IF THE BYTE IS NOT ACCEPTED WITHIN A REASONABLE		;
	2802	; AMOUNT OF TIME, SETTING THE DISKETTE STATUS ON COMPLETION.		;
	2803	; INPUT		;
	2804	; (AH) BYTE TO BE OUTPUT		;
	2805	; OUTPUT		;
	2806	; CY = 0 SUCCESS		;
	2807	; CY = 1 FAILURE -- DISKETTE STATUS UPDATED		;
	2808	; IF A FAILURE HAS OCCURRED, THE RETURN IS MADE ONE LEVEL		;
	2809	; HIGHER THAN THE CALLER OF NEC_OUTPUT.		;
	2810	; THIS REMOVES THE REQUIREMENT OF TESTING AFTER EVERY		;
	2811	; CALL OF NEC_OUTPUT.		;
	2812	; (AL) DESTROYED		;
	2813	;-----		
EE41	2814	NEC_OUTPUT	PROC NEAR	
EE41 52	2815	PUSH	DX	; SAVE REGISTERS
EE42 51	2816	PUSH	CX	
EE43 BAF403	2817	MOV	DX,03F4H	; STATUS PORT
EE46 33C9	2818	XOR	CX,CX	; COUNT FOR TIME OUT
EE48	2819	J23:		
EE48 EC	2820	IN	AL,DX	; GET STATUS
EE49 A840	2821	TEST	AL,040H	; TEST DIRECTION BIT
EE4B 740C	2822	JZ	J25	; DIRECTION OK
EE4D E2F9	2823	LOOP	J23	
EE4F	2824	J24:		; TIME_ERROR
EE4F 800E41000	2825	OR	DISKETTE_STATUS,TIME_OUT	
EE54 59	2826	POP	CX	
EE55 5A	2827	POP	DX	; SET ERROR CODE AND RESTORE REGS
EE56 58	2828	POP	AX	; DISCARD THE RETURN ADDRESS
EE57 F9	2829	STC		; INDICATE ERROR TO CALLER
EE58 C3	2830	RET		
EE59	2831	J25:		
EE59 33C9	2832	XOR	CX,CX	; RESET THE COUNT
EE5B	2833	J26:		
EE5B EC	2834	IN	AL,DX	; GET THE STATUS
EE5C A880	2835	TEST	AL,080H	; IS IT READY
EE5E 7504	2836	JNZ	J27	; YES, GO OUTPUT
EE60 E2F9	2837	LOOP	J26	; COUNT DOWN AND TRY AGAIN
EE62 EBEB	2838	JMP	J24	; ERROR CONDITION
EE64	2839	J27:		; OUTPUT
EE64 8AC4	2840	MOV	AL,AH	; GET BYTE TO OUTPUT
EE66 B2F5	2841	MOV	DL,0F5H	; DATA PORT (3F5)
EE68 EE	2842	OUT	DX,AL	; OUTPUT THE BYTE
EE69 59	2843	POP	CX	; RECOVER REGISTERS
EE6A 5A	2844	POP	DX	
EE6B C3	2845	RET		; CY = 0 FROM TEST INSTRUCTION
	2846	NEC_OUTPUT	ENDP	
	2847	;-----		
	2848	; GET_PARM		;
	2849	; THIS ROUTINE FETCHES THE INDEXED POINTER FROM THE DISK_BASE		;
	2850	; BLOCK POINTED AT BY THE DATA VARIABLE DISK_POINTER. A BYTE FROM		;
	2851	; THAT TABLE IS THEN MOVED INTO AH, THE INDEX OF THAT BYTE BEING		;
	2852	; THE PARM IN BX		;
	2853	; ENTRY --		;
	2854	; BX = INDEX OF BYTE TO BE FETCHED * 2		;
	2855	; IF THE LOW BIT OF BX IS ON, THE BYTE IS IMMEDIATELY OUTPUT		;
	2856	; TO THE NEC CONTROLLER		;
	2857	; EXIT --		;
	2858	; AH = THAT BYTE FROM BLOCK		;
	2859	;-----		
EE6C	2860	GET_PARM	PROC NEAR	
EE6C 1E	2861	PUSH	DS	; SAVE SEGMENT
EE6D 2BC0	2862	SUB	AX,AX	; ZERO TO AX
EE6F 8ED8	2863	MOV	DS,AX	
	2864	ASSUME	DS:ABS0	
EE71 C5367800	2865	LDS	SI,DISK_POINTER	; POINT TO BLOCK
EE75 D1EB	2866	SHR	BX,1	; DIVIDE BX BY 2, AND SET FLAG
	2867			; FOR EXIT
EE77 8A20	2868	MOV	AH,[SI+BX]	; GET THE WORD

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LOC OBJ          LINE  SOURCE
EE79 1F          2869          POP     DS           ; RESTORE SEGMENT
                2870          ASSUME DS:DATA
EE7A 72C5        2871          JC      NEC_OUTPUT   ; IF FLAG SET, OUTPUT TO CONTROLLER
EE7C C3          2872          RET      RET         ; RETURN TO CALLER
                2873          GET_PARM  ENDP
                2874          ;-----
                2875          ; SEEK
                2876          ; THIS ROUTINE WILL MOVE THE HEAD ON THE NAMED DRIVE TO THE
                2877          ; NAMED TRACK. IF THE DRIVE HAS NOT BEEN ACCESSED SINCE THE
                2878          ; DRIVE RESET COMMAND WAS ISSUED, THE DRIVE WILL BE RECALIBRATED.
                2879          ; INPUT
                2880          ; (DL) = DRIVE TO SEEK ON
                2881          ; (CH) = TRACK TO SEEK TO
                2882          ; OUTPUT
                2883          ; CY = 0 SUCCESS
                2884          ; CY = 1 FAILURE -- DISKETTE_STATUS SET ACCORDINGLY
                2885          ; (AX) DESTROYED
                2886          ;-----
EE7D             2887          SEEK    PROC    NEAR
EE7D B001        2888          MOV     AL,1         ; ESTABLISH MASK FOR RECAL TEST
EE7F 51          2889          PUSH   CX           ; SAVE INPUT VALUES
EE80 8ACA        2890          MOV     CL,DL        ; GET DRIVE VALUE INTO CL
EE82 D2C0        2891          ROL    AL,CL        ; SHIFT IT BY THE DRIVE VALUE
EE84 59          2892          POP     CX           ; RECOVER TRACK VALUE
EE85 84063E00    2893          TEST   AL,SEEK_STATUS ; TEST FOR RECAL REQUIRED
EE89 7513        2894          JNZ    J28          ; NO_RECAL
EE8B 08063E00    2895          OR     SEEK_STATUS,AL ; TURN ON THE NO RECAL BIT IN FLAG
EE8F B407        2896          MOV     AH,07H      ; RECALIBRATE COMMAND
EE91 E8ADFF      2897          CALL   NEC_OUTPUT
EE94 8AE2        2898          MOV     AH,DL
EE96 E8A8FF      2899          CALL   NEC_OUTPUT   ; OUTPUT THE DRIVE NUMBER
EE99 E87600      2900          CALL   CHK_STAT_2   ; GET THE INTERRUPT AND SENSE INT STATUS
EE9C 7229        2901          JC     J32          ; SEEK_ERROR
                2902
                2903          ;----- DRIVE IS IN SYNCH WITH CONTROLLER, SEEK TO TRACK
                2904
EE9E            2905          J28:
EE9E B40F        2906          MOV     AH,0FH      ; SEEK COMMAND TO NEC
EEA0 E89EFF      2907          CALL   NEC_OUTPUT
EEA3 8AE2        2908          MOV     AH,DL        ; DRIVE NUMBER
EEA5 E899FF      2909          CALL   NEC_OUTPUT
EEA8 8AE5        2910          MOV     AH,CH        ; TRACK NUMBER
EEAA E894FF      2911          CALL   NEC_OUTPUT
EEAD E86200      2912          CALL   CHK_STAT_2   ; GET ENDING INTERRUPT AND
                2913          ; SENSE STATUS
                2914
                2915          ;----- WAIT FOR HEAD SETTLE
                2916
EEB0 9C          2917          PUSHF          ; SAVE STATUS FLAGS
EEB1 B81200      2918          MOV     BX,18       ; GET HEAD SETTLE PARAMETER
EEB4 E8B5FF      2919          CALL   GET_PARM
EEB7 51          2920          PUSH   CX           ; SAVE REGISTER
EEB8            2921          J29:
EEB8 B92602      2922          MOV     CX,550      ; HEAD_SETTLE
EEBB 0AE4        2923          OR     AH,AH        ; 1 MS LOOP
EEBD 7406        2924          JZ     J31          ; TEST FOR TIME EXPIRED
EEBF            2925          J30:
EEBF E2FE        2926          LOOP   J30          ; DELAY FOR 1 MS
EEC1 FECC       2927          DEC   AH            ; DECREMENT THE COUNT
EEC3 EBF3        2928          JMP   J29           ; DO IT SOME MORE
EEC5            2929          J31:
EEC5 59          2930          POP     CX           ; RECOVER STATE
EEC6 90          2931          POPF
EEC7            2932          J32:
EEC7 C3          2933          RET              ; RETURN TO CALLER
                2934          SEEK    ENDP
                2935          ;-----
                2936          ; DMA_SETUP
                2937          ; THIS ROUTINE SETS UP THE DMA FOR READ/WRITE/VERIFY OPERATIONS.
                2938          ; INPUT
                2939          ; (AL) = MODE BYTE FOR THE DMA
                2940          ; (ES:BX) - ADDRESS TO READ/WRITE THE DATA
                2941          ; OUTPUT
                2942          ; (AX) DESTROYED
                2943          ;-----
EEC8            2944          DMA_SETUP  PROC    NEAR
EEC8 51          2945          PUSH   CX           ; SAVE THE REGISTER

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LOC OBJ	LINE	SOURCE	
EEC9 FA	2946	CLI	; NO MORE INTERRUPTS
EECA E60C	2947	OUT DMA+12,AL	; SET THE FIRST/LAST F/F
EECC 50	2948	PUSH AX	
EECD 58	2949	POP AX	
EECE E60B	2950	OUT DMA+11,AL	; OUTPUT THE MODE BYTE
EED0 8CC0	2951	MOV AX,ES	; GET THE ES VALUE
EE02 B104	2952	MOV CL,4	; SHIFT COUNT
EE04 D3C0	2953	ROL AX,CL	; ROTATE LEFT
EE06 8AE8	2954	MOV CH,AL	; GET HIGHEST NYBLE OF ES TO CH
EE08 24F0	2955	AND AL,0F0H	; ZERO THE LOW NYBBLE FROM SEGMENT
EEDA 03C3	2956	ADD AX,BX	; TEST FOR CARRY FROM ADDITION
EEDC 7302	2957	JNC J33	
EEDF FEC5	2958	INC CH	; CARRY MEANS HIGH 4 BITS MUST BE INC
EEF0	2959		
EEF0 50	2960	J33: PUSH AX	; SAVE START ADDRESS
EEF1 E604	2961	OUT DMA+4,AL	; OUTPUT LOW ADDRESS
EEF3 8AC4	2962	MOV AL,AH	
EEF5 E604	2963	OUT DMA+4,AL	; OUTPUT HIGH ADDRESS
EEF7 8AC5	2964	MOV AL,CH	; GET HIGH 4 BITS
EEF9 240F	2965	AND AL,0FH	
EEEB E681	2966	OUT 061H,AL	; OUTPUT THE HIGH 4 BITS TO
	2967		; THE PAGE REGISTER
	2968		
	2969	J----- DETERMINE COUNT	
	2970		
EEED 8AE6	2971	MOV AH,DH	; NUMBER OF SECTORS
EEEF 2AC0	2972	SUB AL,AL	; TIMES 256 INTO AX
EEF1 D1E8	2973	SHR AX,1	; SECTORS * 128 INTO AX
EEF3 50	2974	PUSH AX	
EEF4 BB0600	2975	MOV BX,6	; GET THE BYTES/SECTOR PARM
EEF7 E872FF	2976	CALL GET_PARM	
EEFA 8ACC	2977	MOV CL,AH	; USE AS SHIFT COUNT (0=128, 1=256 ETC)
EEFC 58	2978	POP AX	
EEFD D3E0	2979	SHL AX,CL	; MULTIPLY BY CORRECT AMOUNT
EEFF 48	2980	DEC AX	; -1 FOR DMA VALUE
EF00 50	2981	PUSH AX	; SAVE COUNT VALUE
EF01 E605	2982	OUT DMA+5,AL	; LOW BYTE OF COUNT
EF03 8AC4	2983	MOV AL,AH	
EF05 E605	2984	OUT DMA+5,AL	; HIGH BYTE OF COUNT
EF07 FB	2985	STI	; INTERRUPTS BACK ON
EF08 59	2986	POP CX	; RECOVER COUNT VALUE
EF09 58	2987	POP AX	; RECOVER ADDRESS VALUE
EF0A 03C1	2988	ADD AX,CX	; ADD, TEST FOR 64K OVERFLOW
EF0C 59	2989	POP CX	; RECOVER REGISTER
EF0D B002	2990	MOV AL,2	; MODE FOR 8237
EF0F E60A	2991	OUT DMA+10,AL	; INITIALIZE THE DISKETTE CHANNEL
EF11 C3	2992	RET	; RETURN TO CALLER,
	2993		; CFL SET BY ABOVE IF ERROR
	2994	DMA_SETUP	ENDP
	2995		
	2996	J-----	
	2997	; CHK_STAT_2	:
	2998	; THIS ROUTINE HANDLES THE INTERRUPT RECEIVED AFTER A	:
	2999	; RECALIBRATE, SEEK, OR RESET TO THE ADAPTER.	:
	3000	; THE INTERRUPT IS WAITED FOR, THE INTERRUPT STATUS SENSED,	:
	3001	; AND THE RESULT RETURNED TO THE CALLER.	:
	3002	; INPUT	:
	3003	; NONE	:
	3004	; OUTPUT	:
	3005	; CY = 0 SUCCESS	:
	3006	; CY = 1 FAILURE -- ERROR IS IN DISKETTE_STATUS	:
	3007	; (AX) DESTROYED	:
	3008	J-----	
EF12	3008	CHK_STAT_2	PROC NEAR
EF12 E81E00	3009	CALL WAIT_INT	; WAIT FOR THE INTERRUPT
EF15 7214	3010	JC J34	; IF ERROR, RETURN IT
EF17 B408	3011	MOV AH,08H	; SENSE INTERRUPT STATUS COMMAND
EF19 E825FF	3012	CALL NEC_OUTPUT	
EF1C E84A00	3013	CALL RESULTS	; READ IN THE RESULTS
EF1F 720A	3014	JC J34	; CHK2_RETURN
EF21 A04200	3015	MOV AL,NEC_STATUS	; GET THE FIRST STATUS BYTE
EF24 2460	3016	AND AL,060H	; ISOLATE THE BITS
EF26 3C60	3017	CHP AL,060H	; TEST FOR CORRECT VALUE
EF28 7402	3018	JZ J35	; IF ERROR, GO MARK IT
EF2A F8	3019	CLC	; GOOD RETURN
EF2B	3020	J34:	
EF2B C3	3021	RET	; RETURN TO CALLER
EF2C	3022	J35:	; CHK2_ERROR

```

LOC OBJ          LINE  SOURCE
EF2C 800E410040 3023          OR    DISKETTE_STATUS,BAD_SEEK
EF31 F9          3024          STC          ; ERROR RETURN CODE
EF32 C3          3025          RET
3026          CHK_STAT_2      ENDP
3027          ;-----
3028          ; WAIT INT          :
3029          ; THIS ROUTINE WAITS FOR AN INTERRUPT TO OCCUR. A TIME OUT :
3030          ; ROUTINE TAKES PLACE DURING THE WAIT, SO THAT AN ERROR MAY BE :
3031          ; RETURNED IF THE DRIVE IS NOT READY. :
3032          ; INPUT          :
3033          ; NONE          :
3034          ; OUTPUT          :
3035          ; CY = 0 SUCCESS :
3036          ; CY = 1 FAILURE -- DISKETTE_STATUS IS SET ACCORDINGLY :
3037          ; (AX) DESTROYED :
3038          ;-----
EF33          3039          WAIT_INT      PROC      NEAR
EF33 FB          3040          STI          ; TURN ON INTERRUPTS, JUST IN CASE
EF34 53          3041          PUSH     BX
EF35 51          3042          PUSH     CX          ; SAVE REGISTERS
EF36 B302        3043          MOV     BL,2          ; CLEAR THE COUNTERS
EF38 33C9        3044          XOR     CX,CX          ; FOR 2 SECOND WAIT
EF3A          3045          J36:
EF3A F6063E0080 3046          TEST    SEEK_STATUS,INT_FLAG ; TEST FOR INTERRUPT OCCURRING
EF3F 750C        3047          JNZ     J37          ;
EF41 E2F7        3048          LOOP   J36          ; COUNT DOWN WHILE WAITING
EF43 FECB        3049          DEC     BL          ; SECOND LEVEL COUNTER
EF45 75F3        3050          JNZ     J36
EF47 800E410080 3051          OR     DISKETTE_STATUS,TIME_OUT ; NOTHING HAPPENED
EF4C F9          3052          STC          ; ERROR RETURN
EF4D          3053          J37:
EF4D 9C          3054          PUSHF         ; SAVE CURRENT CARRY
EF4E 80263E007F 3055          AND    SEEK_STATUS,NOT INT_FLAG ; TURN OFF INTERRUPT FLAG
EF53 9D          3056          POPF         ; RECOVER CARRY
EF54 59          3057          POP     CX
EF55 58          3058          POP     BX          ; RECOVER REGISTERS
EF56 C3          3059          RET          ; GOOD RETURN CODE COMES
3060          ; FROM TEST INST
3061          WAIT_INT      ENDP
3062          ;-----
3063          ; DISK_INT          :
3064          ; THIS ROUTINE HANDLES THE DISKETTE INTERRUPT :
3065          ; INPUT          :
3066          ; NONE          :
3067          ; OUTPUT          :
3068          ; THE INTERRUPT FLAG IS SET IS SEEK_STATUS :
3069          ;-----
EF57          3070          ORG     0EF57H
EF57          3071          DISK_INT    PROC      FAR
EF57 FB          3072          STI          ; RE ENABLE INTERRUPTS
EF58 1E          3073          PUSH     DS
EF59 50          3074          PUSH     AX
EF5A E8E10F      3075          CALL    DDS
EF5D 800E3E0080 3076          OR     SEEK_STATUS,INT_FLAG
EF62 B020        3077          MOV     AL,20H          ; END OF INTERRUPT MARKER
EF64 E620        3078          OUT    20H,AL          ; INTERRUPT CONTROL PORT
EF66 58          3079          POP     AX
EF67 1F          3080          POP     DS          ; RECOVER SYSTEM
EF68 CF          3081          IRET         ; RETURN FROM INTERRUPT
3082          DISK_INT      ENDP
3083          ;-----
3084          ; RESULTS          :
3085          ; THIS ROUTINE WILL READ ANYTHING THAT THE NEC CONTROLLER HAS :
3086          ; TO SAY FOLLOWING AN INTERRUPT. :
3087          ; INPUT          :
3088          ; NONE          :
3089          ; OUTPUT          :
3090          ; CY = 0 SUCCESSFUL TRANSFER :
3091          ; CY = 1 FAILURE -- TIME OUT IN WAITING FOR STATUS :
3092          ; NEC_STATUS AREA HAS STATUS BYTE LOADED INTO IT :
3093          ; (AH) DESTROYED :
3094          ;-----
EF69          3095          RESULTS PROC      NEAR
EF69 FC          3096          CLD
EF6A BF4200      3097          MOV     DI,OFFSET NEC_STATUS ; POINTER TO DATA AREA
EF6D 51          3098          PUSH    CX          ; SAVE COUNTER
EF6E 52          3099          PUSH    DX

```

LOC OBJ	LINE	SOURCE			
EF6F 53	3100	PUSH	BX		
EF70 B307	3101	MOV	BL,7		; MAX STATUS BYTES
	3102				
	3103	;----- WAIT FOR REQUEST FOR MASTER			
	3104				
EF72	3105	J38:			; INPUT_LOOP
EF72 33C9	3106	XOR	CX,CX		; COUNTER
EF74 BAF403	3107	MOV	DX,03F4H		; STATUS PORT
EF77	3108	J39:			; WAIT FOR MASTER
EF77 EC	3109	IN	AL,DX		; GET STATUS
EF78 A880	3110	TEST	AL,080H		; MASTER READY
EF7A 750C	3111	JNZ	J40A		; TEST_DIR
EF7C E2F9	3112	LOOP	J39		; WAIT_MASTER
EF7E 800E410080	3113	OR	DISKETTE_STATUS,TIME_OUT		
EF83	3114	J40:			; RESULTS_ERROR
EF83 F9	3115	STC			; SET ERROR RETURN
EF84 5B	3116	POP	BX		
EF85 5A	3117	POP	DX		
EF86 59	3118	POP	CX		
EF87 C3	3119	RET			
	3120				
	3121	;----- TEST THE DIRECTION BIT			
	3122				
EF88	3123	J40A:			
EF88 EC	3124	IN	AL,DX		; GET STATUS REG AGAIN
EF89 A840	3125	TEST	AL,040H		; TEST DIRECTION BIT
EF8B 7507	3126	JNZ	J42		; OK TO READ STATUS
EF8D	3127	J41:			; NEC_FAIL
EF8D 800E410020	3128	OR	DISKETTE_STATUS,BAD_NEC		
EF92 EBEB	3129	JMP	J40		; RESULTS_ERROR
	3130				
	3131	;----- READ IN THE STATUS			
	3132				
EF94	3133	J42:			; INPUT_STAT
EF94 42	3134	INC	DX		; POINT AT DATA PORT
EF95 EC	3135	IN	AL,DX		; GET THE DATA
EF96 8805	3136	MOV	[DI],AL		; STORE THE BYTE
EF98 47	3137	INC	DI		; INCREMENT THE POINTER
EF99 B90A00	3138	MOV	CX,10		; LOOP TO KILL TIME FOR NEC
EF9C E2FE	3139	J43: LOOP	J43		
EF9E 4A	3140	DEC	DX		; POINT AT STATUS PORT
EF9F EC	3141	IN	AL,DX		; GET STATUS
EFA0 A810	3142	TEST	AL,010H		; TEST FOR NEC STILL BUSY
EFA2 7406	3143	JZ	J44		; RESULTS DONE
EFA4 FECB	3144	DEC	BL		; DECREMENT THE STATUS COUNTER
EFA6 75CA	3145	JNZ	J38		; GO BACK FOR MORE
EFA8 EBEB3	3146	JMP	J41		; CHIP HAS FAILED
	3147				
	3148	;----- RESULT OPERATION IS DONE			
	3149				
EFAA	3150	J44:			
EFAE 5B	3151	POP	BX		
EFA8 5A	3152	POP	DX		
EFA8 59	3153	POP	CX		; RECOVER REGISTERS
EFA8 C3	3154	RET			; GOOD RETURN CODE FROM TEST INST
	3155	;-----			
	3156	; NUM_TRANS	:		:
	3157	; THIS ROUTINE CALCULATES THE NUMBER OF SECTORS THAT	:		:
	3158	; WERE ACTUALLY TRANSFERRED TO/FROM THE DISKETTE	:		:
	3159	; INPUT	:		:
	3160	; (CH) = CYLINDER OF OPERATION	:		:
	3161	; (CL) = START SECTOR OF OPERATION	:		:
	3162	; OUTPUT	:		:
	3163	; (AL) = NUMBER ACTUALLY TRANSFERRED	:		:
	3164	; NO OTHER REGISTERS MODIFIED	:		:
	3165	;-----			
EFAE	3166	NUM_TRANS	PROC	NEAR	
EFAE A04500	3167	MOV	AL,NEC_STATUS+3		; GET CYLINDER ENDED UP ON
EFB1 3AC5	3168	CHP	AL,CH		; SAME AS WE STARTED
EFB3 A04700	3169	MOV	AL,NEC_STATUS+5		; GET ENDING SECTOR
EFB6 740A	3170	JZ	J45		; IF ON SAME CYL, THEN NO ADJUST
EFB8 B08000	3171	MOV	BX,8		
EFBB E8AEFE	3172	CALL	GET_PARM		; GET EOT VALUE
EFBE 8AC4	3173	MOV	AL,AH		; INTO AL
EFC0 FEC0	3174	INC	AL		; USE EOT+1 FOR CALCULATION
EFC2	3175	J45:			
EFC2 2AC1	3176	SUB	AL,CL		; SUBTRACT START FROM END

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LOC OBJ          LINE  SOURCE
EFC4 C3          3177      RET
                 3178      NUM_TRANS      ENDP
                 3179      RESULTS ENDP
                 3180      ;-----
                 3181      ; DISK_BASE
                 3182      ; THIS IS THE SET OF PARAMETERS REQUIRED FOR DISKETTE OPERATION.
                 3183      ; THEY ARE POINTED AT BY THE DATA VARIABLE DISK_POINTER. TO
                 3184      ; MODIFY THE PARAMETERS, BUILD ANOTHER PARAMETER BLOCK AND POINT
                 3185      ; DISK_POINTER TO IT.
                 3186      ;-----
EFC7             3187      ORG      0EFC7H
EFC7             3188      DISK_BASE LABEL BYTE
EFC7 CF          3189      DB      11001111B ; SRT=C, HD UNLOAD=0F - 1ST SPECIFY BYTE
EFC8 02          3190      DB      2 ; HD LOAD=1, MODE=DMA - 2ND SPECIFY RYTF
EFC9 25          3191      DB      MOTOR_WAIT ; WAIT AFTER OPN TIL MOTOR OFF
EFC4 02          3192      DB      2 ; 512 BYTES/SECTOR
EFCB 08          3193      DB      8 ; EOT ( LAST SECTOR ON TRACK)
EFC4 2A          3194      DB      02AH ; GAP LENGTH
EFC4 FF          3195      DB      0FFH ; DTL
EFC4 50          3196      DB      050H ; GAP LENGTH FOR FORMAT
EFCF F6          3197      DB      0F6H ; FILL BYTE FOR FORHAT
EFD0 19          3198      DB      25 ; HEAD SETTLE TIME (MILLISECONDS)
EFD1 04          3199      DB      4 ; MOTOR START TIME (1/8 SECONDS)
                 3200
                 3201      ;--- INT 17 -----
                 3202      ; PRINTER_IO
                 3203      ; THIS ROUTINE PROVIDES COMMUNICATION WITH THE PRINTER
                 3204      ; INPUT
                 3205      ; (AH)=0 PRINT THE CHARACTER IN (AL)
                 3206      ; ON RETURN, AH=1 IF CHARACTER COULD NOT BE PRINTED
                 3207      ; (TIME OUT). OTHER BITS SET AS ON NORMAL STATUS CALL
                 3208      ; (AH)=1 INITIALIZE THE PRINTER PORT
                 3209      ; RETURNS WITH (AH) SET WITH PRINTER STATUS
                 3210      ; (AH)=2 READ THE PRINTER STATUS INTO (AH)
                 3211      ; 7 6 5 4 3 2-1 0
                 3212      ; | | | | | | |
                 3213      ; | | | | | | | TIME OUT
                 3214      ; | | | | | | | UNUSED
                 3215      ; | | | | | | | I/O ERROR
                 3216      ; | | | | | | | _ 1 = SELECTED
                 3217      ; | | | | | | | _ 1 = OUT OF PAPER
                 3218      ; | | | | | | | _ 1 = ACKNOWLEDGE
                 3219      ; | | | | | | | _ 1 = NOT BUSY
                 3220      ;
                 3221      ; (DX) = PRINTER TO BE USED (0,1,2) CORRESPONDING TO ACTUAL
                 3222      ; VALUES IN PRINTER_BASE AREA
                 3223      ;
                 3224      ; DATA AREA PRINTER_BASE CONTAINS THE BASE ADDRESS OF THE PRINTER
                 3225      ; CARD(S) AVAILABLE (LOCATED AT BEGINNING OF DATA SEGMENT,
                 3226      ; 400H ABSOLUTE, 3 WORDS)
                 3227      ;
                 3228      ; DATA AREA PRINT_TIM_OUT (BYTE) MAY BE CHANGED TO CAUSE DIFFERENT
                 3229      ; TIME-OUT WAITS. DEFAULT=20
                 3230      ;
                 3231      ; REGISTERS AH IS MODIFIED
                 3232      ; ALL OTHERS UNCHANGED
                 3233      ;-----
                 3234      ASSUME CS:CODE,DS:DATA
EFD2             3235      ORG      0EFD2H
EFD2             3236      PRINTER_IO PROC FAR
EFD2 FB          3237      STI ; INTERRUPTS BACK ON
EFD3 1E          3238      PUSH DS ; SAVE SEGMENT
EFD4 52          3239      PUSH DX
EFD5 56          3240      PUSH SI
EFD6 51          3241      PUSH CX
EFD7 53          3242      PUSH BX
EFD8 E6630F     3243      CALL DDS
EFD8 8BF2       3244      MOV SI,DX ; GET PRINTER PARM
EFD8 8A5C78     3245      MOV BL,PRINT_TIM_OUT[SI] ; LOAD TIME-OUT PARM
EFD8 D1E6       3246      SHL SI,1 ; WORD OFFSET INTO TABLE
EFE2 8B5408     3247      MOV DX,PRINTER_BASE[SI] ; GET BASE ADDRESS FOR PRINTER CARD
EFE5 0BD2       3248      OR DX,DX ; TEST DX FOR ZERO,
                 3249      ; INDICATING NO PRINTER
EFE7 740C       3250      JZ B1 ; RETURN
EFE9 0AE4       3251      OR AH,AH ; TEST FOR (AH)=0
EFEB 740E       3252      JZ B2 ; PRINT_AL
EFE8 FECC       3253      DEC AH ; TEST FOR (AH)=1
EFEE 743F       3254      JZ B8 ; INIT_PRT

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LOC OBJ          LINE SOURCE
EFF1 FECC        3254      DEC AH          ; TEST FOR (AH)=2
EFF3 7428        3255      JZ B5          ; PRINTER STATUS
EFF5             3256      B1:           ; RETURN
EFF5 5B          3257      POP BX
EFF6 59          3258      POP CX
EFF7 5E          3259      POP SI        ; RECOVER REGISTERS
EFF8 5A          3260      POP DX        ; RECOVER REGISTERS
EFF9 1F          3261      POP DS
EFFA CF          3262      IRET
3263
3264             ;----- PRINT THE CHARACTER IN (AL)
3265
EFFB             3266      B2:
EFFB 50          3267      PUSH AX      ; SAVE VALUE TO PRINT
EFFC EE          3268      OUT DX,AL   ; OUTPUT CHAR TO PORT
EFFD 42          3269      INC DX      ; POINT TO STATUS PORT
EFFE             3270      B3:
EFFE 2BC9        3271      SUB CX,CX   ; WAIT_BUSY
F000             3272      B3_1:
F000 EC          3273      IN AL,DX   ; GET STATUS
F001 8AE0        3274      MOV AH,AL  ; STATUS TO AH ALSO
F003 A880        3275      TEST AL,80H ; IS THE PRINTER CURRENTLY BUSY
F005 750E        3276      JNZ B4     ; OUT_STROBE
F007 E2F7        3277      LOOP B3_1 ; TRY AGAIN
F009 FECB        3278      DEC BL     ; DROP LOOP COUNT
F00B 75F1        3279      JNZ B3     ; GO TILL TIMEOUT ENDS
F00D 80CC01      3280      OR AH,1   ; SET ERROR FLAG
F010 80E4F9      3281      AND AH,0F9H ; TURN OFF THE OTHER BITS
F013 EB13        3282      JMP SHORT B7 ; RETURN WITH ERROR FLAG SET
F015             3283      B4:
F015 B00D        3284      MOV AL,0DH ; SET THE STROBE HIGH
F017 42          3285      INC DX     ; STROBE IS BIT 0 OF PORT C OF 8255
F018 EE          3286      OUT DX,AL
F019 B00C        3287      MOV AL,0CH ; SET THE STROBE LOW
F01B EE          3288      OUT DX,AL
F01C 58          3289      POP AX    ; RECOVER THE OUTPUT CHAR
3290
3291             ;----- PRINTER STATUS
3292
F01D             3293      B5:
F01D 50          3294      PUSH AX   ; SAVE AL REG
F01E             3295      B6:
F01E 8B5408      3296      MOV DX,PRINTER_BASE[SI]
F021 42          3297      INC DX
F022 EC          3298      IN AL,DX  ; GET PRINTER STATUS
F023 8AE0        3299      MOV AH,AL
F025 80E4F8      3300      AND AH,0F8H ; TURN OFF UNUSED BITS
F028             3301      B7:
F028 5A          3302      POP DX    ; STATUS_SET
F029 8AC2        3303      MOV AL,DL ; RECOVER AL REG
F02B 80F448      3304      XOR AH,48H ; GET CHARACTER INTO AL
F02E EBC5        3305      JMP B1    ; FLIP A COUPLE OF BITS
3306             ; RETURN FROM ROUTINE
3307             ;----- INITIALIZE THE PRINTER PORT
3308
F030             3309      B8:
F030 50          3310      PUSH AX   ; SAVE AL
F031 42          3311      INC DX   ; POINT TO OUTPUT PORT
F032 42          3312      INC DX
F033 B008        3313      MOV AL,8 ; SET INIT LINE LOW
F035 EE          3314      OUT DX,AL
F036 B0E803      3315      MOV AX,1000
F039             3316      B9:
F039 48          3317      DEC AX   ; INIT_LOOP
F03A 75FD        3318      JNZ B9  ; LOOP FOR RESET TO TAKE
F03C B00C        3319      MOV AL,0CH ; INIT_LOOP
3320             ; NO INTERRUPTS, NON AUTO LF,
3321             ; INIT HIGH
F03E EE          3321      OUT DX,AL
F03F EBDD        3322      JMP B6   ; PRT_STATUS_1
3323      PRINTER_IO  ENDP
3324
F041 62E1        3325      C2 DW C24 ; RETURN ADDRESS FOR DUMMY STACK
3326
3327             ;--- INT 10 -----
3328             ; VIDEO_IO
3329             ; THESE ROUTINES PROVIDE THE CRT INTERFACE
3330             ; THE FOLLOWING FUNCTIONS ARE PROVIDED:

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3331 ; (AH)=0 SET MODE (AL) CONTAINS MODE VALUE ;
3332 ; (AL)=0 40X25 BH (POWER ON DEFAULT) ;
3333 ; (AL)=1 40X25 COLOR ;
3334 ; (AL)=2 80X25 BH ;
3335 ; (AL)=3 80X25 COLOR ;
3336 ; GRAPHICS MODES ;
3337 ; (AL)=4 320X200 COLOR ;
3338 ; (AL)=5 320X200 BH ;
3339 ; (AL)=6 640X200 BH ;
3340 ; CRT MODE=7 80X25 B&W CARD (USED INTERNAL TO VIDEO ONLY) ;
3341 ; *** NOTE BH MODES OPERATE SAME AS COLOR MODES, BUT ;
3342 ; COLOR BURST IS NOT ENABLED ;
3343 ; (AH)=1 SET CURSOR TYPE ;
3344 ; (CH) = BITS 4-0 = START LINE FOR CURSOR ;
3345 ; ** HARDWARE WILL ALWAYS CAUSE BLIN ;
3346 ; ** SETTING BIT 5 OR 6 WILL CAUSE ERRATIC ;
3347 ; BLINKING OR NO CURSOR AT ALL ;
3348 ; (CL) = BITS 4-0 = END LINE FOR CURSOR ;
3349 ; (AH)=2 SET CURSOR POSITION ;
3350 ; (DH,DL) = ROW,COLUMN (0,0) IS UPPER LEFT ;
3351 ; (BH) = PAGE NUMBER (MUST BE 0 FOR GRAPHICS MODES) ;
3352 ; (AH)=3 READ CURSOR POSITION ;
3353 ; (BH) = PAGE NUMBER (MUST BE 0 FOR GRAPHICS MODES) ;
3354 ; ON EXIT (DH,DL) = ROW,COLUMN OF CURRENT CURSOR ;
3355 ; (CH,CL) = CURSOR MODE CURRENTLY SET ;
3356 ; (AH)=4 READ LIGHT PEN POSITION ;
3357 ; ON EXIT: ;
3358 ; (AH) = 0 -- LIGHT PEN SWITCH NOT DOWN/HOT TRIGGERED ;
3359 ; (AH) = 1 -- VALID LIGHT PEN VALUE IN REGISTERS ;
3360 ; (DH,DL) = ROW,COLUMN OF CHARACTER LP POSN ;
3361 ; (CH) = RASTER LINE (0-199) ;
3362 ; (BX) = PIXEL COLUMN (0-319,639) ;
3363 ; (AH)=5 SELECT ACTIVE DISPLAY PAGE (VALID ONLY FOR ALPHA MODES) ;
3364 ; (AL)=NEW PAGE VAL (0-7 FOR MODES 0&1, 0-3 FOR MODES 2&3) ;
3365 ; (AH)=6 SCROLL ACTIVE PAGE UP ;
3366 ; (AL) = NUMBER OF LINES, INPUT LINES BLANKED AT BOTTOM ;
3367 ; OF WINDOW ;
3368 ; AL = 0 MEANS BLANK ENTIRE WINDOW ;
3369 ; (CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL ;
3370 ; (DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL ;
3371 ; (BH) = ATTRIBUTE TO BE USED ON BLANK LINE ;
3372 ; (AH)=7 SCROLL ACTIVE PAGE DOWN ;
3373 ; (AL) = NUMBER OF LINES, INPUT LINES BLANKED AT TOP ;
3374 ; OF WINDOW ;
3375 ; AL = 0 MEANS BLANK ENTIRE WINDOW ;
3376 ; (CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL ;
3377 ; (DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL ;
3378 ; (BH) = ATTRIBUTE TO BE USED ON BLANK LINE ;
3379 ;
3380 ; CHARACTER HANDLING ROUTINES ;
3381 ;
3382 ; (AH) = 8 READ ATTRIBUTE/CHARACTER AT CURRENT CURSOR POSITION ;
3383 ; (BH) = DISPLAY PAGE (VALID FOR ALPHA MODES ONLY) ;
3384 ; ON EXIT: ;
3385 ; (AL) = CHAR READ ;
3386 ; (AH) = ATTRIBUTE OF CHARACTER READ (ALPHA MODES ONLY) ;
3387 ; (AH) = 9 WRITE ATTRIBUTE/CHARACTER AT CURRENT CURSOR POSITION ;
3388 ; (BH) = DISPLAY PAGE (VALID FOR ALPHA MODES ONLY) ;
3389 ; (CX) = COUNT OF CHARACTERS TO WRITE ;
3390 ; (AL) = CHAR TO WRITE ;
3391 ; (BL) = ATTRIBUTE OF CHARACTER (ALPHA)/COLOR OF CHAR ;
3392 ; (GRAPHICS) ;
3393 ; SEE NOTE ON WRITE DOT FOR BIT 7 OF BL = 1. ;
3394 ; (AH) = 10 WRITE CHARACTER ONLY AT CURRENT CURSOR POSITION ;
3395 ; (BH) = DISPLAY PAGE (VALID FOR ALPHA MODES ONLY) ;
3396 ; (CX) = COUNT OF CHARACTERS TO WRITE ;
3397 ; (AL) = CHAR TO WRITE ;
3398 ; FOR READ/WRITE CHARACTER INTERFACE WHILE IN GRAPHICS MODE, THE ;
3399 ; CHARACTERS ARE FORMED FROM A CHARACTER GENERATOR IMAGE ;
3400 ; MAINTAINED IN THE SYSTEM ROM. ONLY THE 1ST 128 CHARS ;
3401 ; ARE CONTAINED THERE. TO READ/WRITE THE SECOND 128 ;
3402 ; CHARS, THE USER MUST INITIALIZE THE POINTER AT ;
3403 ; INTERRUPT 1FH (LOCATION 0007CH) TO POINT TO THE 1K BYTE ;
3404 ; TABLE CONTAINING THE CODE POINTS FOR THE SECOND ;
3405 ; 128 CHARS (128-255). ;
3406 ; FOR WRITE CHARACTER INTERFACE IN GRAPHICS MODE, THE REPLICATION ;
3407 ; FACTOR CONTAINED IN (CX) ON ENTRY WILL PRODUCE VALID ;

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LOC OBJ	LINE	SOURCE	
	3408	RESULTS ONLY FOR CHARACTERS CONTAINED ON THE SAME ROW.	:
	3409	CONTINUATION TO SUCCEEDING LINES WILL NOT PRODUCE	:
	3410	CORRECTLY.	:
	3411		:
	3412	GRAPHICS INTERFACE	:
	3413	(AH) = 11 SET COLOR PALETTE	:
	3414	(BH) = PALETTE COLOR ID BEING SET (0-127)	:
	3415	(BL) = COLOR VALUE TO BE USED WITH THAT COLOR ID	:
	3416	NOTE: FOR THE CURRENT COLOR CARD, THIS ENTRY POINT	:
	3417	HAS MEANING ONLY FOR 320X200 GRAPHICS.	:
	3418	COLOR ID = 0 SELECTS THE BACKGROUND COLOR (0-15):	:
	3419	COLOR ID = 1 SELECTS THE PALETTE TO BE USED:	:
	3420	0 = GREEN(1)/RED(2)/YELLOW(3)	:
	3421	1 = CYAN(1)/MAGENTA(2)/WHITE(3)	:
	3422	IN 40X25 OR 80X25 ALPHA MODES, THE VALUE SET	:
	3423	FOR PALETTE COLOR 0 INDICATES THE	:
	3424	BORDER COLOR TO BE USED (VALUES 0-31,	:
	3425	WHERE 16-31 SELECT THE HIGH INTENSITY	:
	3426	BACKGROUND SET.	:
	3427	(AH) = 12 WRITE DOT	:
	3428	(DX) = ROW NUMBER	:
	3429	(CX) = COLUMN NUMBER	:
	3430	(AL) = COLOR VALUE	:
	3431	IF BIT 7 OF AL = 1, THEN THE COLOR VALUE IS	:
	3432	EXCLUSIVE OR'D WITH THE CURRENT CONTENTS OF	:
	3433	THE DOT	:
	3434	(AH) = 13 READ DOT	:
	3435	(DX) = ROW NUMBER	:
	3436	(CX) = COLUMN NUMBER	:
	3437	(AL) RETURNS THE DOT READ	:
	3438		:
	3439	ASCII TELETYPE ROUTINE FOR OUTPUT	:
	3440		:
	3441	(AH) = 14 WRITE TELETYPE TO ACTIVE PAGE	:
	3442	(AL) = CHAR TO WRITE	:
	3443	(BL) = FOREGROUND COLOR IN GRAPHICS MODE	:
	3444	NOTE -- SCREEN WIDTH IS CONTROLLED BY PREVIOUS MODE SET	:
	3445		:
	3446	(AH) = 15 CURRENT VIDEO STATE	:
	3447	RETURNS THE CURRENT VIDEO STATE	:
	3448	(AL) = MODE CURRENTLY SET ( SEE AH=0 FOR EXPLANATION)	:
	3449	(AH) = NUMBER OF CHARACTER COLUMNS ON SCREEN	:
	3450	(BH) = CURRENT ACTIVE DISPLAY PAGE	:
	3451		:
	3452	CS,SS,DS,ES,BX,CX,DX PRESERVED DURING CALL	:
	3453	ALL OTHERS DESTROYED	:
	3454	-----	:
	3455	ASSUME CS:CODE,DS:DATA,ES:VIDEO_RAM	:
F045	3456	ORG 0F045H	:
F045	3457	MI LABEL WORD ; TABLE OF ROUTINES WITHIN VIDEO I/O	:
F045 FCF0	3458	DW OFFSET SET_MODE	:
F047 CDF1	3459	DW OFFSET SET_CTYPE	:
F049 EEF1	3460	DW OFFSET SET_CPOS	:
F04B 39F2	3461	DW OFFSET READ_CURSOR	:
F04D 9CF7	3462	DW OFFSET READ_LPEN	:
F04F 17F2	3463	DW OFFSET ACT_DISP_PAGE	:
F051 96F2	3464	DW OFFSET SCROLL_UP	:
F053 38F3	3465	DW OFFSET SCROLL_DOWN	:
F055 74F3	3466	DW OFFSET READ_AC_CURRENT	:
F057 B9F3	3467	DW OFFSET WRITE_AC_CURRENT	:
F059 ECF3	3468	DW OFFSET WRITE_C_CURRENT	:
F05B 4EF2	3469	DW OFFSET SET_COLOR	:
F05D 2FF4	3470	DW OFFSET WRITE_DOT	:
F05F 1EF4	3471	DW OFFSET READ_DOT	:
F061 18F7	3472	DW OFFSET WRITE_TTY	:
F063 74F2	3473	DW OFFSET VIDEO_STATE	:
0020	3474	MIL EQU 9-MI	:
	3475		:
F065	3476	ORG 0F065H	:
F065	3477	VIDEO_ID PROC NEAR	:
F065 FB	3478	STI ; INTERRUPTS BACK ON	:
F066 FC	3479	CLD ; SET DIRECTION FORWARD	:
F067 06	3480	PUSH ES	:
F068 1E	3481	PUSH DS ; SAVE SEGMENT REGISTERS	:
F069 52	3482	PUSH DX	:
F06A 51	3483	PUSH CX	:
F06B 53	3484	PUSH BX	:

Appendix A

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LOC OBJ          LINE  SOURCE
F06C 56          3485      PUSH  SI
F06D 57          3486      PUSH  DI
F06E 50          3487      PUSH  AX          ; SAVE AX VALUE
F06F 0AC4        3488      MOV   AL,AH      ; GET INTO LOW BYTE
F071 32E4        3489      XOR   AH,AH      ; ZERO TO HIGH BYTE
F073 D1E0        3490      SAL  AX,1        ; *2 FOR TABLE LOOKUP
F075 8BF0        3491      MOV   SI,AX      ; PUT INTO SI FOR BRANCH
F077 3D2000      3492      CMP  AX,M1L     ; TEST FOR WITHIN RANGE
F07A 7204        3493      JB   M2          ; BRANCH AROUND BRANCH
F07C 58          3494      POP  AX          ; THROW AWAY THE PARAMETER
F07D E94501      3495      JMP  VIDEO_RETURN ; DO NOTHING IF NOT IN RANGE
F080            3496      M2:
F080 EBB0E        3497      CALL DDS
F083 B000B       3498      MOV  AX,0B800H  ; SEGMENT FOR COLOR CARD
F086 8B3E1000    3499      MOV  DI,EQUIP_FLAG ; GET EQUIPMENT SETTING
F08A 81E73000    3500      AND  DI,30H     ; ISOLATE CRT SWITCHES
F08E 83FF30      3501      CMP  DI,30H     ; IS SETTING FOR BW CARD?
F091 7502        3502      JNE  M3
F093 B4B0        3503      MOV  AH,0B0H   ; SEGMENT FOR BW CARD
F095            3504      M3:
F095 8EC0        3505      MOV  ES,AX      ; SET UP TO POINT AT VIDEO RAM AREAS
F097 58          3506      POP  AX          ; RECOVER VALUE
F098 8A264900    3507      MOV  AH,CRT_MODE ; GET CURRENT MODE INTO AH
F09C 2EFA445F0   3508      JMP  WORD PTR CS:[SI+OFFSET M1]
3509      VIDEO_IO      ENDP
3510      ;-----
3511      ; SET_MODE      :
3512      ;   THIS ROUTINE INITIALIZES THE ATTACHMENT TO      :
3513      ;   THE SELECTED MODE.  THE SCREEN IS BLANKED.      :
3514      ; INPUT      :
3515      ;   (AL) = MODE SELECTED (RANGE 0-9)      :
3516      ; OUTPUT      :
3517      ;   NONE      :
3518      ;-----
3519
3520      ;----- TABLES FOR USE IN SETTING OF MODE
3521
FOA4            3522      ORG   0F0A4H
FOA4            3523      VIDEO_PARMS LABEL BYTE
3524      ;----- INIT_TABLE
FOA4 38          3525      DB   38H,28H,2DH,0AH,1FH,6,19H ; SET UP FOR 40X25
FOA5 28
FOA6 2D
FOA7 0A
FOA8 1F
FOA9 06
FOAA 19
FOAB 1C
FOAC 02
FOAD 07
FOAE 06
FOAF 07
FOB0 00
FOB1 00
FOB2 00
FOB3 00
0010            3528      M4  EQU  $-VIDEO_PARMS
3529
FOB4 71          3530      DB   71H,50H,5AH,0AH,1FH,6,19H ; SET UP FOR 80X25
FOB5 50
FOB6 5A
FOB7 0A
FOB8 1F
FOB9 06
FOBA 19
FOBB 1C
FOBC 02
FOBD 07
FOBE 06
FOBF 07
FOC0 00
FOC1 00
FOC2 00
FOC3 00
3533
FOC4 38          3534      DB   38H,28H,2DH,0AH,7FH,6,64H ; SET UP FOR GRAPHICS
FOC5 28

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LOC OBJ	LINE	SOURCE			
F0C6 2D					
F0C7 0A					
F0C8 7F					
F0C9 06					
F0CA 64					
F0CB 70	3535	DB	70H,2,1,6,7		
F0CC 02					
F0CD 01					
F0CE 06					
F0CF 07					
F0D0 00	3536	DB	0,0,0,0		
F0D1 00					
F0D2 00					
F0D3 00					
	3537				
F0D4 61	3538	DB	61H,50H,52H,0FH,19H,6,19H	; SET UP FOR 80X25 B&W CARD	
F0D5 50					
F0D6 52					
F0D7 0F					
F0D8 19					
F0D9 06					
F0DA 19					
F0DB 19	3539	DB	19H,2,0DH,0BH,0CH		
F0DC 02					
F0DD 0D					
F0DE 0B					
F0DF 0C					
F0E0 00	3540	DB	0,0,0,0		
F0E1 00					
F0E2 00					
F0E3 00					
	3541				
F0E4	3542	M5	LABEL WORD	; TABLE OF REGEN LENGTHS	
F0E4 0008	3543		DK 2048	; 40X25	
F0E6 0010	3544		DK 4096	; 80X25	
F0E6 0040	3545		DW 16384	; GRAPHICS	
F0EA 0040	3546		DW 16384		
	3547				
	3548		;----- COLUMNS		
	3549				
F0EC	3550	M6	LABEL BYTE		
F0EC 28	3551		DB 40,40,80,80,40,40,80,80		
F0ED 28					
F0EE 50					
F0EF 50					
F0F0 28					
F0F1 28					
F0F2 50					
F0F3 50					
	3552				
	3553		;----- C_REG_TAB		
	3554				
F0F4	3555	M7	LABEL BYTE	; TABLE OF MODE SETS	
F0F4 2C	3556		DB 2CH,26H,2DH,29H,2AH,2EH,1EH,29H		
F0F5 28					
F0F6 2D					
F0F7 29					
F0F8 2A					
F0F9 2E					
F0FA 1E					
F0FB 29					
	3557				
F0FC	3558		SET_MODE PROC NEAR		
F0FC BAD403	3559	MOV	DX,0304H	; ADDRESS OF COLOR CARD	
F0FF B300	3560	MOV	BL,0	; MODE SET FOR COLOR CARD	
F101 83FF30	3561	CMP	DI,30H	; IS BW CARD INSTALLED	
F104 7506	3562	JNE	M8	; OK WITH COLOR	
F106 B007	3563	MOV	AL,7	; INDICATE BW CARD MODE	
F108 B2B4	3564	MOV	DL,0B4H	; ADDRESS OF BW CARD (3B4)	
F10A FEC3	3565	INC	BL	; MODE SET FOR BW CARD	
F10C	3566	M8:			
F10C 8AE0	3567	MOV	AH,AL	; SAVE MODE IN AH	
F10E A2490	3568	MOV	CRT_MODE,AL	; SAVE IN GLOBAL VARIABLE	
F111 89166300	3569	MOV	ADDR_6845,DX	; SAVE ADDRESS OF BASE	
F115 1E	3570	PUSH	DS	; SAVE POINTER TO DATA SEGMENT	
F116 50	3571	PUSH	AX	; SAVE MODE	
F117 52	3572	PUSH	DX	; SAVE OUTPUT PORT VALUE	

LOC OBJ	LINE	SOURCE	
F118 83C204	3573	ADD DX,4	; POINT TO CONTROL REGISTER
F118 8AC3	3574	MOV AL,BL	; GET MODE SET FOR CARD
F110 EE	3575	OUT DX,AL	; RESET VIDEO
F11E 5A	3576	POP DX	; BACK TO BASE REGISTER
F11F 2BC0	3577	SUB AX,AX	; SET UP FOR ABSO SEGMENT
F121 8ED8	3578	MOV DS,AX	; ESTABLISH VECTOR TABLE ADDRESSING
	3579	ASSUME DS:ABS0	
F123 C51E7400	3580	LDS BX,PARM_PTR	; GET POINTER TO VIDEO PARMS
F127 58	3581	POP AX	; RECOVER PARMS
	3582	ASSUME DS:CODE	
F128 891000	3583	MOV CX,M4	; LENGTH OF EACH ROW OF TABLE
F128 80FC02	3584	CMF AH,2	; DETERMINE WHICH ONE TO USE
F12E 7210	3585	JC M9	; MODE IS 0 OR 1
F130 0309	3586	ADD BX,CX	; MOVE TO NEXT ROW OF INIT TABLE
F132 80FC04	3587	CMF AH,4	
F135 7209	3588	JC M9	; MODE IS 2 OR 3
F137 0309	3589	ADD BX,CX	; MOVE TO GRAPHICS ROW OF INIT_TABLE
F139 80FC07	3590	CMF AH,7	
F13C 7202	3591	JC M9	; MODE IS 4,5, OR 6
F13E 0309	3592	ADD BX,CX	; MOVE TO BW CARD ROW OF INIT_TABLE
	3593		
	3594	;----- BX POINTS TO CORRECT ROW OF INITIALIZATION TABLE	
	3595		
F140	3596	M9:	; OUT_INIT
F140 50	3597	PUSH AX	; SAVE MODE IN AH
F141 32E4	3598	XOR AH,AH	; AH WILL SERVE AS REGISTER
	3599		; NUMBER DURING LOOP
	3600		
	3601	;----- LOOP THROUGH TABLE, OUTPUTTING REG ADDRESS, THEN VALUE FROM TABLE	
	3602		
F143	3603	M10:	; INIT LOOP
F143 8AC4	3604	MOV AL,AH	; GET 6845 REGISTER NUMBER
F145 EE	3605	OUT DX,AL	
F146 42	3606	INC DX	; POINT TO DATA PORT
F147 FEC4	3607	INC AH	; NEXT REGISTER VALUE
F149 8A07	3608	MOV AL,[BX]	; GET TABLE VALUE
F14B EE	3609	OUT DX,AL	; OUT TO CHIP
F14C 43	3610	INC BX	; NEXT IN TABLE
F14D 4A	3611	DEC DX	; BACK TO POINTER REGISTER
F14E E2F3	3612	LOOP M10	; DO THE WHOLE TABLE
F150 58	3613	POP AX	; GET MODE BACK
F151 1F	3614	POP DS	; RECOVER SEGMENT VALUE
	3615	ASSUME DS:DATA	
	3616		
	3617	;----- FILL REGEN AREA WITH BLANK	
	3618		
F152 33FF	3619	XOR DI,DI	; SET UP POINTER FOR REGEN
F154 893E4E00	3620	MOV CRT_START,DI	; START ADDRESS SAVED IN GLOBAL
F158 C06620000	3621	MOV ACTIVE_PAGE,0	; SET PAGE VALUE
F15D B90020	3622	MOV CX,8192	; NUMBER OF WORDS IN COLOR CARD
F160 80FC04	3623	CMF AH,4	; TEST FOR GRAPHICS
F163 7208	3624	JC M12	; NO_GRAPHICS_INIT
F165 80FC07	3625	CMF AH,7	; TEST FOR BW CARD
F168 7404	3626	JE M11	; BH_CARD_INIT
F16A 33C0	3627	XOR AX,AX	; FILL FOR GRAPHICS MODE
F16C EB05	3628	JMP SHORT M13	; CLEAR_BUFFER
F16E	3629	M11:	; BH_CARD_INIT
F16E B508	3630	MOV CH,08H	; BUFFER SIZE ON BW CARD
F170	3631	M12:	; NO_GRAPHICS_INIT
F170 B82007	3632	MOV AX,' '*7*256	; FILL CHAR FOR ALPHA
F173	3633	M13:	; CLEAR_BUFFER
F173 F3	3634	REP STOSW	; FILL THE REGEN BUFFER WITH BLANKS
F174 AB			
	3635		
	3636	;----- ENABLE VIDEO AND CORRECT PORT SETTING	
	3637		
F175 C70660000706	3638	MOV CURSOR_MODE,607H	; SET CURRENT CURSOR MODE
F17B A04900	3639	MOV AL,CRT_MODE	; GET THE MODE
F17E 32E4	3640	XOR AH,AH	; INTO AX REGISTER
F180 8BF0	3641	MOV SI,AX	; TABLE POINTER, INDEXED BY MODE
F182 8B166300	3642	MOV DX,ADDR_6845	; PREPARE TO OUTPUT TO
	3643		; VIDEO ENABLE PORT
F186 83C204	3644	ADD DX,4	
F189 2E8A84F4F0	3645	MOV AL,CS:[SI+OFFSET M7]	
F18E EE	3646	OUT DX,AL	; SET VIDEO ENABLE PORT
F18F A26500	3647	MOV CRT_MODE_SET,AL	; SAVE THAT VALUE
	3648		

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LOC OBJ          LINE SOURCE
3649             1----- DETERMINE NUMBER OF COLUMNS, BOTH FOR ENTIRE DISPLAY
3650             1----- AND THE NUMBER TO BE USED FOR TTY INTERFACE
3651
3652             MOV     AL,CS:[SI + OFFSET M6]
3653             XOR     AH,AH
3654             MOV     CRT_COLS,AX             ; NUMBER OF COLUMNS IN THIS SCREEN
3655
3656             1----- SET CURSOR POSITIONS
3657
3658             AND     SI,0EH                 ; WORD OFFSET INTO CLEAR LENGTH TABLE
3659             MOV     CX,CS:[SI + OFFSET M5] ; LENGTH TO CLEAR
3660             MOV     CRT_LEN,CX           ; SAVE LENGTH OF CRT -- NOT USED FOR BW
3661             MOV     CX,8                 ; CLEAR ALL CURSOR POSITIONS
3662             MOV     DI,OFFSET CURSOR_POSN
3663             PUSH    DS                   ; ESTABLISH SEGMENT
3664             POP     ES                   ; ADDRESSING
3665             XOR     AX,AX
3666             REP     STOSM                ; FILL WITH ZEROES
3667
3668             1----- SET UP OVERSCAN REGISTER
3669
3670             INC     DX                   ; SET OVERSCAN PORT TO A DEFAULT
3671             MOV     AL,30H               ; VALUE OF 30H FOR ALL MODES
3672                                     ; EXCEPT 640X200
3673             CMP     CRT_MODE,6           ; SEE IF THE MODE IS 640X200 BW
3674             JNZ     M14                  ; IF IT ISNT 640X200, THEN GOTO REGULAR
3675             MOV     AL,3FH               ; IF IT IS 640X200, THEN PUT IN 3FH
3676             M14:
3677             OUT     DX,AL                ; OUTPUT THE CORRECT VALUE TO 309 PORT
3678             MOV     CRT_PALETTE,AL      ; SAVE THE VALUE FOR FUTURE USE
3679
3680             1----- NORMAL RETURN FROM ALL VIDEO RETURNS
3681
3682             VIDEO_RETURN:
3683             POP     DI
3684             POP     SI
3685             POP     BX
3686             M15:
3687             POP     CX                   ; VIDEO_RETURN_C
3688             POP     DX
3689             POP     DS
3690             POP     ES                   ; RECOVER SEGMENTS
3691             IRET                            ; ALL DONE
3692             SET_MODE ENDP
3693             ;-----
3694             ; SET_CTYPE
3695             ; THIS ROUTINE SETS THE CURSOR VALUE
3696             ; INPUT
3697             ; (CX) HAS CURSOR VALUE CH-START LINE, CL-STOP LINE
3698             ; OUTPUT
3699             ; NONE
3700             ;-----
3701             SET_CTYPE PROC NEAR
3702             MOV     AH,10                 ; 6845 REGISTER FOR CURSOR SET
3703             MOV     CURSOR_MODE,CX       ; SAVE IN DATA AREA
3704             CALL    M16                  ; OUTPUT CX REG
3705             JMP     VIDEO_RETURN
3706
3707             1----- THIS ROUTINE OUTPUTS THE CX REGISTER TO THE 6845 REGS NAMED IN AH
3708
3709             M16:
3710             MOV     DX,ADDR_6845         ; ADDRESS REGISTER
3711             MOV     AL,AH                 ; GET VALUE
3712             OUT     DX,AL                 ; REGISTER SET
3713             INC     DX                   ; DATA REGISTER
3714             MOV     AL,CH                 ; DATA
3715             OUT     DX,AL
3716             DEC     DX
3717             MOV     AL,AH
3718             INC     AL                   ; POINT TO OTHER DATA REGISTER
3719             OUT     DX,AL                 ; SET FOR SECOND REGISTER
3720             INC     DX
3721             MOV     AL,CL                 ; SECOND DATA VALUE
3722             OUT     DX,AL
3723             RET
3724             SET_CTYPE ENDP

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LOC OBJ

LINE

SOURCE

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3725 ;-----
3726 ; SET_CPOS          :
3727 ;   THIS ROUTINE SETS THE CURRENT CURSOR   :
3728 ;   POSITION TO THE NEW X-Y VALUES PASSED :
3729 ; INPUT          :
3730 ;   DX - ROW,COLUMN OF NEW CURSOR        :
3731 ;   BH - DISPLAY PAGE OF CURSOR         :
3732 ; OUTPUT          :
3733 ;   CURSOR IS SET AT 6845 IF DISPLAY PAGE :
3734 ;   IS CURRENT DISPLAY                   :
3735 ;-----
F1EE      3736 SET_CPOS      PROC   NEAR
F1EE 8ACF 3737      MOV     CL,BH
F1F0 32ED 3738      XOR     CH,CH          ; ESTABLISH LOOP COUNT
F1F2 D1E1 3739      SAL     CX,1          ; WORD OFFSET
F1F4 8BF1 3740      MOV     SI,CX          ; USE INDEX REGISTER
F1F6 895A50 3741      MOV     [SI+OFFSET CURSOR_POSNI],DX ; SAVE THE POINTER
F1F9 383E6200 3742      CMP     ACTIVE_PAGE,BH
F1FD 7505 3743      JNZ     M17          ; SET_CPOS_RETURN
F1FF 8BC2 3744      MOV     AX,DX          ; GET ROW/COLUMN TO AX
F201 E80200 3745      CALL    M18          ; CURSOR_SET
F204      3746      M17:          ; SET_CPOS_RETURN
F204 EBBF 3747      JMP     VIDEO_RETURN
3748      SET_CPOS      ENDP
3749
3750 ;---- SET CURSOR POSITION, AX HAS ROW/COLUMN FOR CURSOR
3751
F206      3752      M18      PROC   NEAR
F206 E87C00 3753      CALL    POSITION          ; DETERMINE LOCATION IN REGEN BUFFER
F209 8BC8 3754      MOV     CX,AX
F20B 030E4E00 3755      ADD     CX,CRT_START      ; ADD IN THE START ADDR FOR THIS PAGE
F20F D1F9 3756      SAR     CX,1          ; DIVIDE BY 2 FOR CHAR ONLY COUNT
F211 B40E 3757      MOV     AH,14          ; REGISTER NUMBER FOR CURSOR
F213 E8C2FF 3758      CALL    M16          ; OUTPUT THE VALUE TO THE 6845
F216 C3 3759      RET
3760      M18      ENDP
3761 ;-----
3762 ; ACT_DISP_PAGE      :
3763 ;   THIS ROUTINE SETS THE ACTIVE DISPLAY PAGE, ALLOWING THE :
3764 ;   FULL USE OF THE RAM SET ASIDE FOR THE VIDEO ATTACHMENT :
3765 ; INPUT          :
3766 ;   AL HAS THE NEW ACTIVE DISPLAY PAGE
3767 ; OUTPUT          :
3768 ;   THE 6845 IS RESET TO DISPLAY THAT PAGE
3769 ;-----
F217      3770      ACT_DISP_PAGE  PROC   NEAR
F217 A26200 3771      MOV     ACTIVE_PAGE,AL ; SAVE ACTIVE PAGE VALUE
F21A 8B0E4C00 3772      MOV     CX,CRT_LEN      ; GET SAVED LENGTH OF REGEN BUFFER
F21E 98 3773      CBW     ; CONVERT AL TO WORD
F21F 50 3774      PUSH    AX          ; SAVE PAGE VALUE
F220 F7E1 3775      MUL     CX          ; DISPLAY PAGE TIMES REGEN LENGTH
F222 A34E00 3776      MOV     CRT_START,AX    ; SAVE START ADDRESS FOR
3777      ; LATER REQUIREMENTS
F225 8BC8 3778      MOV     CX,AX          ; START ADDRESS TO CX
F227 D1F9 3779      SAR     CX,1          ; DIVIDE BY 2 FOR 6845 HANDLING
F229 B40C 3780      MOV     AH,12          ; 6845 REGISTER FOR START ADDRESS
F22B E8AAFF 3781      CALL    M16
F22E 5B 3782      POP     BX          ; RECOVER PAGE VALUE
F22F D1E3 3783      SAL     BX,1          ; *2 FOR WORD OFFSET
F231 8B4750 3784      MOV     AX,[BX + OFFSET CURSOR_POSNI] ; GET CURSOR FOR THIS PAGE
F234 E8CFFF 3785      CALL    M18          ; SET THE CURSOR POSITION
F237 E8BC 3786      JMP     SHORT VIDEO_RETURN
3787      ACT_DISP_PAGE  ENDP
3788 ;-----
3789 ; READ_CURSOR        :
3790 ;   THIS ROUTINE READS THE CURRENT CURSOR VALUE FROM THE :
3791 ;   6845, FORMATS IT, AND SENDS IT BACK TO THE CALLER   :
3792 ; INPUT          :
3793 ;   BH - PAGE OF CURSOR
3794 ; OUTPUT          :
3795 ;   DX - ROW, COLUMN OF THE CURRENT CURSOR POSITION
3796 ;   CX - CURRENT CURSOR MODE
3797 ;-----
F239      3798      READ_CURSOR   PROC   NEAR
F239 8ADF 3799      MOV     BL,BH
F23B 32FF 3800      XOR     BH,BH
F23D D1E3 3801      SAL     BX,1          ; WORD OFFSET

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LOC OBJ          LINE    SOURCE
F23F 0B5750      3802      MOV     DX,(BX+OFFSET_CURSOR_POSN)
F242 0B0E6000    3803      MOV     CX,CURSOR_MODE
F246 5F          3804      POP     DI
F247 5E          3805      POP     SI
F248 5B          3806      POP     BX
F249 58          3807      POP     AX          ; DISCARD SAVED CX AND DX
F24A 58          3808      POP     AX
F24B 1F          3809      POP     DS
F24C 07          3810      POP     ES
F24D CF          3811      IRET
3812      READ_CURSOR  ENDP
3813      ;-----
3814      ; SET COLOR
3815      ; THIS ROUTINE WILL ESTABLISH THE BACKGROUND COLOR, THE OVERSCAN
3816      ; COLOR, AND THE FOREGROUND COLOR SET FOR MEDIUM RESOLUTION
3817      ; GRAPHICS
3818      ; INPUT
3819      ; (BH) HAS COLOR ID
3820      ; IF BH=0, THE BACKGROUND COLOR VALUE IS SET
3821      ; FROM THE LOW BITS OF BL (0-31)
3822      ; IF BH=1, THE PALETTE SELECTION IS MADE
3823      ; BASED ON THE LOW BIT OF BL:
3824      ; 0=GREEN, RED, YELLOW FOR COLORS 1,2,3
3825      ; 1=BLUE, CYAN, MAGENTA FOR COLORS 1,2,3
3826      ; (BL) HAS THE COLOR VALUE TO BE USED
3827      ; OUTPUT
3828      ; THE COLOR SELECTION IS UPDATED
3829      ;-----
F24E          3830      SET_COLOR  PROC  NEAR
F24E 0B166300    3831      MOV     DX,ADDR_6845          ; I/O PORT FOR PALETTE
F252 83C205     3832      ADD     DX,5                  ; OVERSCAN PORT
F255 A06600     3833      MOV     AL,CRT_PALETTE       ; GET THE CURRENT PALETTE VALUE
F256 0AFF      3834      OR      BH,BH                ; IS THIS COLOR 0?
F25A 750E      3835      JNZ     M20                  ; OUTPUT COLOR 1
3836
3837      ;---- HANDLE COLOR 0 BY SETTING THE BACKGROUND COLOR
3838
F25C 24E0      3839      AND     AL,0E0H              ; TURN OFF LOW 5 BITS OF CURRENT
F25E 80E31F    3840      AND     BL,01FH              ; TURN OFF HIGH 3 BITS OF INPUT VALUE
F261 0AC3      3841      OR      AL,BL                ; PUT VALUE INTO REGISTER
F263          3842      M19:    ; OUTPUT THE PALETTE
F263 EE        3843      OUT     DX,AL                ; OUTPUT COLOR SELECTION TO 3D9 PORT
F264 A26600    3844      MOV     CRT_PALETTE,AL       ; SAVE THE COLOR VALUE
F267 E95BFF    3845      JMP     VIDEO_RETURN
3846
3847      ;---- HANDLE COLOR 1 BY SELECTING THE PALETTE TO BE USED
3848
F26A          3849      M20:
F26A 24DF      3850      AND     AL,0DFH              ; TURN OFF PALETTE SELECT BIT
F26C D0E8      3851      SHR     BL,1                  ; TEST THE LOW ORDER BIT OF BL
F26E 73F3      3852      JNC     M19                  ; ALREADY DONE
F270 0C20      3853      OR      AL,20H              ; TURN ON PALETTE SELECT BIT
F272 EBFF      3854      JMP     M19                  ; GO DO IT
3855      SET_COLOR  ENDP
3856      ;-----
3857      ; VIDEO STATE
3858      ; RETURNS THE CURRENT VIDEO STATE IN AX
3859      ; AH = NUMBER OF COLUMNS ON THE SCREEN
3860      ; AL = CURRENT VIDEO MODE
3861      ; BH = CURRENT ACTIVE PAGE
3862      ;-----
F274          3863      VIDEO_STATE  PROC  NEAR
F274 8A264A00    3864      MOV     AH,BYTE PTR CRT_COLS ; GET NUMBER OF COLUMNS
F278 A06900     3865      MOV     AL,CRT_MODE          ; CURRENT MODE
F27B 8A3E6200    3866      MOV     BH,ACTIVE_PAGE       ; GET CURRENT ACTIVE PAGE
F27F 5F          3867      POP     DI                  ; RECOVER REGISTERS
F280 5E          3868      POP     SI
F281 59          3869      POP     CX          ; DISCARD SAVED BX
F282 E943FF     3870      JMP     M15                  ; RETURN TO CALLER
3871      VIDEO_STATE  ENDP
3872      ;-----
3873      ; POSITION
3874      ; THIS SERVICE ROUTINE CALCULATES THE REGEN
3875      ; BUFFER ADDRESS OF A CHARACTER IN THE ALPHA MODE
3876      ; INPUT
3877      ; AX = ROW, COLUMN POSITION
3878      ; OUTPUT

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LOC OBJ

LINE SOURCE

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3879 ; AX = OFFSET OF CHAR POSITION IN REGEN BUFFER ;
3880 ;-----
F285 3881 POSITION PROC NEAR
F285 53 3882 PUSH BX ; SAVE REGISTER
F286 8808 3883 MOV BX,AX
F288 8AC4 3884 MOV AL,AH ; ROWS TO AL
F28A F6264A00 3885 MUL BYTE PTR CRT_COLS ; DETERMINE BYTES TO ROW
F28E 32FF 3886 XOR BH,BH
F290 03C3 3887 ADD AX,BX ; ADD IN COLUMN VALUE
F292 D1E0 3888 SAL AX,1 ; * 2 FOR ATTRIBUTE BYTES
F294 5B 3889 POP BX
F295 C3 3890 RET
3891 POSITION ENDP
3892 ;-----
3893 ; SCROLL_UP ;
3894 ; THIS ROUTINE MOVES A BLOCK OF CHARACTERS UP ;
3895 ; ON THE SCREEN ;
3896 ; INPUT ;
3897 ; (AH) = CURRENT CRT MODE ;
3898 ; (AL) = NUMBER OF ROWS TO SCROLL ;
3899 ; (CX) = ROW/COLUMN OF UPPER LEFT CORNER ;
3900 ; (DX) = ROW/COLUMN OF LOWER RIGHT CORNER ;
3901 ; (BH) = ATTRIBUTE TO BE USED ON BLANKED LINE ;
3902 ; (DS) = DATA SEGMENT ;
3903 ; (ES) = REGEN BUFFER SEGMENT ;
3904 ; OUTPUT ;
3905 ; NONE -- THE REGEN BUFFER IS MODIFIED ;
3906 ;-----
F296 3907 ASSUME CS:CODE,DS:DATA,ES:DATA
F296 8AD8 3908 SCROLL_UP PROC NEAR
F298 80FC04 3909 MOV BL,AL ; SAVE LINE COUNT IN BL
F29B 7208 3910 CMP AH,4 ; TEST FOR GRAPHICS MODE
F29D 80FC07 3911 JC N1 ; HANDLE SEPARATELY
F2A0 7403 3912 CMP AH,7 ; TEST FOR BW CARD
F2A2 E9F001 3913 JE N1
F2A5 3914 JMP GRAPHICS_UP
F2A5 53 3915 N1: ; UP_CONTINUE
F2A6 8BC1 3916 PUSH BX ; SAVE FILL ATTRIBUTE IN BH
F2A8 E83700 3917 MOV AX,CX ; UPPER LEFT POSITION
F2AB 7431 3918 CALL SCROLL_POSITION ; DO SETUP FOR SCROLL
F2AD 03F0 3919 JZ N2 ; BLANK_FIELD
F2AF 8AE6 3920 ADD SI,AX ; FROM ADDRESS
F2B1 2AE3 3921 MOV AH,DH ; # ROWS IN BLOCK
F2B3 3922 SUB AH,BL ; # ROWS TO BE MOVED
F2B3 E87200 3923 N2: ; ROW_LOOP
F2B6 03F5 3924 CALL N10 ; MOVE ONE ROW
F2B8 03FD 3925 ADD SI,BP
F2BA FECC 3926 ADD DI,BP ; POINT TO NEXT LINE IN BLOCK
F2BC 75F5 3927 DEC AH ; COUNT OF LINES TO MOVE
F2BE 3928 JNZ N2 ; ROW_LOOP
F2BE 58 3929 N3: ; CLEAR_ENTRY
F2BF 8020 3930 POP AX ; RECOVER ATTRIBUTE IN AH
F2C1 3931 MOV AL,' ' ; FILL WITH BLANKS
F2C1 E86D00 3932 N4: ; CLEAR_LOOP
F2C4 03FD 3933 CALL N11 ; CLEAR THE ROW
F2C6 FECD 3934 ADD DI,BP ; POINT TO NEXT LINE
F2C8 75F7 3935 DEC BL ; COUNTER OF LINES TO SCROLL
F2CA 3936 JNZ N4 ; CLEAR_LOOP
F2CA E8710C 3937 N5: ; SCROLL_END
F2CD 803E490007 3938 CALL DDS
F2D2 7407 3939 CMP CRT_MODE,7 ; IS THIS THE BLACK AND WHITE CARD
F2D4 A86500 3940 JE N6 ; IF SO, SKIP THE MODE RESET
F2D7 BAD803 3941 MOV AL,CRT_MODE_SET ; GET THE VALUE OF THE MODE SET
F2DA EE 3942 MOV DX,03D8H ; ALWAYS SET COLOR CARD PORT
F2DB 3943 OUT DX,AL
F2DB E9E7FE 3944 N6: ; VIDEO_RET_HERE
F2DE 3945 JMP VIDEO_RETURN
F2DE 8ADE 3946 N7: ; BLANK_FIELD
F2E0 E8DC 3947 MOV BL,DH ; GET ROW COUNT
F2E2 3948 JMP N3 ; GO CLEAR THAT AREA
F2E7 7218 3949 SCROLL_UP ENDP
3950
3951 ;---- HANDLE COMMON SCROLL SET UP HERE
3952
F2E2 3953 SCROLL_POSITION PROC NEAR
F2E2 803E490002 3954 CMP CRT_MODE,2 ; TEST FOR SPECIAL CASE HERE
F2E7 7218 3955 JB N9 ; HAVE TO HANDLE 80X25 SEPARATELY

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LOC OBJ          LINE SOURCE
F2E9 803E490003 3956      CMP     CRT_MODE,3
F2EE 7711        3957      JA      N9
3958
3959      ;----- 80X25 COLOR CARD SCROLL
3960
F2F0 52          3961      PUSH   DX
F2F1 BADA03      3962      MOV    DX,3DAH      ; GUARANTEED TO BE COLOR CARD HERE
F2F4 50          3963      PUSH   AX
F2F5           3964      N8:
F2F5 EC          3965      IN     AL,DX        ; WAIT_DISP_ENABLE
F2F6 A808       3966      TEST  AL,8         ; GET PORT
F2F8 74FB       3967      JZ     NS          ; WAIT FOR VERTICAL RETRACE
F2FA B025       3968      MOV    AL,25H      ; WAIT_DISP_ENABLE
F2FC B208       3969      MOV    DL,0D6H     ; DX=308
F2FE EE         3970      OUT   DX,AL       ; TURN OFF VIDEO
F2FF 58         3971      POP   AX          ; DURING VERTICAL RETRACE
F300 5A         3972      POP   DX
F301           3973      N9:
F301 E81FF      3974      CALL  POSITION      ; CONVERT TO REGEN POINTER
F304 03064E00  3975      ADD   AX,CRT_START ; OFFSET OF ACTIVE PAGE
F308 8BF8       3976      MOV   DI,AX        ; TO ADDRESS FOR SCROLL
F30A 8BF0       3977      MOV   SI,AX        ; FROM ADDRESS FOR SCROLL
F30C 2BD1       3978      SUB   DX,CX        ; DX = #ROWS, #COLS IN BLOCK
F30E FEC6       3979      INC   DH
F310 FEC2       3980      INC   DL           ; INCREMENT FOR 0 ORIGIN
F312 32ED       3981      XOR   CH,CH        ; SET HIGH BYTE OF COUNT TO ZERO
F314 8B2E4A00  3982      MOV   BP,CRT_COLS  ; GET NUMBER OF COLUMNS IN DISPLAY
F318 03ED       3983      ADD   BP,BP        ; *2 FOR ATTRIBUTE BYTE
F31A 8AC3       3984      MOV   AL,BL        ; GET LINE COUNT
F31C F6264A00  3985      MUL  BYTE PTR CRT_COLS ; DETERMINE OFFSET TO FROM ADDRESS
F320 03C0       3986      ADD   AX,AX        ; *2 FOR ATTRIBUTE BYTE
F322 06         3987      PUSH  ES          ; ESTABLISH ADDRESSING TO REGEN BUFFER
F323 1F         3988      POP   DS          ; FOR BOTH POINTERS
F324 80FB00     3989      CMP   BL,0        ; 0 SCROLL MEANS BLANK FIELD
F327 C3         3990      RET              ; RETURN WITH FLAGS SET
3991      SCROLL_POSITION ENDP
3992
3993      ;----- MOVE_ROW
3994
F328           3995      N10  PROC  NEAR
F328 8ACA       3996      MOV   CL,DL        ; GET # OF COLS TO MOVE
F32A 56         3997      PUSH  SI
F32B 57         3998      PUSH  DI           ; SAVE START ADDRESS
F32C F3         3999      REP  MOVSW        ; MOVE THAT LINE ON SCREEN
F32D A5
F32E 5F         4000      POP   DI
F32F 5E         4001      POP   SI           ; RECOVER ADDRESSES
F330 C3         4002      RET
4003      N10  ENDP
4004
4005      ;----- CLEAR_ROW
4006
F331           4007      N11  PROC  NEAR
F331 8ACA       4008      MOV   CL,DL        ; GET # COLUMNS TO CLEAR
F333 57         4009      PUSH  DI
F334 F3         4010      REP  STOSW        ; STORE THE FILL CHARACTER
F335 AB
F336 5F         4011      POP   DI
F337 C3         4012      RET
4013      N11  ENDP
4014
4015      ;----- SCROLL_DOWN
4016      ; THIS ROUTINE MOVES THE CHARACTERS WITHIN A
4017      ; DEFINED BLOCK DOWN ON THE SCREEN, FILLING THE
4018      ; TOP LINES WITH A DEFINED CHARACTER
4019      ; INPUT
4020      ; (AH) = CURRENT CRT MODE
4021      ; (AL) = NUMBER OF LINES TO SCROLL
4022      ; (CX) = UPPER LEFT CORNER OF REGION
4023      ; (DX) = LOWER RIGHT CORNER OF REGION
4024      ; (BH) = FILL CHARACTER
4025      ; (DS) = DATA SEGMENT
4026      ; (ES) = REGEN SEGMENT
4027      ; OUTPUT
4028      ; NONE -- SCREEN IS SCROLLED
4029      ;-----
F338           4030      SCROLL_DOWN  PROC  NEAR

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LOC OBJ	LINE	SOURCE	
F330 FD	4031	STD	; DIRECTION FOR SCROLL DOWN
F339 8ADB	4032	MOV BL,AL	; LINE COUNT TO BL
F338 80FC04	4033	CHP AH,4	; TEST FOR GRAPHICS
F33E 7208	4034	JC N12	
F340 80FC07	4035	CHP AH,7	; TEST FOR BW CARD
F343 7403	4036	JE N12	
F345 E9A601	4037	JMP GRAPHICS_DOWN	
F348	4038	N12:	; CONTINUE DOWN
F348 53	4039	PUSH BX	; SAVE ATTRIBUTE IN BH
F349 8BC2	4040	MOV AX,DX	; LOWER RIGHT CORNER
F34B E894FF	4041	CALL SCROLL_POSITION	; GET REGEN LOCATION
F34E 7420	4042	JZ N16	
F350 2BF0	4043	SUB SI,AX	; SI IS FROM ADDRESS
F352 8AE6	4044	MOV AH,DH	; GET TOTAL # ROWS
F354 2AE3	4045	SUB AH,BL	; COUNT TO MOVE IN SCROLL
F356	4046	N13:	
F356 E8CFFF	4047	CALL N10	; MOVE ONE ROW
F359 2BF5	4048	SUB SI,BP	
F35B 2BFD	4049	SUB DI,BP	
F35D FECC	4050	DEC AH	
F35F 75F5	4051	JNZ N13	
F361	4052	N14:	
F361 58	4053	POP AX	; RECOVER ATTRIBUTE IN AH
F362 B020	4054	MOV AL,' '	
F364	4055	N15:	
F364 E8CAFF	4056	CALL N11	; CLEAR ONE ROW
F367 2BFD	4057	SUB DI,BP	; GO TO NEXT ROW
F369 FECB	4058	DEC BL	
F36B 75F7	4059	JNZ N15	
F36D E95AFF	4060	JMP N5	; SCROLL_END
F370	4061	N16:	
F370 8ADE	4062	MOV BL,DH	
F372 EBED	4063	JMP N14	
	4064	SCROLL_DOWN ENDP	
	4065	;	-----
	4066	; READ_AC_CURRENT	;
	4067	; THIS ROUTINE READS THE ATTRIBUTE AND CHARACTER	;
	4068	; AT THE CURRENT CURSOR POSITION AND RETURNS THEM	;
	4069	; TO THE CALLER	;
	4070	;INPUT	;
	4071	; (AH) = CURRENT CRT MODE	;
	4072	; (BH) = DISPLAY PAGE ( ALPHA MODES ONLY )	;
	4073	; (DS) = DATA SEGMENT	;
	4074	; (ES) = REGEN SEGMENT	;
	4075	;OUTPUT	;
	4076	; (AL) = CHAR READ	;
	4077	; (AH) = ATTRIBUTE READ	;
	4078	;	-----
	4079	ASSUME CS:CODE,DS:DATA,ES:DATA	
F374	4080	READ_AC_CURRENT PROC NEAR	
F374 80FC04	4081	CHP AH,4	; IS THIS GRAPHICS
F377 7208	4082	JC P1	
F379 80FC07	4083	CHP AH,7	; IS THIS BW CARD
F37C 7403	4084	JE P1	
F37E E9A802	4085	JMP GRAPHICS_READ	
F381	4086	P1:	; READ_AC_CONTINUE
F381 E81A00	4087	CALL FIND_POSITION	
F384 8BF3	4088	MOV SI,BX	; ESTABLISH ADDRESSING IN SI
	4089		
	4090	;	-----
	4091	;	WAIT FOR HORIZONTAL RETRACE
F386 8B166300	4092	MOV DX,ADDR_6845	; GET BASE ADDRESS
F38A 83C206	4093	ADD DX,6	; POINT AT STATUS PORT
F380 06	4094	PUSH ES	
F38E 1F	4095	POP DS	; GET SEGMENT FOR QUICK ACCESS
F38F	4096	P2:	; WAIT FOR RETRACE LOW
F38F EC	4097	IN AL,DX	; GET STATUS
F390 A801	4098	TEST AL,1	; IS HORZ RETRACE LOW
F392 75FB	4099	JNZ P2	; WAIT UNTIL IT IS
F394 FA	4100	CLI	; NO MORE INTERRUPTS
F395	4101	P3:	; WAIT FOR RETRACE HIGH
F395 EC	4102	IN AL,DX	; GET STATUS
F396 A801	4103	TEST AL,1	; IS IT HIGH
F398 74FB	4104	JZ P3	; WAIT UNTIL IT IS
F39A AD	4105	LODSW	; GET THE CHAR/ATTR
F39B E927FE	4106	JMP VIDEO_RETURN	
	4107	READ_AC_CURRENT ENDP	

LOC OBJ	LINE	SOURCE
	4108	
F39E	4109	FIND_POSITION PROC NEAR
F39E BACF	4110	MOV CL,BH ; DISPLAY PAGE TO CX
F3A0 32ED	4111	XOR CH,CH
F3A2 8BF1	4112	MOV SI,CX ; MOVE TO SI FOR INDEX
F3A4 D1E6	4113	SAL SI,1 ; * 2 FOR WORD OFFSET
F3A6 8B4450	4114	MOV AX,SI+ OFFSET CURSOR_POSH1 ; GET ROW/COLUMN OF THAT PAGE
F3A9 330B	4115	XOR BX,BX ; SET START ADDRESS TO ZERO
F3AB E306	4116	JCXZ P5 ; NO_PAGE
F3AD	4117	P4: ; PAGE_LOOP
F3AD 031E4C00	4118	ADD BX,CRT_LEN ; LENGTH OF BUFFER
F3B1 E2FA	4119	LOOP P4
F3B3	4120	P5: ; NO_PAGE
F3B3 EBCFFE	4121	CALL POSITION ; DETERMINE LOCATION IN REGEN
F3B6 030B	4122	ADD BX,AX ; ADD TO START OF REGEN
F3B8 C3	4123	RET
	4124	FIND_POSITION ENDP
	4125	-----
	4126	; WRITE_AC_CURRENT :
	4127	; THIS ROUTINE WRITES THE ATTRIBUTE :
	4128	; AND CHARACTER AT THE CURRENT CURSOR :
	4129	; POSITION :
	4130	; INPUT :
	4131	; (AH) = CURRENT CRT MODE :
	4132	; (BH) = DISPLAY PAGE :
	4133	; (CX) = COUNT OF CHARACTERS TO WRITE :
	4134	; (AL) = CHAR TO WRITE :
	4135	; (BL) = ATTRIBUTE OF CHAR TO WRITE :
	4136	; (DS) = DATA SEGMENT :
	4137	; (ES) = REGEN SEGMENT :
	4138	; OUTPUT :
	4139	; NONE :
	4140	-----
F3B9	4141	WRITE_AC_CURRENT PROC NEAR
F3B9 80FC04	4142	CMF AH,4 ; IS THIS GRAPHICS
F3BC 7208	4143	JC P6
F3BE 80FC07	4144	CMF AH,7 ; IS THIS BW CARD
F3C1 7403	4145	JE P6
F3C3 E9B201	4146	JMP GRAPHICS_WRITE
F3C6	4147	P6: ; WRITE_AC_CONTINUE
F3C6 8AE3	4148	MOV AH,BL ; GET ATTRIBUTE TO AH
F3C8 50	4149	PUSH AX ; SAVE ON STACK
F3C9 51	4150	PUSH CX ; SAVE WRITE COUNT
F3CA E801FF	4151	CALL FIND_POSITION
F3CD 88FB	4152	MOV DI,BX ; ADDRESS TO DI REGISTER
F3CF 59	4153	POP CX ; WRITE COUNT
F3D0 58	4154	POP BX ; CHARACTER IN BX REG
F3D1	4155	P7: ; WRITE_LOOP
	4156	
	4157	;----- WAIT FOR HORIZONTAL RETRACE
	4158	
F3D1 8B166300	4159	MOV DX,ADDR_6845 ; GET BASE ADDRESS
F3D5 83C206	4160	ADD DX,6 ; POINT AT STATUS PORT
F3D8	4161	P8: ;
F3D8 EC	4162	IN AL,DX ; GET STATUS
F3D9 A801	4163	TEST AL,1 ; IS IT LOW
F3DB 75FB	4164	JNZ P8 ; WAIT UNTIL IT IS
F3DD FA	4165	CLI ; NO MORE INTERRUPTS
F3DE	4166	P9: ;
F3DE EC	4167	IN AL,DX ; GET STATUS
F3DF A801	4168	TEST AL,1 ; IS IT HIGH
F3E1 74FB	4169	JZ P9 ; WAIT UNTIL IT IS
F3E3 8BC3	4170	MOV AX,BX ; RECOVER THE CHAR/ATTR
F3E5 AB	4171	STOSW ; PUT THE CHAR/ATTR
F3E6 FB	4172	STI ; INTERRUPTS BACK ON
F3E7 E2E8	4173	LOOP P7 ; AS MANY TIMES AS REQUESTED
F3E9 E9D9FD	4174	JMP VIDEO_RETURN
	4175	WRITE_AC_CURRENT ENDP
	4176	-----
	4177	; WRITE_C_CURRENT :
	4178	; THIS ROUTINE WRITES THE CHARACTER AT :
	4179	; THE CURRENT CURSOR POSITION, ATTRIBUTE :
	4180	; UNCHANGED :
	4181	; INPUT :
	4182	; (AH) = CURRENT CRT MODE :
	4183	; (BH) = DISPLAY PAGE :
	4184	; (CX) = COUNT OF CHARACTERS TO WRITE :

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LOC OBJ          LINE   SOURCE

4185             ;      (AL) = CHAR TO WRITE          :
4186             ;      (DS) = DATA SEGMENT         :
4187             ;      (ES) = REGEN SEGMENT         :
4188             ;      OUTPUT                        :
4189             ;      NONE                          :
4190             ;-----
F3EC             4191     WRITE_C_CURRENT PROC   NEAR
F3EC 80FC04      4192     CMP      AH,4           ; IS THIS GRAPHICS
F3EF 7208       4193     JC      P10
F3F1 80FC07     4194     CMP      AH,7           ; IS THIS BW CARD
F3F4 7403       4195     JE      P10
F3F6 E97F01     4196     JMP      GRAPHICS_WRITE
F3F9           4197     P10:
F3F9 50          4198     PUSH   AX               ; SAVE ON STACK
F3FA 51         4199     PUSH   CX               ; SAVE WRITE COUNT
F3FB E8A0FF     4200     CALL   FIND_POSITION
F3FE 88FB       4201     MOV    DI,BX            ; ADDRESS TO DI
F400 59         4202     PDP   CX               ; WRITE COUNT
F401 5B         4203     POP    BX              ; BL HAS CHAR TO WRITE
F402           4204     P11:                    ; WRITE_LOOP
4205
4206             ;----- WAIT FOR HORIZONTAL RETRACE
4207
F402 8B166300    4208     MOV    DX,ADDR_6845    ; GET BASE ADDRESS
F406 83C206     4209     ADD    DX,6            ; POINT AT STATUS PORT
F409           4210     P12:
F409 EC         4211     IN     AL,DX           ; GET STATUS
F40A A801       4212     TEST   AL,1            ; IS IT LOW
F40C 75FB       4213     JNZ   P12              ; WAIT UNTIL IT IS
F40E FA         4214     CLI    ; NO MORE INTERRUPTS
F40F           4215     P13:
F40F EC         4216     IN     AL,DX           ; GET STATUS
F410 A801       4217     TEST   AL,1            ; IS IT HIGH
F412 74FB       4218     JZ    P13              ; WAIT UNTIL IT IS
F414 8AC3       4219     MOV    AL,BL           ; RECOVER CHAR
F416 AA         4220     STOSB ; PUT THE CHAR/ATTR
F417 FB         4221     STI    ; INTERRUPTS BACK ON
F418 47         4222     INC    DI              ; BUMP POINTER PAST ATTRIBUTE
F419 E2E7       4223     LOOP  P11              ; AS MANY TIMES AS REQUESTED
F41B E9A7FD     4224     JMP    VIDEO_RETURN
4225     WRITE_C_CURRENT ENDP
4226             ;-----
4227             ; READ DOT -- WRITE DOT :
4228             ; THESE ROUTINES WILL WRITE A DOT, OR READ THE DOT AT :
4229             ; THE INDICATED LOCATION :
4230             ; ENTRY -- :
4231             ; DX = ROW (0-199) (THE ACTUAL VALUE DEPENDS ON THE MODE) :
4232             ; CX = COLUMN ( 0-639) ( THE VALUES ARE NOT RANGE CHECKED ) :
4233             ; AL = DOT VALUE TO WRITE (1,2 OR 4 BITS DEPENDING ON MODE, :
4234             ; REQ'D FOR WRITE DOT ONLY, RIGHT JUSTIFIED) :
4235             ; BIT 7 OF AL-1 INDICATES XOR THE VALUE INTO THE LOCATION :
4236             ; DS = DATA SEGMENT :
4237             ; ES = REGEN SEGMENT :
4238             ; :
4239             ; EXIT :
4240             ; AL = DOT VALUE READ, RIGHT JUSTIFIED, READ ONLY :
4241             ;-----
4242             ASSUME CS:CODE,DS:DATA,ES:DATA
F41E           4243     READ_DOT PROC   NEAR
F41E E83100     4244     CALL   R3               ; DETERMINE BYTE POSITION OF DOT
F421 268A04     4245     MOV    AL,ES:[SI]      ; GET THE BYTE
F424 22C4       4246     AND    AL,AH           ; MASK OFF THE OTHER BITS IN THE BYTE
F426 D2E0       4247     SHL   AL,CL           ; LEFT JUSTIFY THE VALUE
F428 8ACE       4248     MOV    CL,DH           ; GET NUMBER OF BITS IN RESULT
F42A D2C0       4249     ROL   AL,CL           ; RIGHT JUSTIFY THE RESULT
F42C E996FD     4250     JMP    VIDEO_RETURN    ; RETURN FROM VIDEO IO
4251     READ_DOT ENDP
4252
F42F           4253     WRITE_DOT PROC   NEAR
F42F 50         4254     PUSH   AX               ; SAVE DOT VALUE
F430 50         4255     PUSH   AX               ; TWICE
F431 E81E00     4256     CALL   R3               ; DETERMINE BYTE POSITION OF THE DOT
F434 D2E8       4257     SHR   AL,CL           ; SHIFT TO SET UP THE BITS FOR OUTPUT
F436 22C4       4258     AND    AL,AH           ; STRIP OFF THE OTHER BITS
F438 26A0C      4259     MOV    CL,ES:[SI]      ; GET THE CURRENT BYTE
F43B 5B         4260     POP    BX              ; RECOVER XOR FLAG
F43C F6C380     4261     TEST   BL,80H          ; IS IT ON

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LOC OBJ	LINE	SOURCE
F43F 750D	4262	JNZ R2 ; YES, XOR THE DOT
F441 F604	4263	NOT AH ; SET THE MASK TO REMOVE THE
F443 22CC	4264	AND CL,AH ; INDICATED BITS
F445 0AC1	4265	OR AL,CL ; OR IN THE NEW VALUE OF THOSE BITS
F447	4266	R1: ; FINISH_DOT
F447 268804	4267	MOV ES:[SI],AL ; RESTORE THE BYTE IN MEMORY
F44A 58	4268	POP AX
F44B E977FD	4269	JMP VIDEO_RETURN ; RETURN FROM VIDEO IO
F44E	4270	R2: ; XOR_DOT
F44E 32C1	4271	XOR AL,CL ; EXCLUSIVE OR THE DOTS
F450 EBF5	4272	JMP R1 ; FINISH UP THE WRITING
	4273	WRITE_DOT ENDP
	4274	;
	4275	;-----
	4275	; THIS SUBROUTINE DETERMINES THE REGEN BYTE LOCATION :
	4276	; OF THE INDICATED ROW COLUMN VALUE IN GRAPHICS MODE. :
	4277	; ENTRY -- :
	4278	; DX = ROW VALUE (0-199) :
	4279	; CX = COLUMN VALUE (0-639) :
	4280	; EXIT -- :
	4281	; SI = OFFSET INTO REGEN BUFFER FOR BYTE OF INTEREST :
	4282	; AH = MASK TO STRIP OFF THE BITS OF INTEREST :
	4283	; CL = BITS TO SHIFT TO RIGHT JUSTIFY THE MASK IN AH :
	4284	; DH = # BITS IN RESULT :
	4285	;
	4285	;-----
F452	4286	R3 PROC NEAR
F452 53	4287	PUSH BX ; SAVE BX DURING OPERATION
F453 50	4288	PUSH AX ; WILL SAVE AL DURING OPERATION
	4289	
	4290	;---- DETERMINE 1ST BYTE IN IDICATED ROW BY MULTIPLYING ROW VALUE BY 40
	4291	;---- ( LOW BIT OF ROW DETERMINES EVEN/ODD, 80 BYTES/ROW
	4292	
F454 8028	4293	MOV AL,40
F456 52	4294	PUSH DX ; SAVE ROW VALUE
F457 80E2FE	4295	AND DL,0FEH ; STRIP OFF ODD/EVEN BIT
F45A F6E2	4296	MUL DL ; AX HAS ADDRESS OF 1ST BYTE
	4297	; OF INDICATED ROW
F45C 5A	4298	POP DX ; RECOVER IT
F45D F6C201	4299	TEST DL,1 ; TEST FOR EVEN/ODD
F460 7403	4300	JZ R4 ; JUMP IF EVEN ROW
F462 050020	4301	ADD AX,2000H ; OFFSET TO LOCATION OF ODD ROWS
F465	4302	R4: ; EVEN_ROW
F465 8BF0	4303	MOV SI,AX ; MOVE POINTER TO SI
F467 58	4304	POP AX ; RECOVER AL VALUE
F468 8BD1	4305	MOV DX,CX ; COLUMN VALUE TO DX
	4306	
	4307	;---- DETERMINE GRAPHICS MODE CURRENTLY IN EFFECT
	4308	
	4309	;
	4310	; SET UP THE REGISTERS ACCORDING TO THE MODE :
	4311	; CH = MASK FOR LOW OF COLUMN ADDRESS ( 7/3 FOR HIGH/MED RES) :
	4312	; CL = # OF ADDRESS BITS IN COLUMN VALUE ( 3/2 FOR H/M) :
	4313	; BL = MASK TO SELECT BITS FROM POINTED BYTE (80H/C0H FOR H/M) :
	4314	; BH = NUMBER OF VALID BITS IN POINTED BYTE ( 1/2 FOR H/M) :
	4315	;
	4316	;
F46A B8C002	4317	MOV BX,2C0H
F46D B9D203	4318	MOV CX,302H ; SET PARMS FOR MED RES
F470 803E490006	4319	CMP CRT_MODE,6
F475 7206	4320	JC R5 ; HANDLE IF MED ARES
F477 B88001	4321	MOV BX,180H
F47A B90307	4322	MOV CX,703H ; SET PARMS FOR HIGH RES
	4323	
	4324	;---- DETERMINE BIT OFFSET IN BYTE FROM COLUMN MASK
	4325	
F47D	4326	R5:
F47D 22EA	4327	AND CH,DL ; ADDRESS OF PEL WITHIN BYTE TO CH
	4328	
	4329	;
	4330	;---- DETERMINE BYTE OFFSET FOR THIS LOCATION IN COLUMN
	4331	
F47F D3EA	4331	SHR DX,CL ; SHIFT BY CORRECT AMOUNT
F481 03F2	4332	ADD SI,DX ; INCREMENT THE POINTER
F483 8AF7	4333	MOV DH,BH ; GET THE # OF BITS IN RESULT TO DH
	4334	
	4335	;
	4336	;
	4337	;
F485 2AC9	4337	SUB CL,CL ; ZERO INTO STORAGE LOCATION
F487	4338	R6:

LOC OBJ	LINE	SOURCE
F407 D0C8	4339	ROR AL,1 ; LEFT JUSTIFY THE VALUE
	4340	; IN AL (FOR WRITE)
F409 02CD	4341	ADD CL,CH ; ADD IN THE BIT OFFSET VALUE
F40B FECF	4342	DEC BH ; LOOP CONTROL
F40D 75F8	4343	JNZ R6 ; ON EXIT, CL HAS SHIFT COUNT
	4344	; TO RESTORE BITS
F40F 8AE3	4345	MOV AH,BL ; GET MASK TO AH
F491 D2EC	4346	SHR AH,CL ; MOVE THE MASK TO CORRECT LOCATION
F493 5B	4347	POP BX ; RECOVER REG
F494 C3	4348	RET ; RETURN WITH EVERYTHING SET UP
	4349	R3 ENDP
	4350	;
	4351	; SCROLL UP
	4352	; THIS ROUTINE SCROLLS UP THE INFORMATION ON THE CRT
	4353	; ENTRY
	4354	; CH,CL = UPPER LEFT CORNER OF REGION TO SCROLL
	4355	; DH,DL = LOWER RIGHT CORNER OF REGION TO SCROLL
	4356	; BOTH OF THE ABOVE ARE IN CHARACTER POSITIONS
	4357	; BH = FILL VALUE FOR BLANKED LINES
	4358	; AL = # LINES TO SCROLL (AL=0 MEANS BLANK THE ENTIRE
	4359	; FIELD)
	4360	; DS = DATA SEGMENT
	4361	; ES = REGEN SEGMENT
	4362	; EXIT
	4363	; NOTHING, THE SCREEN IS SCROLLED
	4364	;
F495	4365	GRAPHICS_UP PROC NEAR
F495 8AD8	4366	MOV BL,AL ; SAVE LINE COUNT IN BL
F497 8BC1	4367	MOV AX,CX ; GET UPPER LEFT POSITION INTO AX REG
	4368	
	4369	;
	4370	;----- USE CHARACTER SUBROUTINE FOR POSITIONING
	4371	;----- ADDRESS RETURNED IS MULTIPLIED BY 2 FROM CORRECT VALUE
F499 E86902	4372	CALL GRAPH_POSN
F49C 8BF8	4373	MOV DI,AX ; SAVE RESULT AS DESTINATION ADDRESS
	4374	
	4375	;
	4376	;----- DETERMINE SIZE OF WINDOW
F49E 2BD1	4377	SUB DX,CX
F4A0 81C20101	4378	ADD DX,101H ; ADJUST VALUES
F4A4 D0E6	4379	SAL DH,1 ; MULTIPLY # ROWS BY 4
	4380	; SINCE 8 VERT DOTS/CHAR
F4A6 D0E6	4381	SAL DH,1 ; AND EVEN/ODD ROWS
	4382	
	4383	;
	4384	;----- DETERMINE CRT MODE
F4A8 803E490006	4385	CMPL CRT_MODE,6 ; TEST FOR MEDIUM RES
F4AD 7304	4386	JNC R7 ; FIND_SOURCE
	4387	
	4388	;
	4389	;----- MEDIUM RES UP
F4AF D0E2	4390	SAL DL,1 ; # COLUMNS * 2, SINCE 2 BYTES/CHAR
F4B1 D1E7	4391	SAL DI,1 ; OFFSET #2 SINCE 2 BYTES/CHAR
	4392	
	4393	;
	4394	;----- DETERMINE THE SOURCE ADDRESS IN THE BUFFER
F4B3	4395	R7: ; FIND_SOURCE
F4B3 06	4396	PUSH ES ; GET SEGMENTS BOTH POINTING TO REGEN
F4B4 1F	4397	POP DS
F4B5 2AED	4398	SUB CH,CH ; ZERO TO HIGH OF COUNT REG
F4B7 D0E3	4399	SAL BL,1 ; MULTIPLY NUMBER OF LINES BY 4
F4B9 D0E3	4400	SAL BL,1
F4BB 742D	4401	JZ R11 ; IF ZERO, THEN BLANK ENTIRE FIELD
F4BD 8AC3	4402	MOV AL,BL ; GET NUMBER OF LINES IN AL
F4BF B450	4403	MOV AH,80 ; 80 BYTES/ROW
F4C1 F6E4	4404	MUL AH ; DETERMINE OFFSET TO SOURCE
F4C3 8BF7	4405	MOV SI,DI ; SET UP SOURCE
F4C5 03F0	4406	ADD SI,AX ; ADD IN OFFSET TO IT
F4C7 8AE6	4407	MOV AH,DH ; NUMBER OF ROWS IN FIELD
F4C9 2AE3	4408	SUB AH,BL ; DETERMINE NUMBER TO MOVE
	4409	
	4410	;
	4411	;----- LOOP THROUGH, MOVING ONE ROW AT A TIME, BOTH EVEN AND ODD FIELDS
F4CB	4412	R8: ; ROW_LOOP
F4CB E8B000	4413	CALL R17 ; MOVE ONE ROW
F4CE 81EEB01F	4414	SUB SI,2000H-80 ; MOVE TO NEXT ROW
F4D2 81EEF01F	4415	SUB DI,2000H-80

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LOC OBJ          LINE  SOURCE
F0D6 FECC        4416          DEC  AH          ; NUMBER OF ROWS TO MOVE
F0D8 75F1        4417          JNZ  R8          ; CONTINUE TILL ALL MOVED
4418
4419          ;----- FILL IN THE VACATED LINE(S)
4420
F4DA             4421          R9:             ; CLEAR_ENTRY
F4DA 8AC7        4422          MOV  AL,BH      ; ATTRIBUTE TO FILL WITH
F4DC             4423          R10:            ;
F4DC E88800      4424          CALL R18        ; CLEAR THAT ROW
F4DF 81EFB01F    4425          SUB  DI,2000H-80 ; POINT TO NEXT LINE
F4E3 FECC        4426          DEC  BL          ; NUMBER OF LINES TO FILL
F4E5 75F5        4427          JNZ  R10        ; CLEAR_LOOP
F4E7 E90BFC      4428          JMP  VIDE0_RETURN ; EVERYTHING DONE
F4EA             4429          R11:            ; BLANK_FIELD
F4EA 8ADE        4430          MOV  BL,DH      ; SET BLANK COUNT TO
4431          ; EVERYTHING IN FIELD
F4EC EBEC        4432          JMP  R9         ; CLEAR THE FIELD
4433          GRAPHICS_UP  ENDP
4434          ;-----
4435          ; SCROLL DOWN          ;
4436          ; THIS ROUTINE SCROLLS DOWN THE INFORMATION ON THE CRT ;
4437          ; ENTRY              ;
4438          ; CH,CL = UPPER LEFT CORNER OF REGION TO SCROLL      ;
4439          ; DH,DL = LOWER RIGHT CORNER OF REGION TO SCROLL   ;
4440          ; BOTH OF THE ABOVE ARE IN CHARACTER POSITIONS     ;
4441          ; BH = FILL VALUE FOR BLANKED LINES                ;
4442          ; AL = # LINES TO SCROLL (AL=0 MEANS BLANK THE ENTIRE ;
4443          ; FIELD)                                           ;
4444          ; DS = DATA SEGMENT                               ;
4445          ; ES = REGEN SEGMENT                               ;
4446          ; EXIT                                              ;
4447          ; NOTHING, THE SCREEN IS SCROLLED                  ;
4448          ;-----
F4EE             4449          GRAPHICS_DOWN  PROC  NEAR
F4EE FD          4450          STD          ; SET DIRECTION
F4EF 8ADB        4451          MOV  BL,AL     ; SAVE LINE COUNT IN BL
F4F1 8BC2        4452          MOV  AX,DX     ; GET LOWER RIGHT POSITION INTO AX REG
4453
4454          ;----- USE CHARACTER SUBROUTINE FOR POSITIONING
4455          ;----- ADDRESS RETURNED IS MULTIPLIED BY 2 FROM CORRECT VALUE
4456
F4F3 E80F02      4457          CALL GRAPH_POSN
F4F6 8BF8        4458          MOV  DI,AX     ; SAVE RESULT AS DESTINATION ADDRESS
4459
4460          ;----- DETERMINE SIZE OF WINDOW
4461
F4F8 2B01        4462          SUB  DX,CX
F4FA 81C20101    4463          ADD  DX,101H   ; ADJUST VALUES
F4FE D0E6        4464          SAL  DH,1     ; MULTIPLY # ROWS BY 4
4465          ; SINCE 8 VERT DOTS/CHAR
F500 D0E6        4466          SAL  DH,1     ; AND EVEN/ODD ROWS
4467
4468          ;----- DETERMINE CRT MODE
4469
F502 803E490006  4470          CHP  CRT_MODE,6 ; TEST FOR MEDIUM RES
F507 7305        4471          JNC  R12      ; FIND_SOURCE_DOWN
4472
4473          ;----- MEDIUM RES DOWN
4474
F509 D0E2        4475          SAL  DL,1     ; # COLUMNS * 2, SINCE
4476          ; 2 BYTES/CHAR (OFFSET OK)
F50B D1E7        4477          SAL  DI,1     ; OFFSET #2 SINCE 2 BYTES/CHAR
F50D 47          4478          INC  DI       ; POINT TO LAST BYTE
4479
4480          ;----- DETERMINE THE SOURCE ADDRESS IN THE BUFFER
4481
F50E             4482          R12:            ; FIND_SOURCE_DOWN
F50E 06          4483          PUSH ES       ; BOTH SEGMENTS TO REGEN
F50F 1F          4484          POP  DS
F510 2AED        4485          SUB  CH,CH    ; ZERO TO HIGH OF COUNT REG
F512 81C7F000    4486          ADD  DI,240   ; POINT TO LAST ROW OF PIXELS
F516 D0E3        4487          SAL  BL,1     ; MULTIPLY NUMBER OF LINES BY 4
F518 D0E3        4488          SAL  BL,1
F51A 742E        4489          JZ   R16     ; IF ZERO, THEN BLANK ENTIRE FIELD
F51C 8AC3        4490          MOV  AL,BL    ; GET NUMBER OF LINES IN AL
F51E B450        4491          MOV  AH,80    ; 80 BYTES/ROW
F520 F6E4        4492          MUL  AH      ; DETERMINE OFFSET TO SOURCE

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LOC OBJ	LINE	SOURCE	
F522 8BF7	4493	MOV SI,DI	; SET UP SOURCE
F524 2BF0	4494	SUB SI,AX	; SUBTRACT THE OFFSET
F526 8AE6	4495	MOV AH,DH	; NUMBER OF ROWS IN FIELD
F528 2AE3	4496	SUB AH,BL	; DETERMINE NUMBER TO MOVE
	4497		
	4498	;----- LOOP THROUGH, MOVING ONE ROW AT A TIME, BOTH EVEN AND ODD FIELDS	
	4499		
F52A	4500	R13:	; ROW_LOOP_DOWN
F52A E82100	4501	CALL R17	; MOVE ONE ROW
F52D 81EE5020	4502	SUB SI,2000H*80	; MOVE TO NEXT ROW
F531 81EF5020	4503	SUB DI,2000H*80	
F535 FECC	4504	DEC AH	; NUMBER OF ROWS TO MOVE
F537 75F1	4505	JNZ R13	; CONTINUE TILL ALL MOVED
	4506		
	4507	;----- FILL IN THE VACATED LINE(S)	
	4508		
F539	4509	R14:	; CLEAR_ENTRY_DOWN
F539 8AC7	4510	MOV AL,BH	; ATTRIBUTE TO FILL WITH
F53B	4511	R15:	; CLEAR_LOOP_DOWN
F53B E82900	4512	CALL R18	; CLEAR A ROW
F53E 81EF5020	4513	SUB DI,2000H*80	; POINT TO NEXT LINE
F542 FECB	4514	DEC BL	; NUMBER OF LINES TO FILL
F544 75F5	4515	JNZ R15	; CLEAR_LOOP_DOWN
F546 FC	4516	CLD	; RESET THE DIRECTION FLAG
F547 E97BFC	4517	JMP VIDEO_RETURN	; EVERYTHING DONE
F54A	4518	R16:	; BLANK_FIELD_DOWN
F54A 8ADE	4519	MOV BL,DH	; SET BLANK COUNT TO
	4520		; EVERYTHING IN FIELD
F54C EBEB	4521	JMP R14	; CLEAR THE FIELD
	4522	GRAPHICS_DOWN	ENDP
	4523		
	4524	;----- ROUTINE TO MOVE ONE ROW OF INFORMATION	
	4525		
F54E	4526	R17	PROC NEAR
F54E 8ACA	4527	MOV CL,DL	; NUMBER OF BYTES IN THE ROW
F550 56	4528	PUSH SI	
F551 57	4529	PUSH DI	; SAVE POINTERS
F552 F3	4530	REP MOVSB	; MOVE THE EVEN FIELD
F553 A4			
F554 5F	4531	POP DI	
F555 5E	4532	POP SI	
F556 81C60020	4533	ADD SI,2000H	
F55A 81C70020	4534	ADD DI,2000H	; POINT TO THE ODD FIELD
F55E 56	4535	PUSH SI	
F55F 57	4536	PUSH DI	; SAVE THE POINTERS
F560 8ACA	4537	MOV CL,DL	; COUNT BACK
F562 F3	4538	REP MOVSB	; MOVE THE ODD FIELD
F563 A4			
F564 5F	4539	POP DI	
F565 5E	4540	POP SI	; POINTERS BACK
F566 C3	4541	RET	; RETURN TO CALLER
	4542	R17	ENDP
	4543		
	4544	;----- CLEAR A SINGLE ROW	
	4545		
F567	4546	R18	PROC NEAR
F567 8ACA	4547	MOV CL,DL	; NUMBER OF BYTES IN FIELD
F569 57	4548	PUSH DI	; SAVE POINTER
F56A F3	4549	REP STOSB	; STORE THE NEW VALUE
F56B AA			
F56C 5F	4550	POP DI	; POINTER BACK
F56D 81C70020	4551	ADD DI,2000H	; POINT TO ODD FIELD
F571 57	4552	PUSH DI	
F572 8ACA	4553	MOV CL,DL	
F574 F3	4554	REP STOSB	; FILL THE ODD FIELD
F575 AA			
F576 5F	4555	POP DI	
F577 C3	4556	RET	; RETURN TO CALLER
	4557	R18	ENDP
	4558		
	4559	;----- GRAPHICS WRITE	
	4560	; THIS ROUTINE WRITES THE ASCII CHARACTER TO THE	:
	4561	; CURRENT POSITION ON THE SCREEN.	:
	4562	; ENTRY	:
	4563	; AL = CHARACTER TO WRITE	:
	4564	; BL = COLOR ATTRIBUTE TO BE USED FOR FOREGROUND COLOR	:
	4565	; IF BIT 7 IS SET, THE CHAR IS XOR'D INTO THE REGEN	:

LOC OBJ	LINE	SOURCE
	4566	; BUFFER (0 IS USED FOR THE BACKGROUND COLOR)
	4567	; CX = NUMBER OF CHARS TO WRITE
	4568	; DS = DATA SEGMENT
	4569	; ES = REGEN SEGMENT
	4570	; EXIT
	4571	; NOTHING IS RETURNED
	4572	;
	4573	; GRAPHICS READ
	4574	; THIS ROUTINE READS THE ASCII CHARACTER AT THE CURRENT
	4575	; CURSOR POSITION ON THE SCREEN BY MATCHING THE DOTS ON
	4576	; THE SCREEN TO THE CHARACTER GENERATOR CODE POINTS
	4577	; ENTRY
	4578	; NONE ( 0 IS ASSUMED AS THE BACKGROUND COLOR
	4579	; EXIT
	4580	; AL = CHARACTER READ AT THAT POSITION (0 RETURNED IF
	4581	; NONE FOUND)
	4582	;
	4583	; FOR BOTH ROUTINES, THE IMAGES USED TO FORM CHARS ARE
	4584	; CONTAINED IN ROM FOR THE 1ST 128 CHARS. TO ACCESS CHARS
	4585	; IN THE SECOND HALF, THE USER MUST INITIALIZE THE VECTOR AT
	4586	; INTERRUPT 1FH (LOCATION 0007CH) TO POINT TO THE USER
	4587	; SUPPLIED TABLE OF GRAPHIC IMAGES (8X8 BOXES).
	4588	; FAILURE TO DO SO WILL CAUSE IN STRANGE RESULTS
	4589	-----
	4590	ASSUME CS:CODE,DS:DATA,ES:DATA
F578	4591	GRAPHICS_WRITE PROC NEAR
F578 B400	4592	MOV AH,0 ; ZERO TO HIGH OF CODE POINT
F57A 50	4593	PUSH AX ; SAVE CODE POINT VALUE
	4594	;
	4595	;----- DETERMINE POSITION IN REGEN BUFFER TO PUT CODE POINTS
	4596	;
F57B E884D1	4597	CALL S26 ; FIND LOCATION IN REGEN BUFFER
F57E 8BF8	4598	MOV DI,AX ; REGEN POINTER IN DI
	4599	;
	4600	;----- DETERMINE REGION TO GET CODE POINTS FROM
	4601	;
F580 58	4602	POP AX ; RECOVER CODE POINT
F581 3C80	4603	CMP AL,80H ; IS IT IN SECOND HALF
F583 7306	4604	JAE S1 ; YES
	4605	;
	4606	;----- IMAGE IS IN FIRST HALF, CONTAINED IN ROM
	4607	;
F585 BE6EFA	4608	MOV SI,0FA6EH ; CRT_CHAR_GEN (OFFSET OF IMAGES)
F586 0E	4609	PUSH CS ; SAVE SEGMENT ON STACK
F589 E80F	4610	JMP SHORT S2 ; DETERMINE_MODE
	4611	;
	4612	;----- IMAGE IS IN SECOND HALF, IN USER RAM
	4613	;
F58B	4614	S1: ; EXTEND_CHAR
F58B 2C80	4615	SUB AL,80H ; ZERO ORIGIN FOR SECOND HALF
F58D 1E	4616	PUSH DS ; SAVE DATA POINTER
F58E 2BF6	4617	SUB SI,SI
F590 8EDE	4618	MOV DS,SI ; ESTABLISH VECTOR ADDRESSING
	4619	ASSUME DS:ABS0
F592 C536C00	4620	LDS SI,EXT_PTR ; GET THE OFFSET OF THE TABLE
F596 8CDA	4621	MOV DX,DS ; GET THE SEGMENT OF THE TABLE
	4622	ASSUME DS:DATA
F598 1F	4623	POP DS ; RECOVER DATA SEGMENT
F599 52	4624	PUSH DX ; SAVE TABLE SEGMENT ON STACK
	4625	;
	4626	;----- DETERMINE GRAPHICS MODE IN OPERATION
	4627	;
F59A	4628	S2: ; DETERMINE_MODE
F59A D1E0	4629	SAL AX,1 ; MULTIPLY CODE POINT
F59C D1E0	4630	SAL AX,1 ; VALUE BY 8
F59E D1E0	4631	SAL AX,1
F5A0 03F0	4632	ADD SI,AX ; SI HAS OFFSET OF DESIRED CODES
F5A2 803E490006	4633	CHP CRT_MODE,6
F5A7 1F	4634	POP DS ; RECOVER TABLE POINTER SEGMENT
F5A8 722C	4635	JC S7 ; TEST FOR MEDIUM RESOLUTION MODE
	4636	;
	4637	;----- HIGH RESOLUTION MODE
	4638	;
F5AA	4639	S3: ; HIGH_CHAR
F5AA 57	4640	PUSH DI ; SAVE REGEN POINTER
F5AB 56	4641	PUSH SI ; SAVE CODE POINTER
F5AC B604	4642	MOV DH,4 ; NUMBER OF TIMES THROUGH LOOP

LOC OBJ	LINE	SOURCE	
F5AE	4643	S4:	
F5AE AC	4644	LDSB	; GET BYTE FROM CODE POINTS
F5AF F6C380	4645	TEST BL,80H	; SHOULD WE USE THE FUNCTION
F5B2 7516	4646	JNZ S6	; TO PUT CHAR IN
F5B4 AA	4647	STOSB	; STORE IN REGEN BUFFER
F5B5 AC	4648	LDSB	
F5B6	4649	S5:	
F5B6 268885FF1F	4650	MOV ES:[DI+2000H-1],AL	; STORE IN SECOND HALF
F5B8 83C74F	4651	ADD DI,79	; MOVE TO NEXT ROW IN REGEN
F5B8 FECE	4652	DEC DH	; DONE WITH LOOP
F5C0 75EC	4653	JNZ S4	
F5C2 5E	4654	POP SI	
F5C3 5F	4655	POP DI	; RECOVER REGEN POINTER
F5C4 47	4656	INC DI	; POINT TO NEXT CHAR POSITION
F5C5 E2E3	4657	LOOP S3	; MORE CHARS TO WRITE
F5C7 E9F8FB	4658	JMP VIDEO_RETURN	
F5CA	4659	S6:	
F5CA 263205	4660	XOR AL,ES:[DI]	; EXCLUSIVE OR WITH CURRENT
F5CD AA	4661	STOSB	; STORE THE CODE POINT
F5CE AC	4662	LDSB	; AGAIN FOR ODD FIELD
F5CF 263285FF1F	4663	XOR AL,ES:[DI+2000H-1]	
F5D4 EBE0	4664	JMP S5	; BACK TO MAINSTREAM
	4665		
	4666	;----- MEDIUM RESOLUTION WRITE	
	4667		
F5D6	4668	S7:	; MED_RES_WRITE
F5D6 8AD3	4669	MOV DL,BL	; SAVE HIGH COLOR BIT
F5D8 D1E7	4670	SAL DI,1	; OFFSET*2 SINCE 2 BYTES/CHAR
F5DA E8D100	4671	CALL S19	; EXPAND BL TO FULL WORD OF COLOR
F5DD	4672	S8:	; MED_CHAR
F5DD 57	4673	PUSH DI	; SAVE REGEN POINTER
F5DE 56	4674	PUSH SI	; SAVE THE CODE POINTER
F5DF B604	4675	MOV DH,4	; NUMBER OF LOOPS
F5E1	4676	S9:	
F5E1 AC	4677	LDSB	; GET CODE POINT
F5E2 E8DE00	4678	CALL S21	; DOUBLE UP ALL THE BITS
F5E5 23C3	4679	AND AX,BX	; CONVERT THEM TO FOREGROUND
	4680		; COLOR ( 0 BACK )
F5E7 F6C280	4681	TEST DL,80H	; IS THIS XOR FUNCTION
F5EA 7407	4682	JZ S10	; NO, STORE IT IN AS IT IS
F5EC 263225	4683	XOR AH,ES:[DI]	; DO FUNCTION WITH HALF
F5EF 26324501	4684	XOR AL,ES:[DI+1]	; AND WITH OTHER HALF
F5F3	4685	S10:	
F5F3 268825	4686	MOV ES:[DI],AH	; STORE FIRST BYTE
F5F6 26884501	4687	MOV ES:[DI+1],AL	; STORE SECOND BYTE
F5FA AC	4688	LDSB	; GET CODE POINT
F5FB E8C500	4689	CALL S21	
F5FE 23C3	4690	AND AX,BX	; CONVERT TO COLOR
F600 F6C280	4691	TEST DL,80H	; AGAIN, IS THIS XOR FUNCTION
F603 740A	4692	JZ S11	; NO, JUST STORE THE VALUES
F605 2632A50020	4693	XOR AH,ES:[DI+2000H]	; FUNCTION WITH FIRST HALF
F60A 2632850120	4694	XOR AL,ES:[DI+2001H]	; AND WITH SECOND HALF
F60F	4695	S11:	
F60F 2688A50020	4696	MOV ES:[DI+2000H],AH	
F614 2688850120	4697	MOV ES:[DI+2000H+1],AL	; STORE IN SECOND PORTION OF BUFFER
F619 83C750	4698	ADD DI,80	; POINT TO NEXT LOCATION
F61C FECE	4699	DEC DH	
F61E 75C1	4700	JNZ S9	; KEEP GOING
F620 5E	4701	POP SI	; RECOVER CODE POINTER
F621 5F	4702	POP DI	; RECOVER REGEN POINTER
F622 47	4703	INC DI	; POINT TO NEXT CHAR POSITION
F623 47	4704	INC DI	
F624 E2B7	4705	LOOP S8	; MORE TO WRITE
F626 E99CFB	4706	JMP VIDEO_RETURN	
	4707	GRAPHICS_WRITE ENDP	
	4708	;-----	
	4709	; GRAPHICS_READ :	
	4710	;-----	
F629	4711	GRAPHICS_READ PROC NEAR	
F629 E8D600	4712	CALL S26	; CONVERTED TO OFFSET IN REGEN
F62C 8BF0	4713	MOV SI,AX	; SAVE IN SI
F62E 83EC08	4714	SUB SP,8	; ALLOCATE SPACE TO SAVE THE
	4715		; READ CODE POINT
F631 8BEC	4716	MOV BP,SP	; POINTER TO SAVE AREA
	4717		
	4718	;----- DETERMINE GRAPHICS MODES	
	4719		

LOC OBJ	LINE	SOURCE	
F633 803E490006	4720	CHP CRT_MODE,6	
F638 06	4721	PUSH ES	
F639 1F	4722	POP DS	; POINT TO REGEN SEGMENT
F63A 721A	4723	JC S13	; MEDIUM RESOLUTION
	4724		
	4725	;----- HIGH RESOLUTION READ	
	4726		
	4727	;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT	
	4728		
F63C B604	4729	MOV DH,4	; NUMBER OF PASSES
F63E	4730	S12:	
F63E 8A04	4731	MOV AL,[SI]	; GET FIRST BYTE
F640 884600	4732	MOV [BP],AL	; SAVE IN STORAGE AREA
F643 45	4733	INC BP	; NEXT LOCATION
F644 8A840020	4734	MOV AL,[SI+2000H]	; GET LOWER REGION BYTE
F648 884600	4735	MOV [BP],AL	; ADJUST AND STORE
F648 45	4736	INC BP	
F64C 83C650	4737	ADD SI,80	; POINTER INTO REGEN
F64F FECE	4738	DEC DH	; LOOP CONTROL
F651 75EB	4739	JNZ S12	; DO IT SOME MORE
F653 EB1790	4740	JMP S15	; GO MATCH THE SAVED CODE POINTS
	4741		
	4742	;----- MEDIUM RESOLUTION READ	
	4743		
F656	4744	S13:	; MED_RES_READ
F656 01E6	4745	SAL SI,1	; OFFSET*2 SINCE 2 BYTES/CHAR
F658 B604	4746	MOV DH,4	; NUMBER OF PASSES
F65A	4747	S14:	
F65A E88000	4748	CALL S23	; GET PAIR BYTES FROM REGEN
	4749		; INTO SINGLE SAVE
F65D 81C60020	4750	ADD SI,2000H	; GO TO LOWER REGION
F661 E88100	4751	CALL S23	; GET THIS PAIR INTO SAVE
F664 81EE001F	4752	SUB SI,2000H-80	; ADJUST POINTER BACK INTO UPPER
F668 FECE	4753	DEC DH	
F66A 75EE	4754	JNZ S14	; KEEP GOING UNTIL ALL 8 DONE
	4755		
	4756	;----- SAVE AREA HAS CHARACTER IN IT, MATCH IT	
	4757		
F66C	4758	S15:	; FIND_CHAR
F66C BF6EFA90	4759	MOV DI,OFFSET CRT_CHAR_GEN	; ESTABLISH ADDRESSING
F670 0E	4760	PUSH CS	
F671 07	4761	POP ES	; CODE POINTS IN CS
F672 83ED08	4762	SUB BP,8	; ADJUST POINTER TO BEGINNING
	4763		; OF SAVE AREA
F675 8BF5	4764	MOV SI,BP	
F677 FC	4765	CLD	; ENSURE DIRECTION
F678 8000	4766	MOV AL,0	; CURRENT CODE POINT BEING MATCHED
F67A	4767	S16:	
F67A 16	4768	PUSH SS	; ESTABLISH ADDRESSING TO STACK
F67B 1F	4769	POP DS	; FOR THE STRING COMPARE
F67C BA8000	4770	MOV DX,128	; NUMBER TO TEST AGAINST
F67F	4771	S17:	
F67F 56	4772	PUSH SI	; SAVE SAVE AREA POINTER
F680 57	4773	PUSH DI	; SAVE CODE POINTER
F681 B90800	4774	MOV CX,8	; NUMBER OF BYTES TO MATCH
F684 F3	4775	REPE CMPSB	; COMPARE THE 8 BYTES
F685 A6			
F686 5F	4776	POP DI	; RECOVER THE POINTERS
F687 5E	4777	POP SI	
F688 741E	4778	JZ S18	; IF ZERO FLAG SET, THEN MATCH OCCURRED
F68A FECC	4779	INC AL	; NO MATCH, MOVE ON TO NEXT
F68C 83C708	4780	ADD DI,8	; NEXT CODE POINT
F68F 4A	4781	DEC DX	; LOOP CONTROL
F690 75ED	4782	JNZ S17	; DO ALL OF THEM
	4783		
	4784	;----- CHAR NOT MATCHED, MIGHT BE IN USER SUPPLIED SECOND HALF	
	4785		
F692 3C00	4786	CHP AL,0	; AL <> 0 IF ONLY 1ST HALF SCANNED
F694 7412	4787	JE S18	; IF = 0, THEN ALL HAS BEEN SCANNED
F696 2BC0	4788	SUB AX,AX	
F698 8ED8	4789	MOV DS,AX	; ESTABLISH ADDRESSING TO VECTOR
	4790	ASSUME DS:ABS0	
F69A C43E7C00	4791	LES DI,EXT_PTR	; GET POINTER
F69E 8CC0	4792	MOV AX,ES	; SEE IF THE POINTER REALLY EXISTS
F6A0 0BC7	4793	OR AX,DI	; IF ALL 0, THEN DOESN'T EXIST
F6A2 7404	4794	JZ S18	; NO SENSE LOOKING
F6A4 B080	4795	MOV AL,128	; ORIGIN FOR SECOND HALF

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LOC OBJ          LINE  SOURCE
F6A6 EBD2        4796      JMP     S16                ; GO BACK AND TRY FOR IT
                  4797      ASSUME DS:DATA
                  4798
                  4799      ;----- CHARACTER IS FOUND ( AL=0 IF NOT FOUND )
                  4800
F6A8              4801      S18:
F6A8 83C4DB      4802      ADD     SP,8                ; READJUST THE STACK, THROW AWAY SAVE
F6A8 E917FB      4803      JMP     VIDEO_RETURN        ; ALL DONE
                  4804      GRAPHICS_READ  ENDP
                  4805      ;-----
                  4806      ; EXPAND_MED_COLOR
                  4807      ; THIS ROUTINE EXPANDS THE LOW 2 BITS IN BL TO
                  4808      ; FILL THE ENTIRE BX REGISTER
                  4809      ; ENTRY
                  4810      ; BL = COLOR TO BE USED ( LOW 2 BITS )
                  4811      ; EXIT
                  4812      ; BX = COLOR TO BE USED ( 8 REPLICATIONS OF THE
                  4813      ; 2 COLOR BITS )
                  4814      ;-----
F6AE              4815      S19  PROC   NEAR
F6AE 80E303      4816      AND     BL,3                ; ISOLATE THE COLOR BITS
F6B1 8AC3        4817      MOV     AL,BL               ; COPY TO AL
F6B3 51          4818      PUSH   CX                  ; SAVE REGISTER
F6B4 B90300      4819      MOV     CX,3                ; NUMBER OF TIMES TO DO THIS
F6B7              4820      S20:
F6B7 D0E0        4821      SAL     AL,1                ;
F6B9 D0E0        4822      SAL     AL,1                ; LEFT SHIFT BY 2
F6BB 0ADB        4823      OR      BL,AL               ; ANOTHER COLOR VERSION INTO BL
F6BD E2F8        4824      LOOP   S20                 ; FILL ALL OF BL
F6BF 8AFB        4825      MOV     BH,BL               ; FILL UPPER PORTION
F6C1 59          4826      POP     CX                  ; REGISTER BACK
F6C2 C3          4827      RET                          ; ALL DONE
                  4828      S19  ENDP
                  4829      ;-----
                  4830      ; E_PAND_BYTE
                  4831      ; THIS ROUTINE TAKES THE BYTE IN AL AND DOUBLES
                  4832      ; ALL OF THE BITS, TURNING THE 8 BITS INTO
                  4833      ; 16 BITS. THE RESULT IS LEFT IN AX
                  4834      ;-----
F6C3              4835      S21  PROC   NEAR
F6C3 52          4836      PUSH   DX                  ; SAVE REGISTERS
F6C4 51          4837      PUSH   CX
F6C5 53          4838      PUSH   BX
F6C6 2B02        4839      SUB     DX,DX               ; RESULT REGISTER
F6C8 B90100      4840      MOV     CX,1                ; MASK REGISTER
F6CB              4841      S22:
F6CB 8BD8        4842      MOV     BX,AX               ; BASE INTO TEMP
F6CD 23D9        4843      AND     BX,CX               ; USE MASK TO EXTRACT A BIT
F6CF 0BD3        4844      OR      DX,BX               ; PUT INTO RESULT REGISTER
F6D1 D1E0        4845      SHL     AX,1                ;
F6D3 D1E1        4846      SHL     CX,1                ; SHIFT BASE AND MASK BY 1
F6D5 8BD8        4847      MOV     BX,AX               ; BASE TO TEMP
F6D7 23D9        4848      AND     BX,CX               ; EXTRACT THE SAME BIT
F6D9 0BD3        4849      OR      DX,BX               ; PUT INTO RESULT
F6DB D1E1        4850      SHL     CX,1                ; SHIFT ONLY MASK NOW,
                  4851      ; MOVING TO NEXT BASE
F6DD 73EC        4852      JNC     S22                 ; USE MASK BIT COMING OUT TO TERMINATE
F6DF 8BC2        4853      MOV     AX,DX               ; RESULT TO PARH REGISTER
F6E1 5B          4854      POP     BX
F6E2 59          4855      POP     CX                  ; RECOVER REGISTERS
F6E3 5A          4856      POP     DX
F6E4 C3          4857      RET                          ; ALL DONE
                  4858      S21  ENDP
                  4859      ;-----
                  4860      ; MED_READ_BYTE
                  4861      ; THIS ROUTINE WILL TAKE 2 BYTES FROM THE REGEN
                  4862      ; BUFFER, COMPARE AGAINST THE CURRENT FOREGROUND
                  4863      ; COLOR, AND PLACE THE CORRESPONDING ON/OFF BIT
                  4864      ; PATTERN INTO THE CURRENT POSITION IN THE SAVE
                  4865      ; AREA
                  4866      ; ENTRY
                  4867      ; SI,DS = POINTER TO REGEN AREA OF INTEREST
                  4868      ; BX = EXPANDED FOREGROUND COLOR
                  4869      ; BP = POINTER TO SAVE AREA
                  4870      ; EXIT
                  4871      ; BP IS INCREMENT AFTER SAVE
                  4872      ;-----

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LOC OBJ	LINE	SOURCE		
F6E5	4873	S23	PROC	NEAR
F6E5 8A24	4874		MOV	AH,[SI] ; GET FIRST BYTE
F6E7 8A4401	4875		MOV	AL,[SI+1] ; GET SECOND BYTE
F6EA B900C0	4876		MOV	CX,0C000H ; 2 BIT MASK TO TEST THE ENTRIES
F6EB B200	4877		MOV	DL,0 ; RESULT REGISTER
F6EF	4878			
F6EF 85C1	4879	S24:	TEST	AX,CX ; IS THIS SECTION BACKGROUND?
F6F1 F8	4880		CLC	; CLEAR CARRY IN HOPES THAT IT IS
F6F2 7401	4881		JZ	S25 ; IF ZERO, IT IS BACKGROUND
F6F4 F9	4882		STC	; WASH'T, SO SET CARRY
F6F5 00D2	4883	S25:	RCL	DL,1 ; MOVE THAT BIT INTO THE RESULT
F6F7 D1E9	4884		SHR	CX,1
F6F9 D1E9	4885		SHR	CX,1 ; MOVE THE MASK TO THE RIGHT BY 2 BITS
F6FB 73F2	4886		JNC	S24 ; DO IT AGAIN IF MASK DIDN'T FALL OUT
F6FD 8B5600	4887		MOV	[BP],DL ; STORE RESULT IN SAVE AREA
F700 45	4888		INC	BP ; ADJUST POINTER
F701 C3	4889		RET	; ALL DONE
	4890	S23	ENDP	
	4891			
	4892			; V4_POSITION :
	4893			; THIS ROUTINE TAKES THE CURSOR POSITION :
	4894			; CONTAINED IN THE MEMORY LOCATION, AND :
	4895			; CONVERTS IT INTO AN OFFSET INTO THE :
	4896			; REGEN BUFFER, ASSUMING ONE BYTE/CHAR. :
	4897			; FOR MEDIUM RESOLUTION GRAPHICS, :
	4898			; THE NUMBER MUST BE DOUBLED. :
	4899			; ENTRY :
	4900			; NO REGISTERS, MEMORY LOCATION :
	4901			; CURSOR_POSN IS USED :
	4902			; EXIT :
	4903			; AX CONTAINS OFFSET INTO REGEN BUFFER :
	4904			;
F702	4905	S26	PROC	NEAR
F702 A15000	4906		MOV	AX,CURSOR_POSN ; GET CURRENT CURSOR
F705	4907	GRAPH_POSN	LABEL	NEAR
F705 53	4908		PUSH	BX ; SAVE REGISTER
F706 8BD8	4909		MOV	BX,AX ; SAVE A COPY OF CURRENT CURSOR
F708 8AC4	4910		MOV	AL,AH ; GET ROWS TO AL
F70A F6264A00	4911		MUL	BYTE PTR CRT_COLS ; MULTIPLY BY BYTES/COLUMN
F70E D1E0	4912		SHL	AX,1 ; MULTIPLY * 4 SINCE 4 ROWS/BYTE
F710 D1E0	4913		SHL	AX,1
F712 2AFF	4914		SUB	BH,BH ; ISOLATE COLUMN VALUE
F714 03C3	4915		ADD	AX,BX ; DETERMINE OFFSET
F716 5B	4916		POP	BX ; RECOVER POINTER
F717 C3	4917		RET	; ALL DONE
	4918	S26	ENDP	
	4919			
	4920			; WRITE_TTY :
	4921			; THIS INTERFACE PROVIDES A TELETYPE LIKE INTERFACE TO THE VIDEO :
	4922			; CARD. THE INPUT CHARACTER IS WRITTEN TO THE CURRENT CURSOR :
	4923			; POSITION, AND THE CURSOR IS MOVED TO THE NEXT POSITION. IF THE :
	4924			; CURSOR LEAVES THE LAST COLUMN OF THE FIELD, THE COLUMN IS SET :
	4925			; TO ZERO, AND THE ROW VALUE IS INCREMENTED. IF THE ROW VALUE :
	4926			; LEAVES THE FIELD, THE CURSOR IS PLACED ON THE LAST ROW, FIRST :
	4927			; COLUMN, AND THE ENTIRE SCREEN IS SCROLLED UP ONE LINE. WHEN :
	4928			; THE SCREEN IS SCROLLED UP, THE ATTRIBUTE FOR FILLING THE NEWLY :
	4929			; BLANKED LINE IS READ FROM THE CURSOR POSITION ON THE PREVIOUS :
	4930			; LINE BEFORE THE SCROLL, IN CHARACTER MODE. IN GRAPHICS MODE, :
	4931			; THE 0 COLOR IS USED. :
	4932			; ENTRY :
	4933			; (AH) = CURRENT CRT MODE :
	4934			; (AL) = CHARACTER TO BE WRITTEN :
	4935			; NOTE THAT BACK SPACE, CAR RET, BELL AND LINE FEED ARE HANDLED :
	4936			; AS COMMANDS RATHER THAN AS DISPLAYABLE GRAPHICS :
	4937			; (BL) = FOREGROUND COLOR FOR CHAR WRITE IF CURRENTLY IN A :
	4938			; GRAPHICS MODE :
	4939			; EXIT :
	4940			; ALL REGISTERS SAVED :
	4941			;
	4942			ASSUME CS:CODE,DS:DATA
F718	4943	WRITE_TTY	PROC	NEAR
F718 50	4944		PUSH	AX ; SAVE REGISTERS
F719 50	4945		PUSH	AX ; SAVE CHAR TO WRITE
F71A B403	4946		MOV	AH,3
F71C 8A3E6200	4947		MOV	BH,ACTIVE_PAGE ; GET THE CURRENT ACTIVE PAGE
F720 CD10	4948		INT	10H ; READ THE CURRENT CURSOR POSITION
F722 5B	4949		POP	AX ; RECOVER CHAR

LOC OBJ	LINE	SOURCE
	4950	
	4951	;----- DX NOW HAS THE CURRENT CURSOR POSITION
	4952	
F723 3C08	4953	CHP AL,8 ; IS IT A BACKSPACE
F725 7452	4954	JE U8 ; BACK_SPACE
F727 3C0D	4955	CHP AL,0DH ; IS IT CARRIAGE RETURN
F729 7457	4956	JE U9 ; CAR_RET
F72B 3C0A	4957	CHP AL,0AH ; IS IT A LINE FEED
F72D 7457	4958	JE U10 ; LINE_FEED
F72F 3C07	4959	CHP AL,07H ; IS IT A BELL
F731 745A	4960	JE U11 ; BELL
	4961	
	4962	;----- WRITE THE CHAR TO THE SCREEN
	4963	
	4964	
F733 B40A	4965	MOV AH,10 ; WRITE CHAR ONLY
F735 B9010D	4966	MOV CX,1 ; ONLY ONE CHAR
F738 CD10	4967	INT 10H ; WRITE THE CHAR
	4968	
	4969	;----- POSITION THE CURSOR FOR NEXT CHAR
	4970	
F73A FEC2	4971	INC DL
F73C 3A164A00	4972	CHP DL,BYTE PTR CRT_COLS ; TEST FOR COLUMN OVERFLOW
F740 7533	4973	JNZ U7 ; SET_CURSOR
F742 B200	4974	MOV DL,0 ; COLUMN FOR CURSOR
F744 80FE18	4975	CHP DH,24
F747 752A	4976	JNZ U6 ; SET_CURSOR_INC
	4977	
	4978	;----- SCROLL REQUIRED
	4979	
F749	4980	U1:
F749 B402	4981	MOV AH,2
F74B CD10	4982	INT 10H ; SET THE CURSOR
	4983	
	4984	;----- DETERMINE VALUE TO FILL WITH DURING SCROLL
	4985	
F74D A04900	4986	MOV AL,CRT_MODE ; GET THE CURRENT MODE
F750 3C04	4987	CHP AL,4
F752 7206	4988	JC U2 ; READ-CURSOR
F754 3C07	4989	CHP AL,7
F756 B700	4990	MOV BH,0 ; FILL WITH BACKGROUND
F758 7506	4991	JNE U3 ; SCROLL-UP
F75A	4992	U2: ; READ-CURSOR
F75A B408	4993	MOV AH,8
F75C CD10	4994	INT 10H ; READ CHAR/ATTR AT CURRENT CURSOR
F75E 8AFC	4995	MOV BH,AH ; STORE IN BH
F760	4996	U3: ; SCROLL-UP
F760 B80106	4997	MOV AX,601H ; SCROLL ONE LINE
F763 2BC9	4998	SUB CX,CX ; UPPER LEFT CORNER
F765 B618	4999	MOV DH,24 ; LOWER RIGHT ROW
F767 8A164A00	5000	MOV DL,BYTE PTR CRT_COLS ; LOWER RIGHT COLUMN
F76B FECA	5001	DEC DL
F76D	5002	U4: ; VIDEO-CALL-RETURN
F76D CD10	5003	INT 10H ; SCROLL UP THE SCREEN
F76F	5004	U5: ; TTY-RETURN
F76F 5B	5005	POP AX ; RESTORE THE CHARACTER
F770 E952FA	5006	JMP VIDEO_RETURN ; RETURN TO CALLER
F773	5007	U6: ; SET-CURSOR-INC
F773 FEC6	5008	INC DH ; NEXT ROW
F775	5009	U7: ; SET-CURSOR
F775 B402	5010	MOV AH,2
F777 EBF4	5011	JMP U4 ; ESTABLISH THE NEW CURSOR
	5012	
	5013	;----- BACK SPACE FOUND
	5014	
F779	5015	U8: ;
F779 80FA00	5016	CHP DL,0 ; ALREADY AT END OF LINE
F77C 74F7	5017	JE U7 ; SET_CURSOR
F77E FECA	5018	DEC DL ; NO -- JUST MOVE IT BACK
F780 EBF3	5019	JMP U7 ; SET_CURSOR
	5020	
	5021	;----- CARRIAGE RETURN FOUND
	5022	
F782	5023	U9: ;
F782 B200	5024	MOV DL,0 ; MOVE TO FIRST COLUMN
F784 EBEF	5025	JMP U7 ; SET_CURSOR
	5026	

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LOC OBJ          LINE SOURCE
5027             ;----- LINE FEED FOUND
5028
F786             5029             U10:
F786 80FE18      5030             CMP     DH,24             ; BOTTOM OF SCREEN
F789 75E8        5031             JNE     U6             ; YES, SCROLL THE SCREEN
F78B EBBC        5032             JMP     U1             ; NO, JUST SET THE CURSOR
5033
5034             ;----- BELL FOUND
5035
F78D             5036             U11:
F78D B302        5037             MOV     BL,2             ; SET UP COUNT FOR BEEP
F78E E871EE      5038             CALL    BEEP            ; SOUND THE POD BELL
F792 EBDB        5039             JMP     U5             ; TTY_RETURN
5040             WRITE_TTY     ENDP
5041             ;-----
5042             ; LIGHT PEN                               :
5043             ; THIS ROUTINE TESTS THE LIGHT PEN SWITCH AND THE LIGHT :
5044             ; PEN TRIGGER. IF BOTH ARE SET, THE LOCATION OF THE LIGHT :
5045             ; PEN IS DETERMINED. OTHERWISE, A RETURN WITH NO :
5046             ; INFORMATION IS MADE. :
5047             ; ON EXIT :
5048             ; (AH) = 0 IF NO LIGHT PEN INFORMATION IS AVAILABLE :
5049             ; BX,CX,DX ARE DESTROYED :
5050             ; (AH) = 1 IF LIGHT PEN IS AVAILABLE :
5051             ; (DH,DL) = ROW,COLUMN OF CURRENT LIGHT PEN :
5052             ; POSITION :
5053             ; (CH) = RASTER POSITION :
5054             ; (BX) = BEST GUESS AT PIXEL HORIZONTAL POSITION :
5055             ;-----
5056             ASSUME CS:CODE,DS:DATA
5057             ;----- SUBTRACT_TABLE
F794             5058             V1 LABEL BYTE
F794 03          5059             DB     3,3,5,5,3,3,3,4 ;
F795 03
F796 05
F797 05
F798 03
F799 03
F79A 03
F79B 04
F79C             5060             READ_LPEN     PROC NEAR
5061
5062             ;----- WAIT FOR LIGHT PEN TO BE DEPRESSED
5063
F79C B400        5064             MOV     AH,0             ; SET NO LIGHT PEN RETURN CODE
F79E 8B166300    5065             MOV     DX,ADDR_6845    ; GET BASE ADDRESS OF 6845
F7A2 83C206      5066             ADD     DX,6             ; POINT TO STATUS REGISTER
F7A5 EC          5067             IN     AL,DX            ; GET STATUS REGISTER
F7A6 A804        5068             TEST   AL,4             ; TEST LIGHT PEN SWITCH
F7A8 757E        5069             JNZ    V6             ; NOT SET, RETURN
5070
5071             ;----- NOW TEST FOR LIGHT PEN TRIGGER
5072
F7AA A802        5073             TEST   AL,2             ; TEST LIGHT PEN TRIGGER
F7AC 7503        5074             JNZ    V7A            ; RETURN WITHOUT RESETTING TRIGGER
F7AE E98100      5075             JMP     V7
5076
5077             ;----- TRIGGER HAS BEEN SET, READ THE VALUE IN
5078
F7B1             5079             V7A:
F7B1 B410        5080             MOV     AH,16            ; LIGHT PEN REGISTERS ON 6845
5081
5082             ;----- INPUT REGS POINTED TO BY AH, AND CONVERT TO ROW COLUMN IN DX
5083
F7B3 8B166300    5084             MOV     DX,ADDR_6845    ; ADDRESS REGISTER FOR 6845
F7B7 8AC4        5085             MOV     AL,AH            ; REGISTER TO READ
F7B9 EE          5086             OUT    DX,AL            ; SET IT UP
F7BA 42          5087             INC     DX             ; DATA REGISTER
F7BB EC          5088             IN     AL,DX            ; GET THE VALUE
F7BC 8AE8        5089             MOV     CH,AL            ; SAVE IN CX
F7BE 4A          5090             DEC     DX             ; ADDRESS REGISTER
F7BF FEC4        5091             INC     AH
F7C1 8AC4        5092             MOV     AL,AH            ; SECOND DATA REGISTER
F7C3 EE          5093             OUT    DX,AL            ; GET SECOND DATA VALUE
F7C4 42          5094             INC     DX             ; POINT TO DATA REGISTER
F7C5 EC          5095             IN     AL,DX            ; GET SECOND DATA VALUE
F7C6 8AE5        5096             MOV     AH,CH            ; AX HAS INPUT VALUE

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LOC OBJ          LINE  SOURCE
5097
5098      ;----- AX HAS THE VALUE READ IN FROM THE 6845
5099
F7C8 8A1E4900    5100      MOV     BL,CRT_MODE
F7CC 2AFF        5101      SUB     BH,BH          ; MODE VALUE TO BX
F7CE 2E8A9F90F7  5102      MOV     BL,CS:VLI[BX] ; DETERMINE AMOUNT TO SUBTRACT
F7D3 2BC3        5103      SUB     AX,BX          ; TAKE IT AWAY
F7D5 8B1E4E00    5104      MOV     BX,CRT_START
F7D9 D1EB        5105      SHR     BX,1
F7DB 2BC3        5106      SUB     AX,BX
F7DD 7902        5107      JNS    V2             ; IF POSITIVE, DETERMINE MODE
F7DF 2BC0        5108      SUB     AX,AX         ; <0 PLAYS AS 0
5109
5110      ;----- DETERMINE MODE OF OPERATION
5111
F7E1            5112      V2:          ; DETERMINE_MODE
F7E1 B103        5113      MOV     CL,3          ; SET *8 SHIFT COUNT
F7E3 803E490004  5114      CMP     CRT_MODE,4    ; DETERMINE IF GRAPHICS OR ALPHA
F7E8 722A        5115      JB     V4             ; ALPHA_PEN
F7EA 803E490007  5116      CMP     CRT_MODE,7
F7EF 7423        5117      JE     V4             ; ALPHA_PEN
5118
5119      ;----- GRAPHICS MODE
5120
F7F1 B228        5121      MOV     DL,40         ; DIVISOR FOR GRAPHICS
F7F3 F6F2        5122      DIV     DL            ; DETERMINE ROW(AL) AND COLUMN(AH)
5123      ; AL RANGE 0-99, AH RANGE 0-39
5124
5125      ;----- DETERMINE GRAPHIC ROW POSITION
5126
F7F5 8AE8        5127      MOV     CH,AL         ; SAVE ROW VALUE IN CH
F7F7 02ED        5128      ADD     CH,CH         ; *2 FOR EVEN/ODD FIELD
F7F9 8ADC        5129      MOV     BL,AH         ; COLUMN VALUE TO BX
F7FB 2AFF        5130      SUB     BH,BH         ; MULTIPLY BY 8 FOR MEDIUM RES
F7FD 803E490006  5131      CMP     CRT_MODE,6    ; DETERMINE MEDIUM OR HIGH RES
F802 7504        5132      JNE     V3            ; NOT_HIGH_RES
F804 B104        5133      MOV     CL,4          ; SHIFT VALUE FOR HIGH RES
F806 D0E4        5134      SAL     AH,1          ; COLUMN VALUE TIMES 2 FOR HIGH RES
F808            5135      V3:          ; NOT_HIGH_RES
F808 D3E3        5136      SHL     BX,CL         ; MULTIPLY *16 FOR HIGH RES
5137
5138      ;----- DETERMINE ALPHA CHAR POSITION
5139
F80A 8AD4        5140      MOV     DL,AH         ; COLUMN VALUE FOR RETURN
F80C 8AF0        5141      MOV     DH,AL         ; ROW VALUE
F80E D0EE        5142      SHR     DH,1          ; DIVIDE BY 4
F810 D0EE        5143      SHR     DH,1          ; FOR VALUE IN 0-24 RANGE
F812 EB12        5144      JMP     SHORT V5      ; LIGHT_PEN_RETURN_SET
5145
5146      ;----- ALPHA MODE ON LIGHT PEN
5147
F814            5148      V4:          ; ALPHA_PEN
F814 F6364A00    5149      DIV     BYTE PTR CRT_COLS ; DETERMINE ROW,COLUMN VALUE
F818 8AF0        5150      MOV     DH,AL         ; ROWS TO DH
F81A 8AD4        5151      MOV     DL,AH         ; COLS TO DL
F81C D2E0        5152      SAL     AL,CL         ; MULTIPLY ROWS * 8
F81E 8AE8        5153      MOV     CH,AL         ; GET RASTER VALUE TO RETURN REG
F820 8ADC        5154      MOV     BL,AH         ; COLUMN VALUE
F822 32FF        5155      XOR     BH,BH         ; TO BX
F824 D3E3        5156      SAL     BX,CL
F826            5157      V5:          ; LIGHT_PEN_RETURN_SET
F826 B401        5158      MOV     AH,1          ; INDICATE EVERTHING SET
F828            5159      V6:          ; LIGHT_PEN_RETURN
F828 52          5160      PUSH    DX            ; SAVE RETURN VALUE (IN CASE)
F829 8B166300    5161      MOV     DX,ADDR_6845  ; GET BASE ADDRESS
F82D 83C207      5162      ADD     DX,7          ; POINT TO RESET PARM
F830 EE          5163      OUT     DX,AL         ; ADDRESS, NOT DATA, IS IMPORTANT
F831 5A          5164      POP     DX            ; RECOVER VALUE
F832            5165      V7:          ; RETURN_NO_RESET
F832 5F          5166      POP     DI
F833 5E          5167      POP     SI
F834 1F          5168      POP     DS            ; DISCARD SAVED BX,CX,DX
F835 1F          5169      POP     DS
F836 1F          5170      POP     DS
5171
F837 1F          5172      POP     DS
F838 07          5173      POP     ES

```

LOC OBJ

LINE SOURCE

F839 CF

```

5174             IRET
5175 READ_LPEN     ENDP
5176
5177 ;--- INT 12 ---
5178 ; MEMORY_SIZE_DET
5179 ; THIS ROUTINE DETERMINES THE AMOUNT OF MEMORY IN THE SYSTEM
5180 ; AS REPRESENTED BY THE SWITCHES ON THE PLANAR. NOTE THAT THE
5181 ; SYSTEM MAY NOT BE ABLE TO USE I/O MEMORY UNLESS THERE IS A FULL
5182 ; COMPLEMENT OF 64K BYTES ON THE PLANAR.
5183 ; INPUT
5184 ; NO REGISTERS
5185 ; THE MEMORY_SIZE VARIABLE IS SET DURING POWER ON DIAGNOSTICS
5186 ; ACCORDING TO THE FOLLOWING HARDWARE ASSUMPTIONS:
5187 ; PORT 60 BITS 3,2 = 00 - 16K BASE RAM
5188 ;                   01 - 32K BASE RAM
5189 ;                   10 - 48K BASE RAM
5190 ;                   11 - 64K BASE RAM
5191 ; PORT 62 BITS 3-0 INDICATE AMOUNT OF I/O RAM IN 32K INCREMENTS
5192 ; E.G., 0000 - NO RAM IN I/O CHANNEL
5193 ;                   0010 - 64K RAM IN I/O CHANNEL, ETC.
5194 ; OUTPUT
5195 ; (AX) = NUMBER OF CONTIGUOUS 1K BLOCKS OF MEMORY
5196 ;-----
5197             ASSUME CS:CODE,DS:DATA
5198             ORG 0F841H
F841
F841             MEMORY_SIZE_DET PROC FAR
F841 FB          STI                               ; INTERRUPTS BACK ON
F842 IE          PUSH DS                          ; SAVE SEGMENT
F843 E8F806      CALL DDS
F846 A11300      MOV AX,MEMORY_SIZE              ; GET VALUE
F849 1F          POP DS                           ; RECOVER SEGMENT
F84A CF          IRET                             ; RETURN TO CALLER
5206             MEMORY_SIZE_DET ENDP
5207

```

F841

F841

F841 FB

F842 IE

F843 E8F806

F846 A11300

F849 1F

F84A CF

```

5208 ;--- INT 11 ---
5209 ; EQUIPMENT DETERMINATION
5210 ; THIS ROUTINE ATTEMPTS TO DETERMINE WHAT OPTIONAL
5211 ; DEVICES ARE ATTACHED TO THE SYSTEM.
5212 ; INPUT
5213 ; NO REGISTERS
5214 ; THE EQUIP_FLAG VARIABLE IS SET DURING THE POWER ON
5215 ; DIAGNOSTICS USING THE FOLLOWING HARDWARE ASSUMPTIONS:
5216 ; PORT 60 = LOW ORDER BYTE OF EQUIPMENT
5217 ; PORT 3FA = INTERRUPT ID REGISTER OF 8250
5218 ; BITS 7-3 ARE ALWAYS 0
5219 ; PORT 378 = OUTPUT PORT OF PRINTER -- 8255 PORT THAT
5220 ; CAN BE READ AS WELL AS WRITTEN
5221 ; OUTPUT
5222 ; (AX) IS SET, BIT SIGNIFICANT, TO INDICATE ATTACHED I/O
5223 ; BIT 15,14 = NUMBER OF PRINTERS ATTACHED
5224 ; BIT 13 NOT USED
5225 ; BIT 12 = GAME I/O ATTACHED
5226 ; BIT 11,10,9 = NUMBER OF RS232 CARDS ATTACHED
5227 ; BIT 8 UNUSED
5228 ; BIT 7,6 = NUMBER OF DISKETTE DRIVES
5229 ; 00=1, 01=2, 10=3, 11=4 ONLY IF BIT 0 = 1
5230 ; BIT 5,4 = INITIAL VIDEO MODE
5231 ; 00 - UNUSED
5232 ; 01 - 40X25 BW USING COLOR CARD
5233 ; 10 - 80X25 BW USING COLOR CARD
5234 ; 11 - 80X25 BW USING BW CARD
5235 ; BIT 3,2 = PLANAR RAM SIZE (00=16K,01=32K,10=48K,11=64K)
5236 ; BIT 1 NOT USED
5237 ; BIT 0 = IPL FROM DISKETTE -- THIS BIT INDICATES THAT
5238 ; THERE ARE DISKETTE DRIVES ON THE SYSTEM
5239 ;
5240 ; NO OTHER REGISTERS AFFECTED
5241 ;-----
5242             ASSUME CS:CODE,DS:DATA
5243             ORG 0F84DH
F84D
F84D             EQUIPMENT PROC FAR
5245             STI                               ; INTERRUPTS BACK ON
5246             PUSH DS                          ; SAVE SEGMENT REGISTER
5247             CALL DDS
5248             MOV AX,EQUIP_FLAG                ; GET THE CURRENT SETTINGS
5249             POP DS                           ; RECOVER SEGMENT
5250             IRET                             ; RETURN TO CALLER

```

F84D

F84D

F84D FB

F84E 1E

F84F E8EC06

F852 A11000

F855 1F

F856 CF

Appendix A

LOC OBJ

LINE SOURCE

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5251 EQUIPMENT      ENDP
5252
5253 ;--- INT 15 -----
5254 ; CASSETTE I/O
5255 ; (AH) = 0  TURN CASSETTE MOTOR ON
5256 ; (AH) = 1  TURN CASSETTE MOTOR OFF
5257 ; (AH) = 2  READ 1 OR MORE 256 BYTE BLOCKS FROM CASSETTE
5258 ; (ES,BX) = POINTER TO DATA BUFFER
5259 ; (CX) = COUNT OF BYTES TO READ
5260 ; ON EXIT
5261 ; (ES,BX) = POINTER TO LAST BYTE READ + 1
5262 ; (DX) = COUNT OF BYTES ACTUALLY READ
5263 ; (CY) = 0 IF NO ERROR OCCURRED
5264 ;       = 1 IF ERROR OCCURRED
5265 ; (AH) = ERROR RETURN IF (CY)= 1
5266 ;       = 01 IF CRC ERROR WAS DETECTED
5267 ;       = 02 IF DATA TRANSITIONS ARE LOST
5268 ;       = 04 IF NO DATA WAS FOUND
5269 ; (AH) = 3  WRITE 1 OR MORE 256 BYTE BLOCKS TO CASSETTE
5270 ; (ES,BX) = POINTER TO DATA BUFFER
5271 ; (CX) = COUNT OF BYTES TO WRITE
5272 ; ON EXIT
5273 ; (EX,BX) = POINTER TO LAST BYTE WRITTEN + 1
5274 ; (CX) = 0
5275 ; (AH) = ANY OTHER THAN ABOVE VALUES CAUSES (CY)= 1
5276 ; AND (AH)= 60 TO BE RETURNED (INVALID COMMAND).
5277 ;-----
5278 ASSUME DS:DATA,ES:NOTHING,SS:NOTHING,CS:CODE
5279 ORG 0F859H
5280 CASSETTE_IO PROC FAR
5281 STI ; INTERRUPTS BACK ON
5282 PUSH DS ; ESTABLISH ADDRESSING TO DATA
5283 CALL DDS
5284 AND BIOS_BREAK, 7FH ; MAKE SURE BREAK FLAG IS OFF
5285 CALL W1 ; CASSETTE_IO_CONT
5286 PDP DS
5287 RET 2 ; INTERRUPT RETURN
5288 CASSETTE_IO ENDP
5289 W1 PROC NEAR
5290 ;-----
5291 ; PURPOSE:
5292 ; TO CALL APPROPRIATE ROUTINE DEPENDING ON REG AH
5293 ;
5294 ; AH ROUTINE
5295 ;-----
5296 ; 0 MOTOR ON
5297 ; 1 MOTOR OFF
5298 ; 2 READ CASSETTE BLOCK
5299 ; 3 WRITE CASSETTE BLOCK
5300 ;-----
5301 OR AH,AH ; TURN ON MOTOR?
5302 JZ MOTOR_ON ; YES, DO IT
5303 DEC AH ; TURN OFF MOTOR?
5304 JZ MOTOR_OFF ; YES, DO IT
5305 DEC AH ; READ CASSETTE BLOCK?
5306 JZ READ_BLOCK ; YES, DO IT
5307 DEC AH ; WRITE CASSETTE BLOCK?
5308 JNZ W2 ; NOT_DEFINED
5309 JMP WRITE_BLOCK ; YES, DO IT
5310 W2: ; COMMAND NOT DEFINED
5311 MOV AH,080H ; ERROR, UNDEFINED OPERATION
5312 STC ; ERROR FLAG
5313 RET
5314 W1 ENDP
5315 MOTOR_ON PROC NEAR
5316 ;-----
5317 ; PURPOSE:
5318 ; TO TURN ON CASSETTE MOTOR
5319 ;-----
5320 IN AL,PORT_B ; READ CASSETTE OUTPUT
5321 AND AL,NOT 08H ; CLEAR BIT TO TURN ON MOTOR
5322 W3:
5323 OUT PORT_B,AL ; WRITE IT OUT
5324 SUB AH,AH ; CLEAR AH
5325 RET
5326 MOTOR_ON ENDP
5327 MOTOR_OFF PROC NEAR

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LOC OBJ          LINE    SOURCE
5328      ;-----
5329      ; PURPOSE:          :
5330      ;   TO TURN CASSETTE MOTOR OFF   :
5331      ;-----
F88A E461      5332      IN      AL,PORT_B      ; READ CASSETTE OUTPUT
F88C 0C08      5333      OR      AL,08H        ; SET BIT TO TURN OFF
F88E EBF5      5334      JMP     M3             ; WRITE IT, CLEAR ERROR, RETURN
5335      MOTOR_OFF  ENDP
F890          5336      READ_BLOCK  PROC   NEAR
5337      ;-----
5338      ; PURPOSE:          :
5339      ;   TO READ 1 OR MORE 256 BYTE BLOCKS FROM CASSETTE :
5340      ; :
5341      ; ON ENTRY:        :
5342      ;   ES IS SEGMENT FOR MEMORY BUFFER (FOR COMPACT CODE) :
5343      ;   BX POINTS TO START OF MEMORY BUFFER                :
5344      ;   CX CONTAINS NUMBER OF BYTES TO READ              :
5345      ; ON EXIT:        :
5346      ;   BX POINTS 1 BYTE PAST LAST BYTE PUT IN MEM      :
5347      ;   CX CONTAINS DECREMENTED BYTE COUNT              :
5348      ;   DX CONTAINS NUMBER OF BYTES ACTUALLY READ        :
5349      ; :
5350      ;   CARRY FLAG IS CLEAR IF NO ERROR DETECTED          :
5351      ;   CARRY FLAG IS SET IF CRC ERROR DETECTED          :
5352      ;-----
F890 53      5353      PUSH   BX             ; SAVE BX
F891 51      5354      PUSH   CX             ; SAVE CX
F892 56      5355      PUSH   SI             ; SAVE SI
F893 8E0700  5356      MOV    SI, 7          ; SET UP RETRY COUNT FOR LEADER
F896 E8BF01  5357      CALL  BEGIN_OP       ; BEGIN BY STARTING MOTOR
F899        5358      M4:      ; SEARCH FOR LEADER
F899 E462      5359      IN     AL,PORT_C     ; GET INITIAL VALUE
F89B 2410      5360      AND   AL,010H        ; MASK OFF EXTRANEIOUS BITS
F89D A26B00  5361      MOV   LAST_VAL,AL    ; SAVE IN LOC LAST_VAL
F8A0 BA7A3F  5362      MOV   DX,16250       ; # OF TRANSITIONS TO LOOK FOR
F8A3        5363      M5:      ; WAIT_FOR_EDGE
F8A3 F606710080 5364      TEST  BIOS_BREAK, 80H ; CHECK FOR BREAK KEY
F8A8 7503      5365      JNZ   M6A            ; JUMP IF NO BREAK KEY
5366      ; JUMP IF BREAK KEY HIT
F8AA        5367      M6:      ;
F8AA 4A      5368      DEC   DX              ;
F8AB 7503      5369      JNZ   M7              ; JUMP IF BEGINNING OF LEADER
F8AD        5370      M6A:     ;
F8AD E98400  5371      JMP   M17             ; JUMP IF NO LEADER FOUND
F8B0        5372      M7:      ;
F8B0 E8C600  5373      CALL  READ_HALF_BIT  ; IGNORE FIRST EDGE
F8B3 E3EE      5374      JCXZ  M5              ; JUMP IF NO EDGE DETECTED
F8B5 BA7803  5375      MOV   DX,0376H       ; CHECK FOR HALF BITS
F8B8 B90002  5376      MOV   CX,200H        ; MUST HAVE AT LEAST THIS MANY ONE SIZE
5377      ; PULSES BEFORE CHCKNG FOR SYNC BIT (0)
F8BB E421      5378      IN   AL, 021H        ; INTERRUPT MASK REGISTER
F8BD 0C01      5379      OR   AL,1             ; DISABLE TIHER INTERRUPTS
F8BF E621      5380      OUT  021H, AL        ;
F8C1        5381      M8:      ; SEARCH-LDR
F8C1 F606710080 5382      TEST  BIOS_BREAK, 80H ; CHECK FOR BREAK KEY
F8C6 756C      5383      JNZ   M17             ; JUMP IF BREAK KEY HIT
F8C8 51      5384      PUSH  CX              ; SAVE REG CX
F8C9 E8AD00  5385      CALL  READ_HALF_BIT  ; GET PULSE WIDTH
F8CC 0BC9      5386      OR   CX, CX           ; CHECK FOR TRANSITION
F8CE 59      5387      POP  CX               ; RESTORE ONE BIT COUNTER
F8CF 74C8      5388      JZ   M4               ; JUMP IF NO TRANSITION
F8D1 38D3      5389      CMP  DX,BX            ; CHECK PULSE WIDTH
F8D3 E304      5390      JCXZ  M9              ; IF CX=0 THEN WE CAN LOOK
5391      ; FOR SYNC BIT (0)
F8D5 73C2      5392      JNC  M4               ; JUMP IF ZERO BIT (NOT GOOD LEADER)
F8D7 E2E8      5393      LOOP M8              ; DEC CX AND READ ANOTHER HALF ONE BIT
F8D9        5394      M9:      ; FIND-SYNC
F8D9 72E6      5395      JC   M8               ; JUMP IF ONE BIT (STILL LEADER)
5396      ;
5397      ;----- A SYNCH BIT HAS BEEN FOUND. READ SYN CHARACTER:
5398      ;
F8DB E89B00  5399      CALL  READ_HALF_BIT  ; SKIP OTHER HALF OF SYNC BIT (0)
F8DE E86A00  5400      CALL  READ_BYTE      ; READ SYN BYTE
F8E1 3C16      5401      CMP  AL, 16H         ; SYNCHRONIZATION CHARACTER
F8E3 7549      5402      JNE  M16             ; JUMP IF BAD LEADER FOUND.
5403      ;
5404      ;----- GOOD CRC SO READ DATA BLOCK(S)

```

LOC OBJ	LINE	SOURCE			
	5405				
F8E5 5E	5406	POP SI			RESTORE REGS
F8E6 59	5407	POP CX			
F8E7 5B	5408	POP BX			
	5409				
	5410				
	5411				
	5412				
	5413				
	5414				
	5415				
	5416				
	5417				
	5418				
	5419				
	5420				
F8E8 51	5421	PUSH CX			SAVE BYTE COUNT
F8E9	5422	W10:			COME HERE BEFORE EACH
	5423				256 BYTE BLOCK IS READ
F8E9 C7066900FFFF	5424	MOV CRC_REG,0FFFFH			INIT CRC REG
F8EF BA0001	5425	MOV DX,256			SET DX TO DATA BLOCK SIZE
F8F2	5426	W11:			RD_BLK
F8F2 F606710080	5427	TEST BIOS_BREAK, 80H			CHECK FOR BREAK KEY
F8F7 7523	5428	JNZ W13			JUMP IF BREAK KEY HIT
F8F9 E84F00	5429	CALL READ_BYTE			READ BYTE FROM CASSETTE
F8FC 721E	5430	JC W13			CY SET INDICATES NO DATA TRANSITIONS
F8FE E305	5431	JCXZ W12			IF WE'VE ALREADY REACHED
	5432				END OF MEMORY BUFFER
	5433				SKIP REST OF BLOCK
F900 268807	5434	MOV ES:[BX],AL			STORE DATA BYTE AT BYTE PTR
F903 43	5435	INC BX			INC BUFFER PTR
F904 49	5436	DEC CX			DEC BYTE COUNTER
F905	5437	W12:			LOOP UNTIL DATA BLOCK HAS BEEN
	5438				READ FROM CASSETTE.
F905 4A	5439	DEC DX			DEC BLOCK CNT
F906 7FEA	5440	JG W11			RD_BLK
F908 E84000	5441	CALL READ_BYTE			NOW READ TWO CRC BYTES
F908 E83000	5442	CALL READ_BYTE			
F90E 2AE4	5443	SUB AH,AH			CLEAR AH
F910 813E69000F1D	5444	CHP CRC_REG,1D0FH			IS THE CRC CORRECT
F916 7506	5445	JNE W14			IF NOT EQUAL CRC IS BAD
F918 E306	5446	JCXZ W15			IF BYTE COUNT IS ZERO
	5447				THEN WE HAVE READ ENOUGH
	5448				SO WE WILL EXIT
F91A E8CD	5449	JMP W10			STILL MORE, SO READ ANOTHER BLOCK
F91C	5450	W13:			MISSING-DATA
	5451				NO DATA TRANSITIONS SO
F91C B401	5452	MOV AH,01H			SET AH=02 TO INDICATE
	5453				DATA TIMEOUT
F91E	5454	W14:			BAD-CRC
F91E FEC4	5455	INC AH			EXIT EARLY ON ERROR
	5456				SET AH=01 TO INDICATE CRC ERROR
F920	5457	W15:			RD-BLK-EX
F920 5A	5458	POP DX			CALCULATE COUNT OF
F921 2BD1	5459	SUB DX,CX			DATA BYTES ACTUALLY READ
	5460				RETURN COUNT IN REG DX
F923 50	5461	PUSH AX			SAVE AX (RET CODE)
F924 F6C490	5462	TEST AH, 90H			CHECK FOR ERRORS
F927 7513	5463	JNZ W18			JUMP IF ERROR DETECTED
F929 E81F00	5464	CALL READ_BYTE			READ TRAILER
F92C E80E	5465	JMP SHORT W18			SKIP TO TURN OFF MOTOR
F92E	5466	W16:			BAD-LEADER
F92E 4E	5467	DEC SI			CHECK RETRIES
F92F 7403	5468	JZ W17			JUMP IF TOO MANY RETRIES
F931 E965FF	5469	JMP W4			JUMP IF NOT TOO MANY RETRIES
F934	5470	W17:			NO VALID DATA FOUND
	5471				
	5472				----- NO DATA FROM CASSETTE ERROR, I.E. TIMEOUT
	5473				
F934 5E	5474	POP SI			RESTORE REGS
F935 59	5475	POP CX			RESTORE REGS
F936 5B	5476	POP BX			
F937 2B02	5477	SUB DX,DX			ZERO NUMBER OF BYTES READ
F939 B404	5478	MOV AH,04H			TIME OUT ERROR (NO LEADER)
F93B 50	5479	PUSH AX			
F93C	5480	W18:			MOT-OFF

LOC OBJ	LINE	SOURCE		
F93C E421	5481	IN	AL, 021H	; RE_ENABLE INTERRUPTS
F93E 24FE	5482	AND	AL, 0FFH- 1	
F940 E621	5483	OUT	021H, AL	
F942 EB45FF	5484	CALL	MOTOR_OFF	; TURN OFF MOTOR
F945 58	5485	POP	AX	; RESTORE RETURN CODE
F946 80FC01	5486	CHP	AH,01H	; SET CARRY IF ERROR (AH=0)
F949 F5	5487	CMC		
F94A C3	5488	RET		; FINISHED
	5489	READ_BLOCK	ENDP	
	5490	;-----		
	5491	; PURPOSE:		
	5492	; TO READ A BYTE FROM CASSETTE		
	5493	; ON EXIT		
	5494	; REG AL CONTAINS READ DATA BYTE		
	5495	;-----		
F94B	5496	READ_BYTE	PROC NEAR	
F94B 53	5497	PUSH	BX	; SAVE REGS BX,CX
F94C 51	5498	PUSH	CX	
F94D B108	5499	MOV	CL,8H	; SET BIT COUNTER FOR 8 BITS
F94F	5500	M19:		; BYTE-ASM
F94F 51	5501	PUSH	CX	; SAVE CX
	5502	;-----		
	5503	; READ DATA BIT FROM CASSETTE		
	5504	;-----		
F950 E82600	5505	CALL	READ_HALF_BIT	; READ ONE PULSE
F953 E320	5506	JCXZ	W21	; IF CX=0 THEN TIMEOUT
	5507			; BECAUSE OF NO DATA TRANSITIONS
F955 53	5508	PUSH	BX	; SAVE 1ST HALF BIT'S
	5509			; PULSE WIDTH (IN BX)
F956 E82000	5510	CALL	READ_HALF_BIT	; READ COMPLEMENTARY PULSE
F959 58	5511	POP	AX	; COMPUTE DATA BIT
F95A E319	5512	JCXZ	W21	; IF CX=0 THEN TIMEOUT DUE TO
	5513			; NO DATA TRANSITIONS
F95C 0308	5514	ADD	BX,AX	; PERIOD
F95E 81FBF006	5515	CHP	BX, 06F0H	; CHECK FOR ZERO BIT
F962 F5	5516	CMC		; CARRY IS SET IF ONE BIT
F963 9F	5517	LAHF		; SAVE CARRY IN AH
F964 59	5518	POP	CX	; RESTORE CX
	5519			; NOTE:
	5520			; MS BIT OF BYTE IS READ FIRST.
	5521			; REG CH IS SHIFTED LEFT WITH
	5522			; CARRY BEING INSERTED INTO LS
	5523			; BIT OF CH.
	5524			; AFTER ALL 8 BITS HAVE BEEN
	5525			; READ, THE MS BIT OF THE DATA BYTE
	5526			; WILL BE IN THE MS BIT OF REG CH
F965 00D5	5527	RCL	CH,1	; ROTATE REG CH LEFT WITH CARRY TO
	5528			; LS BIT OF REG CH
F967 9E	5529	SAHF		; RESTORE CARRY FOR CRC ROUTINE
F968 E8D900	5530	CALL	CRC_GEN	; GENERATE CRC FOR BIT
F96B FEC9	5531	DEC	CL	; LOOP TILL ALL 8 BITS OF DATA
	5532			; ASSEMBLED IN REG CH
F96D 75E0	5533	JNZ	W19	; BYTE_ASM
F96F 8AC5	5534	MOV	AL,CH	; RETURN DATA BYTE IN REG AL
F971 F8	5535	CLC		
F972	5536	W20:		; RD-BYT-EX
F972 59	5537	POP	CX	; RESTORE REGS CX,BX
F973 58	5538	POP	BX	
F974 C3	5539	RET		; FINISHED
F975	5540	W21:		; NO-DATA
F975 59	5541	POP	CX	; RESTORE CX
F976 F9	5542	STC		; INDICATE ERROR
F977 EBF9	5543	JMP	W20	; RD_BYT_EX
	5544	READ_BYTE	ENDP	
	5545	;-----		
	5546	; PURPOSE:		
	5547	; TO COMPUTE TIME TILL NEXT DATA		
	5548	; TRANSITION (EDGE)		
	5549	; ON ENTRY:		
	5550	; EDGE_CNT CONTAINS LAST EDGE COUNT		
	5551	; ON EXIT:		
	5552	; AX CONTAINS OLD LAST EDGE COUNT		
	5553	; BX CONTAINS PULSE WIDTH (HALF BIT)		
	5554	;-----		
F979	5555	READ_HALF_BIT	PROC NEAR	
F979 B96400	5556	MOV	CX, 100	; SET TIME TO WAIT FOR BIT
F97C 8A266B00	5557	MOV	AH, LAST_VAL	; GET PRESENT INPUT VALUE

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LOC OBJ          LINE  SOURCE

F980             5558  W22:                ; RD-H-BIT
F980 E462        5559          IN    AL,PORT_C      ; INPUT DATA BIT
F982 2410        5560          AND  AL,010H        ; MASK OFF EXTRANEIOUS BITS
F984 3AC4        5561          CMP  AL,AH          ; SAME AS BEFORE?
F986 E1F8        5562          LOOPE W22          ; LOOP TILL IT CHANGES
F988 A26B00      5563          MOV  LAST_VAL,AL   ; UPDATE LAST_VAL WITH NEW VALUE
F98B B000        5564          MOV  AL,0          ; READ TIMER'S COUNTER COMMAND
F98D E643        5565          OUT  TIM_CTL,AL    ; LATCH COUNTER
F98F 8B1E6700   5566          MOV  BX,EDGE_CNT   ; BX GETS LAST EDGE COUNT
F993 E440        5567          IN   AL,TIMERO     ; GET LS BYTE
F995 8AE0        5568          MOV  AH,AL         ; SAVE IN AH
F997 E440        5569          IN   AL,TIMERO     ; GET HS BYTE
F999 86C4        5570          XCHG AL,AH        ; XCHG AL,AH
F99B 2BD8        5571          SUB  BX,AX         ; SET BX EQUAL TO HALF BIT PERIOD
F99D A36700     5572          MOV  EDGE_CNT,AX  ; UPDATE EDGE COUNT;
F9A0 C3          5573          RET

5574          READ_HALF_BIT  ENDP
5575          ;
5576          ; -----
5576          ; PURPOSE                ;
5577          ;   WRITE 1 OR MORE 256 BYTE BLOCKS TO CASSETTE.  ;
5578          ;   THE DATA IS PADDED TO FILL OUT THE LAST 256 BYTE BLOCK.  ;
5579          ; ON ENTRY:                ;
5580          ;   BX POINTS TO MEMORY BUFFER ADDRESS                ;
5581          ;   CX CONTAINS NUMBER OF BYTES TO WRITE            ;
5582          ; ON EXIT:                ;
5583          ;   BX POINTS 1 BYTE PAST LAST BYTE WRITTEN TO CASSETTE  ;
5584          ;   CX IS ZERO                ;
5585          ; -----

F9A1             5586  WRITE_BLOCK  PROC   NEAR
F9A1 53          5587          PUSH  BX
F9A2 51          5588          PUSH  CX
F9A3 E461        5589          IN   AL,PORT_B      ; DISABLE SPEAKER
F9A5 24FD        5590          AND  AL,NOT 02H
F9A7 0C01        5591          OR   AL, 01H        ; ENABLE TIMER
F9A9 E661        5592          OUT  PORT_B,AL
F9AB B0B6        5593          MOV  AL,066H        ; SET UP TIMER -- MODE 3 SQUARE WAVE
F9AD E443        5594          OUT  TIM_CTL,AL
F9AF E8A600      5595          CALL BEGIN_OP      ; START MOTOR AND DELAY
F9B2 B8A004      5596          MOV  AX,1184        ; SET NORMAL BIT SIZE
F9B5 E8B500      5597          CALL W31           ; SET_TIMER
F9B8 B90008      5598          MOV  CX,0800H      ; SET CX FOR LEADER BYTE COUNT
F9BB             5599          W23:                ; WRITE LEADER
F9BB F9          5600          STC                ; WRITE ONE BITS
F9BC E86800      5601          CALL WRITE_BIT
F9BF E2FA        5602          LOOP W23           ; LOOP 'TIL LEADER IS WRITTEN
F9C1 F8          5603          CLC                ; WRITE SYNC BIT (0)
F9C2 E86200      5604          CALL WRITE_BIT
F9C5 59          5605          POP  CX            ; RESTORE REGS CX,BX
F9C6 5B          5606          POP  BX
F9CF B016        5607          MOV  AL, 16H       ; WRITE SYN CHARACTER
F9C9 E84400      5608          CALL WRITE_BYTE
5609          ; -----
5610          ; PURPOSE                ;
5611          ;   WRITE 1 OR MORE 256 BYTE BLOCKS TO CASSETTE  ;
5612          ; ON ENTRY:                ;
5613          ;   BX POINTS TO MEMORY BUFFER ADDRESS                ;
5614          ;   CONTAINS NUMBER OF BYTES TO WRITE            ;
5615          ; ON EXIT:                ;
5616          ;   BX POINTS 1 BYTE PAST LAST BYTE WRITTEN TO CASSETTE  ;
5617          ;   CX IS ZERO                ;
5618          ; -----

F9CC             5619  WR_BLOCK:
F9CC C7066900FFFF 5620          MOV  CRC_REG,0FFFFH ; INIT CRC
F9D2 BA0001      5621          MOV  DX,256        ; FOR 256 BYTES
F9D5             5622          W24:                ; WR-BLK
F9D5 268A07      5623          MOV  AL,ES:[BX]    ; READ BYTE FROM MEM
F9D8 E83500      5624          CALL WRITE_BYTE    ; WRITE IT TO CASSETTE
F9DB E302        5625          JCXZ W25           ; UNLESS CX=0, ADVANCE PTRS & DEC COUNT
F9DD 43          5626          INC  BX            ; INC BUFFER POINTER
F9DE 49          5627          DEC  CX            ; DEC BYTE COUNTER
F9DF             5628          W25:                ; SKIP-ADV
F9DF 4A          5629          DEC  DX            ; DEC BLOCK CNT
F9E0 7FF3        5630          JG   W24           ; LOOP TILL 256 BYTE BLOCK
5631          ; IS WRITTEN TO TAPE
5632          ; -----
5633          ; WRITE CRC                ;
5634          ;   WRITE 1'S COMPLEMENT OF CRC REG TO CASSETTE  ;

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5635 ; WHICH IS CHECKED FOR CORRECTNESS WHEN THE BLOCK IS READ :
5636 ; REG AX IS MODIFIED :
5637 ;-----
F9E2 A16900 5638 MOV AX,CRC_REG ; WRITE THE ONE'S COMPLEMENT OF THE
5639 ; TWO BYTE CRC TO TAPE
F9E5 F7D0 5640 NOT AX ; FOR 1'S COMPLEMENT
F9E7 50 5641 PUSH AX ; SAVE IT
F9E8 86E0 5642 XCHG AH,AL ; WRITE HS BYTE FIRST
F9EA E82300 5643 CALL WRITE_BYTE ; WRITE IT
F9ED 58 5644 POP AX ; GET IT BACK
F9EE E81F00 5645 CALL WRITE_BYTE ; NOW WRITE LS BYTE
F9F1 08C9 5646 OR CX,CX ; IS BYTE COUNT EXHAUSTED?
F9F3 7507 5647 JNZ WR_BLOCK ; JUMP IF NOT DONE YET
F9F5 51 5648 PUSH CX ; SAVE REG CX
F9F6 B92000 5649 MOV CX, 32 ; WRITE OUT TRAILER BITS
F9F9 5650 W26: ; TRAIL-LOOP
F9F9 F9 5651 STC
F9FA E82A00 5652 CALL WRITE_BIT
F9FD E2FA 5653 LOOP W26 ; WRITE UNTIL TRAILER WRITTEN
F9FF 59 5654 POP CX ; RESTORE REG CX
FA00 B0B0 5655 MOV AL, 0B0H ; TURN TIMER2 OFF
FA02 E643 5656 OUT TIM_CTL, AL
FA04 B80100 5657 MOV AX, 1
FA07 E83300 5658 CALL W31 ; SET_TIMER
FA0A E87DFE 5659 CALL MOTOR_OFF ; TURN MOTOR OFF
FA0D 2BC0 5660 SUB AX,AX ; NO ERRORS REPORTED ON WRITE OP
FA0F C3 5661 RET ; FINISHED
5662 WRITE_BLOCK ENDP
5663 ;-----
5664 ; WRITE A BYTE TO CASSETTE. :
5665 ; BYTE TO WRITE IS IN REG AL. :
5666 ;-----
FA10 5667 WRITE_BYTE PROC NEAR
FA10 51 5668 PUSH CX ; SAVE REGS CX,AX
FA11 50 5669 PUSH AX
FA12 8AE8 5670 MOV CH,AL ; AL=BYTE TO WRITE.
5671 ; (HS BIT WRITTEN FIRST)
FA14 B108 5672 MOV CL,8 ; FOR 8 DATA BITS IN BYTE.
5673 ; NOTE: TWO EDGES PER BIT
FA16 5674 W27: ; DISASSEMBLE THE DATA BIT
FA16 0DD5 5675 RCL CH,1 ; ROTATE HS BIT INTO CARRY
FA18 9C 5676 PUSHF ; SAVE FLAGS.
5677 ; NOTE: DATA BIT IS IN CARRY
FA19 E80B00 5678 CALL WRITE_BIT ; WRITE DATA BIT
FA1C 9D 5679 POPF ; RESTORE CARRY FOR CRC CALC
FA1D E82400 5680 CALL CRC_GEN ; COMPUTE CRC ON DATA BIT
FA20 FEC9 5681 DEC CL ; LOOP TILL ALL 8 BITS DONE
FA22 75F2 5682 JNZ W27 ; JUMP IF NOT DONE YET
FA24 58 5683 POP AX ; RESTORE REGS AX,CX
FA25 59 5684 POP CX
FA26 C3 5685 RET ; WE ARE FINISHED
5686 WRITE_BYTE ENDP
5687 ;-----
5688 ; PURPOSE: :
5689 ; TO WRITE A DATA BIT TO CASSETTE :
5690 ; CARRY FLAG CONTAINS DATA BIT :
5691 ; I.E. IF SET DATA BIT IS A ONE :
5692 ; IF CLEAR DATA BIT IS A ZERO :
5693 ; :
5694 ; NOTE: TWO EDGES ARE WRITTEN PER BIT :
5695 ; ONE BIT HAS 500 USEC BETWEEN EDGES :
5696 ; FOR A 1000 USEC PERIOD (1 MILLISEC) :
5697 ; :
5698 ; ZERO BIT HAS 250 USEC BETWEEN EDGES :
5699 ; FOR A 500 USEC PERIOD (.5 MILLISEC) :
5700 ; CARRY FLAG IS DATA BIT :
5701 ;-----
FA27 5702 WRITE_BIT PROC NEAR
5703 ; ASSUME IT'S A '1'
FA27 B8A004 5704 MOV AX,1184 ; SET AX TO NOMINAL ONE SIZE
FA2A 7203 5705 JC W28 ; JUMP IF ONE BIT
FA2C B85002 5706 MOV AX,592 ; NO, SET TO NOMINAL ZERO SIZE
FA2F 5707 W28: ; WRITE-BIT-AX
FA2F 50 5708 PUSH AX ; WRITE BIT WITH PERIOD EQ TO VALUE AX
FA30 5709 W29:
FA30 E462 5710 IN AL,PORT_C ; INPUT TIMER_0 OUTPUT
FA32 2420 5711 AND AL,020H

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LOC OBJ	LINE	SOURCE			
FA34 74FA	5712	JZ	W29		; LOOP TILL HIGH
FA36	5713	W30:	IN		
FA36 E642	5714		AL,PORT_C		; NOW WAIT TILL TIMER'S OUTPUT IS LOW
FA38 2420	5715	AND	AL,020H		
FA3A 75FA	5716	JNZ	W30		
	5717				; RELOAD TIMER WITH PERIOD
	5718				; FOR NEXT DATA BIT
FA3C 58	5719	POP	AX		; RESTORE PERIOD COUNT
FA3D	5720	W31:			; SET TIMER
FA3D E642	5721	OUT	042H, AL		; SET LOW BYTE OF TIMER 2
FA3F 0AC4	5722	MOV	AL, AH		
FA41 E642	5723	OUT	042H, AL		; SET HIGH BYTE OF TIMER 2
FA43 C3	5724	RET			
	5725	WRITE_BIT	ENDP		
	5726				-----
	5727				; UPDATE CRC REGISTER WITH NEXT DATA BIT
	5728				; CRC IS USED TO DETECT READ ERRORS
	5729				; ASSUMES DATA BIT IS IN CARRY
	5730				;
	5731				; REG AX IS MODIFIED
	5732				; FLAGS ARE MODIFIED
	5733				-----
FA44	5734	CRC_GEN	PROC NEAR		
FA44 A16900	5735	MOV	AX,CRC_REG		
	5736				; THE FOLLOWING INSTUCTIONS
	5737				; WILL SET THE OVERFLOW FLAG
	5738				; IF CARRY AND MS BIT OF CRC
	5739				; ARE UNEQUAL
FA47 D1D8	5740	RCL	AX,1		
FA49 D1D0	5741	RCL	AX,1		
FA4B F8	5742	CLC			; CLEAR CARRY
FA4C 7104	5743	JND	W32		; SKIP IF NO OVERFLOW
	5744				; IF DATA BIT XORED WITH
	5745				; CRC REG BIT 15 IS ONE
FA4E 351008	5746	XOR	AX,0B10H		; THEN XOR CRC REG WITH 0B01H
FA51 F9	5747	STC			; SET CARRY
FA52	5748	W32:			
FA52 D1D0	5749	RCL	AX,1		; ROTATE CARRY (DATA BIT)
	5750				; INTO CRC REG
FA54 A36900	5751	MOV	CRC_REG,AX		; UPDATE CRC_REG
FA57 C3	5752	RET			; FINISHED
	5753	CRC_GEN	ENDP		
	5754				
FA58	5755	BEGIN_OP	PROC NEAR		; START TAPE AND DELAY
FA58 E826FE	5756	CALL	MOTOR_ON		;TURN ON MOTOR
FA58 B342	5757	MOV	BL,42H		;DELAY FOR TAPE DRIVE
	5758				;TO GET UP TO SPEED (1/2 SEC)
FA5D	5759	W33:			
FA5D B90007	5760	MOV	CX,700H		;INNER LOOP= APPROX. 10 MILLISEC
FA60 E2FE	5761	W34:	LOOP W34		
FA62 FECB	5762	DEC	BL		
FA64 75F7	5763	JNZ	W33		
FA66 C3	5764	RET			
	5765	BEGIN_OP	ENDP		
	5766				
FA67 20323031	5767	E1	DB	' 201',13,10	
FA68 0D					
FA6C 0A					
	5768				-----
	5769				; CHARACTER GENERATOR GRAPHICS FOR 320X200 AND 640X200 GRAPHICS
	5770				-----
FA6E	5772	ORG	0FA6EH		
FA6E	5773	CRT_CHAR_GEN	LABEL BYTE		
FA6E 0000000000000000	5774	DB	000H,000H,000H,000H,000H,000H,000H,000H		; D_00
FA76 7E81A581B099817E	5775	DB	07EH,081H,0A5H,0B1H,0BDH,099H,0B1H,07EH		; D_01
FA7E 7EFPDBFFC3E7FF7E	5776	DB	07EH,0FFH,0DBH,0FFH,0C3H,0E7H,0FFH,07EH		; D_02
FA86 6CFEFEFE7C3E1000	5777	DB	06CH,0FEH,0FEH,0FEH,07CH,03BH,010H,000H		; D_03
FA8E 10387CFE7C381000	5778	DB	010H,038H,07CH,0FEH,07CH,03BH,010H,000H		; D_04
FA96 387C38FEFE7C387C	5779	DB	03BH,07CH,03BH,0FEH,0FEH,07CH,03BH,07CH		; D_05
FA9E 1010387CFE7C387C	5780	DB	010H,010H,03BH,07CH,0FEH,07CH,03BH,07CH		; D_06
FAA6 0000183C3C180000	5781	DB	000H,000H,018H,03CH,03CH,018H,000H,000H		; D_07
FAAE FFFFE7C3C3E7FFF	5782	DB	0FFH,0FFH,0E7H,0C3H,0C3H,0E7H,0FFH,0FFH		; D_08
FAB6 003C664202663C00	5783	DB	000H,03CH,066H,042H,042H,066H,03CH,000H		; D_09
FABE FFC399BDD099C3FF	5784	DB	0FFH,0C3H,099H,0BDH,0BDH,099H,0C3H,0FFH		; D_0A
FAc6 0F070F7DCCCCC78	5785	DB	0FFH,007H,00FH,07DH,0CCH,0CCH,0CCH,078H		; D_0B
FACE 3C6666663C187E18	5786	DB	03CH,066H,066H,066H,03CH,018H,07EH,018H		; D_0C

LOC OBJ	LINE	SOURCE	
FAD6	3F333F303070F0E0	5787	DB 03FH, 033H, 03FH, 030H, 030H, 070H, 0F0H, 0E0H ; D_0D
FADE	7F637F63637E6C0	5788	DB 07FH, 063H, 07FH, 063H, 063H, 067H, 066H, 066H ; D_0E
FAE6	995A3CE7E73CSA99	5789	DB 099H, 05AH, 03CH, 0E7H, 0E7H, 03CH, 05AH, 099H ; D_0F
FAEE	80E0F8FEF80E0000	5790	DB 080H, 0E0H, 0F8H, 0FEH, 0FEH, 0F8H, 0E0H, 080H ; D_10
FAF6	020E3FE3E30E0E0200	5791	DB 002H, 0E0H, 03EH, 0FEH, 03EH, 0E0H, 002H, 000H ; D_11
FAFE	183C7E18187E3C18	5792	DB 018H, 03CH, 07EH, 018H, 018H, 018H, 07EH, 03CH, 018H ; D_12
FB06	6666666666066060	5793	DB 066H, 066H, 066H, 066H, 066H, 000H, 066H, 000H ; D_13
FB0E	7F08D87B18181800	5794	DB 07FH, 08DH, 08DH, 07EH, 018H, 018H, 018H, 000H ; D_14
FB16	3E63386C6C38CC78	5795	DB 03EH, 063H, 038H, 06CH, 06CH, 038H, 0CCH, 078H ; D_15
FB1E	000000007E7E7E00	5796	DB 000H, 000H, 000H, 000H, 07EH, 07EH, 07EH, 000H ; D_16
FB26	183C7E18187E3C18F0	5797	DB 018H, 03CH, 07EH, 018H, 07EH, 03CH, 018H, 0FFH ; D_17
FB2E	183C7E1818181800	5798	DB 018H, 03CH, 07EH, 018H, 018H, 018H, 018H, 000H ; D_18
FB36	18181818187E3C1800	5799	DB 018H, 018H, 018H, 018H, 07EH, 03CH, 018H, 000H ; D_19
FB3E	00180CFE0C180000	5800	DB 000H, 018H, 00CH, 0FEH, 00CH, 018H, 000H, 000H ; D_1A
FB46	003060FE60300000	5801	DB 000H, 030H, 060H, 0FEH, 060H, 030H, 000H, 000H ; D_1B
FB4E	0000C0C0CF0E0000	5802	DB 000H, 000H, 0C0H, 0C0H, 0C0H, 0FEH, 000H, 000H ; D_1C
FB56	002464FF66240000	5803	DB 000H, 024H, 066H, 0FFH, 066H, 024H, 000H, 000H ; D_1D
FB5E	00183C7EFFFF0000	5804	DB 000H, 018H, 03CH, 07EH, 0FFH, 0FFH, 000H, 000H ; D_1E
FB66	00FFFF7E3C180000	5805	DB 000H, 0FFH, 0FFH, 07EH, 03CH, 018H, 000H, 000H ; D_1F
FB6E	0000000000000000	5806	DB 000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; SP_D_20
FB76	3078783030000000	5807	DB 030H, 078H, 078H, 030H, 030H, 000H, 030H, 000H ; I_D_21
FB7E	6C6C6C0000000000	5808	DB 06CH, 06CH, 06CH, 000H, 000H, 000H, 000H, 000H ; I_D_22
FB86	6C6CFE6CFE6C6C00	5809	DB 06CH, 06CH, 0FEH, 06CH, 0FEH, 06CH, 06CH, 000H ; I_D_23
FB8E	307CC0780CF83000	5810	DB 030H, 07CH, 0C0H, 078H, 0C0H, 0F8H, 030H, 000H ; I_D_24
FB96	00C6C183066C6600	5811	DB 000H, 0C6H, 0CCH, 018H, 030H, 066H, 06CH, 000H ; PER_CENT_D_25
FB9E	386C38780CCC7800	5812	DB 038H, 06CH, 038H, 07EH, 0CCH, 0CCH, 078H, 000H ; I_D_26
FA8A	060C000000000000	5813	DB 060H, 060H, 0C0H, 000H, 000H, 000H, 000H, 000H ; I_D_27
FA8E	1830606060301800	5814	DB 018H, 030H, 060H, 060H, 060H, 030H, 018H, 000H ; I_D_28
FA8E	6030181818306000	5815	DB 060H, 030H, 018H, 018H, 018H, 030H, 060H, 000H ; I_D_29
FA8E	00663CFF3C660000	5816	DB 000H, 066H, 03CH, 0FFH, 03CH, 066H, 000H, 000H ; *D_2A
FA8E	0030F0C303000000	5817	DB 000H, 030H, 030H, 0FCH, 030H, 030H, 000H, 000H ; +D_2B
FA8E	0000000000303060	5818	DB 000H, 000H, 000H, 000H, 000H, 030H, 030H, 060H ; +D_2C
FA8E	0000000FC0000000	5819	DB 000H, 000H, 000H, 0FCH, 000H, 000H, 000H, 000H ; -D_2D
FA8E	0000000000000000	5820	DB 000H, 000H, 000H, 000H, 000H, 030H, 030H, 000H ; -D_2E
FA8E	060C183060C08000	5821	DB 060H, 0CCH, 018H, 030H, 060H, 0CCH, 080H, 000H ; /D_2F
FA8E	7CC0C0E0F6E67C00	5822	DB 07CH, 0C6H, 0CEH, 0DEH, 0F6H, 0E6H, 07CH, 000H ; 0_D_30
FA8E	307030303030FC00	5823	DB 030H, 070H, 030H, 030H, 030H, 030H, 0FCCH, 000H ; 1_D_31
FA8E	78CC0C3860CCFC00	5824	DB 078H, 0CCH, 00CH, 038H, 060H, 0CCH, 0FCH, 000H ; 2_D_32
FA8E	78CC0C380CC78000	5825	DB 078H, 0CCH, 00CH, 038H, 00CH, 0CCH, 078H, 000H ; 3_D_33
FA8E	1C3C6CCCFE0C1E00	5826	DB 01CH, 03CH, 06CH, 0CCH, 0FEH, 0FEH, 0CCH, 01EH, 000H ; 4_D_34
FA8E	FC0CF80C0CC78000	5827	DB 0FCH, 0C0H, 0F8H, 00CH, 00CH, 0CCH, 078H, 000H ; 5_D_35
FA8E	3860C0F8CC0C7800	5828	DB 038H, 060H, 0C0H, 0F8H, 0CCH, 0CCH, 078H, 000H ; 6_D_36
FA8E	FC0C018303030000	5829	DB 0FCH, 0CCH, 00CH, 018H, 030H, 030H, 030H, 000H ; 7_D_37
FA8E	78CC0C78CC0C7800	5830	DB 078H, 0CCH, 0CCH, 078H, 0CCH, 0CCH, 078H, 000H ; 8_D_38
FA8E	78CC0C7C0C187000	5831	DB 078H, 0CCH, 0CCH, 07CH, 00CH, 018H, 070H, 000H ; 9_D_39
FA8E	0030300000303060	5832	DB 000H, 030H, 030H, 000H, 000H, 030H, 030H, 000H ; +D_3A
FA8E	0030300000303060	5833	DB 000H, 030H, 030H, 000H, 000H, 030H, 030H, 060H ; +D_3B
FA8E	183060C060301800	5834	DB 018H, 030H, 060H, 0C0H, 060H, 030H, 030H, 018H, 000H ; <D_3C
FA8E	0000FC0000FC0000	5835	DB 000H, 000H, 0FCH, 000H, 000H, 0FCH, 000H, 000H ; =D_3D
FA8E	6030180C18306000	5836	DB 060H, 030H, 018H, 00CH, 018H, 030H, 060H, 000H ; >D_3E
FA8E	78CC0C1830003000	5837	DB 078H, 0CCH, 00CH, 018H, 030H, 000H, 030H, 000H ; ?D_3F
FA8E	7CC6DEDE0C07800	5838	DB 07CH, 0C6H, 0DEH, 0DEH, 0DEH, 0C0H, 078H, 000H ; @D_40
FA8E	3078CC0CFCCCC000	5839	DB 030H, 078H, 0CCH, 0CCH, 0FCH, 0CCH, 0CCH, 000H ; A_D_41
FA8E	FC6667C6666C0000	5840	DB 0FCH, 066H, 066H, 07CH, 066H, 066H, 0FCH, 000H ; B_D_42
FA8E	3C66C0C0C0663C00	5841	DB 03CH, 066H, 0C0H, 0C0H, 0C0H, 066H, 03CH, 000H ; C_D_43
FA8E	F86C6666666CF800	5842	DB 0F8H, 06CH, 066H, 066H, 066H, 06CH, 0F8H, 000H ; D_D_44
FA8E	FE6268786862FE00	5843	DB 0FEH, 062H, 068H, 078H, 068H, 062H, 0FEH, 000H ; E_D_45
FA8E	FE6268786862FE00	5844	DB 0FEH, 062H, 068H, 078H, 068H, 060H, 0F0H, 000H ; F_D_46
FA8E	3C66C0C0CE663E00	5845	DB 03CH, 066H, 0C0H, 0C0H, 0CEH, 066H, 03EH, 000H ; 6_D_47
FA8E	CCCCCFCCCCCCCC00	5846	DB 0CCH, 0CCH, 0CCH, 0FCH, 0CCH, 0CCH, 0CCH, 000H ; H_D_48
FA8E	7830303030307800	5847	DB 078H, 030H, 030H, 030H, 030H, 030H, 078H, 000H ; I_D_49
FA8E	1E0C0C0C0C0C0C00	5848	DB 01EH, 0C0H, 00CH, 00CH, 0CCH, 0CCH, 0CCH, 078H, 000H ; J_D_4A
FA8E	E6666786C66E6000	5849	DB 0E6H, 066H, 06CH, 078H, 06CH, 066H, 0E6H, 000H ; K_D_4B
FA8E	F0606060626E6F00	5850	DB 0F0H, 060H, 060H, 060H, 062H, 066H, 0FEH, 000H ; L_D_4C
FA8E	C6E6E6E6E6C6C600	5851	DB 0C6H, 0EEH, 0FEH, 0FEH, 0DEH, 0C6H, 0C6H, 000H ; M_D_4D
FA8E	C6E6E6E6E6C6C600	5852	DB 0C6H, 0E6H, 0F6H, 0DEH, 0CEH, 0C6H, 0C6H, 000H ; N_D_4E
FA8E	386C0C6C64C38000	5853	DB 038H, 06CH, 0C6H, 0C6H, 0C6H, 06CH, 038H, 000H ; O_D_4F
FA8E	FC66667C6060F000	5854	DB 0FCH, 066H, 066H, 07CH, 060H, 060H, 0F0H, 000H ; P_D_50
FA8E	78CCCCC0C781C000	5855	DB 078H, 0CCH, 0CCH, 0CCH, 0DCH, 078H, 01CH, 000H ; Q_D_51
FA8E	FC66667C606E6000	5856	DB 0FCH, 066H, 066H, 07CH, 06CH, 066H, 0E6H, 00CH ; R_D_52
FA8E	78CCE0701CCC7800	5857	DB 078H, 0CCH, 0E0H, 070H, 01CH, 0CCH, 078H, 000H ; S_D_53
FA8E	FC84303030307800	5858	DB 0FCH, 0B4H, 030H, 030H, 030H, 030H, 078H, 000H ; T_D_54
FA8E	CCCCCCCCCCCC0000	5859	DB 0CCH, 0CCH, 0CCH, 0CCH, 0CCH, 0CCH, 0FCH, 000H ; U_D_55
FA8E	CCCCCCCC78300000	5860	DB 0CCH, 0CCH, 0CCH, 0CCH, 0CCH, 078H, 030H, 000H ; V_D_56
FA8E	C6C6C6D6FEFEE6C000	5861	DB 0C6H, 0C6H, 0C6H, 0D6H, 0FEH, 0EEH, 0C6H, 000H ; W_D_57
FA8E	C6C6C638386C6C0000	5862	DB 0C6H, 0C6H, 06CH, 038H, 038H, 06CH, 0C6H, 000H ; X_D_58
FA8E	CCCCC78303078000	5863	DB 0CCH, 0CCH, 0CCH, 078H, 030H, 030H, 078H, 000H ; Y_D_59

Appendix A

LOC OBJ	LINE	SOURCE
FD3E	FE68C183266FE00	5864 DB 0FEH,0C6H,08CH,018H,032H,066H,0FEH,000H ; Z_D_5A
FD46	7860606060607800	5865 DB 078H,060H,060H,060H,060H,060H,078H,000H ; I_D_5B
FD4E	C0630180C060200	5866 DB 0C0H,060H,030H,018H,0C0H,066H,002H,000H ; BACKSLASH_D_5C
FD56	7818181818187800	5867 DB 078H,018H,018H,018H,018H,018H,078H,000H ; I_D_5D
FD5E	10366CC600000000	5868 DB 010H,038H,06CH,0C6H,000H,000H,000H,000H ; CIRCUMFLEX_D_5E
FD66	00000000000000FF	5869 DB 000H,000H,000H,000H,000H,000H,000H,0FFH ; _D_5F
FD6E	3030180000000000	5870 DB 030H,030H,018H,000H,000H,000H,000H,000H ; 'D_60
FD76	0000780C7CC67600	5871 DB 000H,000H,078H,0C0H,07CH,066H,076H,000H ; LOWER CASE A D_61
FD7E	E060607C66660C00	5872 DB 0E0H,060H,060H,07CH,066H,066H,0DCH,000H ; L.C. B_D_62
FD86	000078CC0CC78000	5873 DB 000H,000H,078H,0CCH,0C0H,0CCH,078H,000H ; L.C. C_D_63
FD8E	1C0C0C7C0CC07600	5874 DB 01CH,0C0H,0C0H,07CH,0CCH,0CCH,076H,000H ; L.C. D_D_64
FD96	000078CCFFC07800	5875 DB 000H,000H,078H,0CCH,0FCH,0C0H,078H,000H ; L.C. E_D_65
FD9E	386C6F06060F0000	5876 DB 038H,06CH,060H,0F0H,060H,0F0H,060H,000H ; L.C. F_D_66
FDAA	000076CCCC7C0CF8	5877 DB 000H,000H,076H,0CCH,0CCH,07CH,0CCH,0F8H ; L.C. G_D_67
FDAA	E0606C766666E000	5878 DB 0E0H,060H,06CH,076H,066H,066H,0E6H,000H ; L.C. H_D_68
FD86	3000703030307800	5879 DB 030H,000H,070H,030H,030H,030H,078H,000H ; L.C. I_D_69
FD8E	0C000C0C0C0C0C78	5880 DB 0C0H,000H,0C0H,0C0H,0C0H,0C0H,0C0H,078H ; L.C. J_D_6A
FD06	E060666C786C6E00	5881 DB 0E0H,060H,066H,06CH,078H,06CH,0E6H,000H ; L.C. K_D_6B
FD0E	7030303030307800	5882 DB 070H,030H,030H,030H,030H,030H,078H,000H ; L.C. L_D_6C
FD06	0000CFEF66CC6C00	5883 DB 000H,000H,0CCH,0FEH,0FEH,0D6H,0C6H,000H ; L.C. M_D_6D
FD0E	0000F8CC0CC0C000	5884 DB 000H,000H,0F8H,0CCH,0CCH,0CCH,0CCH,000H ; L.C. N_D_6E
FD06	000078CCCC0C7800	5885 DB 000H,000H,078H,0CCH,0CCH,0CCH,078H,000H ; L.C. O_D_6F
FD0E	0000DC66667C0F00	5886 DB 000H,000H,0DCH,066H,066H,07CH,060H,0F0H ; L.C. P_D_70
FD06	000076CCCC7C0C1E	5887 DB 000H,000H,076H,0CCH,0CCH,07CH,060H,01EH ; L.C. Q_D_71
FD0E	0000DC766660F000	5888 DB 000H,000H,0DCH,076H,066H,060H,0F0H,000H ; L.C. R_D_72
FE06	00007CC0780CF800	5889 DB 000H,000H,07CH,0C0H,078H,0C0H,0F8H,000H ; L.C. S_D_73
FE0E	10307C3030341800	5890 DB 010H,030H,07CH,030H,030H,034H,018H,000H ; L.C. T_D_74
FE16	0000CCCC0CC78000	5891 DB 000H,000H,0CCH,0CCH,0CCH,0CCH,0CCH,078H,000H ; L.C. U_D_75
FE1E	0000CCCC0C783000	5892 DB 000H,000H,0CCH,0CCH,0CCH,0CCH,078H,030H,000H ; L.C. V_D_76
FE26	0000C6D6FFE6C000	5893 DB 000H,000H,0C6H,0D6H,0FEH,0FEH,06CH,000H ; L.C. W_D_77
FE2E	0000C66C386CC600	5894 DB 000H,000H,0C6H,06CH,038H,06CH,0C6H,000H ; L.C. X_D_78
FE36	0000C0CC0C7C0CF8	5895 DB 000H,000H,0CCH,0CCH,0CCH,07CH,0C0H,0F8H ; L.C. Y_D_79
FE3E	0000FC983064FC00	5896 DB 000H,000H,0FCH,098H,030H,064H,0FCH,000H ; L.C. Z_D_7A
FE46	1C3030E030301C00	5897 DB 01CH,030H,030H,0E0H,030H,030H,01CH,000H ; C_D_7B
FE4E	1818180018181800	5898 DB 018H,018H,018H,000H,018H,018H,018H,000H ; D_7C
FE56	E030301C3030E000	5899 DB 0E0H,030H,030H,01CH,030H,030H,0E0H,000H ; D_7D
FE5E	76D0000000000000	5900 DB 076H,0DCH,000H,000H,000H,000H,000H,000H ; TILDE_D_7E
FE66	0010366CC6C6FE00	5901 DB 000H,010H,038H,06CH,0C6H,0C6H,0FEH,000H ; DELTA_D_7F
	5902	
	5903	-----INT 1A-----
	5904	; TIME_OF_DAY ;
	5905	; THIS ROUTINE ALLOWS THE CLOCK TO BE SET/READ ;
	5906	; ;
	5907	; INPUT ;
	5908	; (AH) = 0 READ THE CURRENT CLOCK SETTING ;
	5909	; RETURNS CX = HIGH PORTION OF COUNT ;
	5910	; DX = LOW PORTION OF COUNT ;
	5911	; AL = 0 IF TIMER HAS NOT PASSED; ;
	5912	; 24 HOURS SINCE LAST READ ;
	5913	; <0 IF ON ANOTHER DAY ;
	5914	; (AH) = 1 SET THE CURRENT CLOCK ;
	5915	; CX = HIGH PORTION OF COUNT ;
	5916	; DX = LOW PORTION OF COUNT ;
	5917	; NOTE: COUNTS OCCUR AT THE RATE OF ;
	5918	; 1193180/65536 COUNTS/SEC ;
	5919	; (OR ABOUT 18.2 PER SECOND -- SEE EQUATES BELOW) ;
	5920	-----
FE6E	5921	ASSUME CS:CODE,DS:DATA
FE6E	5922	ORG OFE6EH
FE6E	5923	TIME_OF_DAY PROC FAR
FE6E	5924	STI ; INTERRUPTS BACK ON
FE6F	5925	PUSH DS ; SAVE SEGMENT
FE70	5926	CALL DDS
FE73	5927	OR AH,AH ; AH=0
FE75	5928	JZ T2 ; READ_TIME
FE77	5929	DEC AH ; AH=1
FE79	5930	JZ T3 ; SET_TIME
FE7B	5931	T1: ; TOD_RETURN
FE7B	5932	STI ; INTERRUPTS BACK ON
FE7C	5933	POP DS ; RECOVER SEGMENT
FE7D	5934	IRET ; RETURN TO CALLER
FE7E	5935	T2: ; READ_TIME
FE7E	5936	CLI ; NO TIMER INTERRUPTS WHILE READING
FE7F	5937	MOV AL,TIMER_OFL
FE82	5938	MOV TIMER_OFL,0 ; GET OVERFLOW, AND RESET THE FLAG
FE87	5939	MOV CX,TIMER_HIGH
FE8B	5940	MOV DX,TIMER_LOW

LOC OBJ	LINE	SOURCE
FE0F EBEA	5941	JMP T1 ; TOD_RETURN
FE91	5942	T3: ; SET_TIME
FE91 FA	5943	CLI ; NO INTERRUPTS WHILE WRITING
FE92 89166C00	5944	MOV TIMER_LOW,DX
FE96 890E6E00	5945	MOV TIMER_HIGH,CX ; SET THE TIME
FE9A C606700000	5946	MOV TIMER_OF_L,0 ; RESET OVERFLOW
FE9F EBDA	5947	JMP T1 ; TOD_RETURN
	5948	TIME_OF_DAY ENDP
	5949	
	5950	;
	5951	; THIS ROUTINE HANDLES THE TIMER INTERRUPT FROM :
	5952	; CHANNEL 0 OF THE 8253 TIMER. INPUT FREQUENCY :
	5953	; IS 1.19318 MHZ AND THE DIVISOR IS 65536, RESULTING :
	5954	; IN APPROX. 18.2 INTERRUPTS EVERY SECOND. :
	5955	;
	5956	; THE INTERRUPT HANDLER MAINTAINS A COUNT OF INTERRUPTS :
	5957	; SINCE POWER ON TIME, WHICH MAY BE USED TO ESTABLISH :
	5958	; TIME OF DAY. :
	5959	; THE INTERRUPT HANDLER ALSO DECREMENTS THE MOTOR :
	5960	; CONTROL COUNT OF THE DISKETTE, AND WHEN IT EXPIRES, :
	5961	; WILL TURN OFF THE DISKETTE MOTOR, AND RESET THE :
	5962	; MOTOR RUNNING FLAGS. :
	5963	; THE INTERRUPT HANDLER WILL ALSO INVOKE A USER ROUTINE :
	5964	; THROUGH INTERRUPT ICH AT EVERY TIME TICK. THE USER :
	5965	; MUST CODE A ROUTINE AND PLACE THE CORRECT ADDRESS IN :
	5966	; THE VECTOR TABLE. :
	5967	;
	5968	ORG OFEA5H
FEA5	5969	TIMER_INT PROC FAR
FEA5 FB	5970	STI ; INTERRUPTS BACK ON
FEA6 1E	5971	PUSH DS
FEA7 50	5972	PUSH AX
FEA8 52	5973	PUSH DX ; SAVE MACHINE STATE
FEA9 E89200	5974	CALL DDS
FEAC FF066C00	5975	INC TIMER_LOW ; INCREMENT TIME
FEB0 7504	5976	JNZ T4 ; TEST_DAY
FEB2 FF066E00	5977	INC TIMER_HIGH ; INCREMENT HIGH WORD OF TIME
FEB6	5978	T4: ; TEST_DAY
FEB6 833E6E0018	5979	CHP TIMER_HIGH,018H ; TEST FOR COUNT EQUALING 24 HOURS
FEBB 7515	5980	JNZ T5 ; DISKETTE_CTL
FEBD 813E6C00B000	5981	CHP TIMER_LOW,0B0H
FEC3 750D	5982	JNZ T5 ; DISKETTE_CTL
	5983	
	5984	;----- TIMER HAS GONE 24 HOURS
	5985	
FEC5 28C0	5986	SUB AX,AX
FEC7 A36E00	5987	MOV TIMER_HIGH,AX
FECA A36C00	5988	MOV TIMER_LOW,AX
FEC0 C606700001	5989	MOV TIMER_OF_L,1
	5990	
	5991	;----- TEST FOR DISKETTE TIME OUT
	5992	
FED2	5993	T5: ; DISKETTE_CTL
FED2 FE0E4000	5994	DEC MOTOR_COUNT
FED6 750B	5995	JNZ T6 ; RETURN IF COUNT NOT OUT
FED8 80263F00F0	5996	AND MOTOR_STATUS,0F0H ; TURN OFF MOTOR RUNNING BITS
FEDD B00C	5997	MOV AL,0CH
FEDF BAF203	5998	MOV DX,03F2H ; FDC CTL PORT
FE2E EE	5999	OUT DX,AL ; TURN OFF THE MOTOR
FE23	6000	T6: ; TIMER_RET:
FE23 CD1C	6001	INT ICH ; TRANSFER CONTROL TO A USER ROUTINE
FE25 8020	6002	MOV AL,EOI
FE27 E620	6003	OUT 020H,AL ; END OF INTERRUPT TO 8259
FE29 5A	6004	POP DX
FE2A 58	6005	POP AX
FE2B 1F	6006	POP DS ; RESET MACHINE STATE
FE2C CF	6007	IRET ; RETURN FROM INTERRUPT
	6008	TIMER_INT ENDP
	6009	
FEE0 31883031	6010	F3B DB '1801',13,10
FEF1 0D		
FEF2 0A		
	6011	
	6012	;
	6013	; THESE ARE THE VECTORS WHICH ARE MOVED INTO :
	6014	; THE 8086 INTERRUPT AREA DURING POWER ON. :
	6015	; ONLY THE OFFSETS ARE DISPLAYED HERE, CODE SEGMENT :

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LOC OBJ          LINE  SOURCE
6016 ;           WILL BE ADDED FOR ALL OF THEM, EXCEPT WHERE NOTED :
6017 ;-----
6018           ASSUME  CS:CODE
6019           ORG    0FEF3H
FEF3           6020  VECTOR_TABLE LABEL WORD ; VECTOR TABLE FOR MOVE TO INTERRUPTS
FEF3 A5FE      6021  DW    OFFSET TIMER_INT ; INTERRUPT 8
FEF5 87E9      6022  DW    OFFSET KB_INT ; INTERRUPT 9
FEF7 DDE6      6023  DW    OFFSET D_EOI ; INTERRUPT A
FEF9 DDE6      6024  DW    OFFSET D_EOI ; INTERRUPT B
FEFB DDE6      6025  DW    OFFSET D_EOI ; INTERRUPT C
FEFD DDE6      6026  DW    OFFSET D_EOI ; INTERRUPT D
FEFF 57EF      6027  DW    OFFSET DISK_INT ; INTERRUPT E
FF01 DDE6      6028  DW    OFFSET D_EOI ; INTERRUPT F
FF03 65F0      6029  DW    OFFSET VIDEO_IO ; INTERRUPT 10H
FF05 4DF8      6030  DW    OFFSET EQUIPMENT ; INTERRUPT 11H
FF07 41F8      6031  DW    OFFSET MEMORY_SIZE_DET ; INTERRUPT 12H
FF09 59EC      6032  DW    OFFSET DISKETTE_IO ; INTERRUPT 13H
FF0B 39E7      6033  DW    OFFSET RS232_IO ; INTERRUPT 14H
FF0D 59F8      6034  DW    OFFSET CASSETTE_IO ; INTERRUPT 15H
FF0F 2EE8      6035  DW    OFFSET KEYBOARD_IO ; INTERRUPT 16H
FF11 D2EF      6036  DW    OFFSET PRINTER_IO ; INTERRUPT 17H
6037
FF13 0000      6038  DW    00000H ; INTERRUPT 18H
6039 ;          DW    0F600H ; MUST BE INSERTED INTO TABLE LATER
6040
FF15 F2E6      6041  DW    OFFSET BOOT_STRAP ; INTERRUPT 19H
FF17 6EFE      6042  DW    TIME_OF_DAY ; INTERRUPT 1AH -- TIME OF DAY
FF19 53FF      6043  DW    DUMMY_RETURN ; INTERRUPT 1BH -- KEYBOARD BREAK ADDR
FF1B 53FF      6044  DW    DUMMY_RETURN ; INTERRUPT 1C -- TIMER BREAK ADDR
FF1D A4F0      6045  DW    VIDEO_PARMS ; INTERRUPT 1D -- VIDEO PARAMETERS
FF1F C7EF      6046  DW    OFFSET DISK_BASE ; INTERRUPT 1E -- DISK PARMS
FF21 0000      6047  DW    0 ; INTERRUPT 1F -- POINTER TO VIDEO EXT
6048
FF23 5041B249545920 6049  D2  DB 'PARITY CHECK 1',13,10
43484543482031
FF31 0D
FF32 0A
FF33 20333031    6050  F1  DB ' 301',13,10
FF37 0D
FF38 0A
FF39 313331    6051  F2  DB '131',13,10
FF3C 0D
FF3D 0A
6052
FF3E           6053  DDS  PROC  NEAR
FF3E 50         6054  PUSH  AX ; SAVE AX
FF3F B84000     6055  MOV   AX,DATA
FF42 8ED8      6056  MOV   DS,AX ; SET DATA SEGMENT
FF44 58        6057  POP   AX ; RESTORE AX
FF45 C3        6058  RET
6059  DDS  ENDP
6060
6061 ;-----
6062 ;          TEMPORARY INTERRUPT SERVICE ROUTINE :
6063 ;-----
FF47           6064  ORG    0FF47H
FF47           6065  D11  PROC  NEAR
FF47 B401      6066  MOV   AH,1
FF49 50       6067  PUSH  AX ; SAVE REG AX CONTENTS
FF4A B0FF     6068  MOV   AL,OFFH ; MASK ALL INTERRUPTS OFF
FF4C E621     6069  OUT   INTA01,AL
FF4E B020     6070  MOV   AL,E0I
FF50 E620     6071  OUT   INTA00,AL
FF52 58       6072  POP   AX ; RESTORE REG AX CONTENTS
FF53           6073  DUMMY_RETURN: ; NEED IRET FOR VECTOR TABLE
FF53 CF       6074  IRET
6075  D11  ENDP
6076
6077 ;-- INT 5 -----
6078 ;          THIS LOGIC WILL BE INVOKED BY INTERRUPT 05H TO PRINT THE :
6079 ;          SCREEN. THE CURSOR POSITION AT THE TIME THIS ROUTINE IS INVOKED :
6080 ;          WILL BE SAVED AND RESTORED UPON COMPLETION. THE ROUTINE IS :
6081 ;          INTENDED TO RUN WITH INTERRUPTS ENABLED. IF A SUBSEQUENT :
6082 ;          'PRINT SCREEN' KEY IS DEPRESSED DURING THE TIME THIS ROUTINE :
6083 ;          IS PRINTING IT WILL BE IGNORED. :
6084 ;          ADDRESS 50:0 CONTAINS THE STATUS OF THE PRINT SCREEN: :
6085 ;

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6086 ; 50:0 =0 EITHER PRINT SCREEN HAS NOT BEEN CALLED ;
6087 ; OR UPON RETURN FROM A CALL THIS INDICATES ;
6088 ; A SUCCESSFUL OPERATION. ;
6089 ; =1 PRINT SCREEN IS IN PROGRESS ;
6090 ; =255 ERROR ENCOUNTERED DURING PRINTING ;
6091 ;-----;
6092 ASSUME CS:CODE,DS:XXDATA
FF54 6093 ORG OFF54H
FF54 6094 PRINT_SCREEN PROC FAR
FF54 FB 6095 STI ; MUST RUN WITH INTERRUPTS ENABLED
FF55 1E 6096 PUSH DS ; MUST USE 50:0 FOR DATA AREA STORAGE
FF56 50 6097 PUSH AX
FF57 53 6098 PUSH BX
FF58 51 6099 PUSH CX ; WILL USE THIS LATER FOR CURSOR LIMITS
FF59 52 6100 PUSH DX ; WILL HOLD CURRENT CURSOR POSITION
FF5A B85000 6101 MOV AX,XXDATA ; HEX 50
FF5D 8ED8 6102 MOV DS,AX
FF5F 803E000001 6103 CMP STATUS_BYTE,1 ; SEE IF PRINT ALREADY IN PROGRESS
FF64 745F 6104 JZ EXIT ; JUMP IF PRINT ALREADY IN PROGRESS
FF66 C060000001 6105 MOV STATUS_BYTE,1 ; INDICATE PRINT NOW IN PROGRESS
FF6B B40F 6106 MOV AH,15 ; WILL REQUEST THE CURRENT SCREEN MODE
FF6D C010 6107 INT 10H ; [AL]=MODE
6108 ; [AH]=NUMBER COLUMNS/LINE
6109 ; [BH]=VISUAL PAGE
6110 ;-----;
6111 ; AT THIS POINT WE KNOW THE COLUMNS/LINE ARE IN ;
6112 ; [AX] AND THE PAGE IF APPLICABLE IS IN [BH]. THE STACK ;
6113 ; HAS DS,AX,BX,CX,DX PUSHED. [AL] HAS VIDEO MODE ;
6114 ;-----;
FF6F 8ACC 6115 MOV CL,AH ; WILL MAKE USE OF [CX] REGISTER TO
FF71 B519 6116 MOV CH,25 ; CONTROL ROW & COLUMNS
FF73 E85500 6117 CALL CRLF ; CARRIAGE RETURN LINE FEED ROUTINE
FF76 51 6118 PUSH CX ; SAVE SCREEN BOUNDS
FF77 B403 6119 MOV AH,3 ; WILL NOW READ THE CURSOR.
FF79 C010 6120 INT 10H ; AND PRESERVE THE POSITION
FF7B 59 6121 POP CX ; RECALL SCREEN BOUNDS
FF7C 52 6122 PUSH DX ; RECALL [BH]=VISUAL PAGE
FF7D 33D2 6123 XOR DX,DX ; WILL SET CURSOR POSITION TO [0,0]
6124 ;-----;
6125 ; THE LOOP FROM PRI10 TO THE INSTRUCTION PRIOR TO PRI20 ;
6126 ; IS THE LOOP TO READ EACH CURSOR POSITION FROM THE ;
6127 ; SCREEN AND PRINT. ;
6128 ;-----;
FF7F 6129 PRI10:
FF7F B402 6130 MOV AH,2 ; TO INDICATE CURSOR SET REQUEST
FF81 C010 6131 INT 10H ; NEW CURSOR POSITION ESTABLISHED
FF83 B408 6132 MOV AH,8 ; TO INDICATE READ CHARACTER
FF85 C010 6133 INT 10H ; CHARACTER NOW IN [AL]
FF87 0AC0 6134 OR AL,AL ; SEE IF VALID CHAR
FF89 7502 6135 JNZ PRI15 ; JUMP IF VALID CHAR
FF8B B020 6136 MOV AL,' ' ; MAKE A BLANK
FF8D 6137 PRI15:
FF8D 52 6138 PUSH DX ; SAVE CURSOR POSITION
FF8E 33D2 6139 XOR DX,DX ; INDICATE PRINTER 1
FF90 32E4 6140 XOR AH,AH ; TO INDICATE PRINT CHAR IN [AL]
FF92 C017 6141 INT 17H ; PRINT THE CHARACTER
FF94 5A 6142 POP DX ; RECALL CURSOR POSITION
FF95 F6CA25 6143 TEST AH,25H ; TEST FOR PRINTER ERROR
FF98 7521 6144 JNZ ERR10 ; JUMP IF ERROR DETECTED
FF9A FEC2 6145 INC DL ; ADVANCE TO NEXT COLUMN
FF9C 3ACA 6146 CMP CL,DL ; SEE IF AT END OF LINE
FF9E 75DF 6147 JNZ PRI10 ; IF NOT PROCEED
FFA0 32D2 6148 XOR DL,DL ; BACK TO COLUMN 0
FFA2 8AE2 6149 MOV AH,DL ; [AH]=0
FFA4 52 6150 PUSH DX ; SAVE NEW CURSOR POSITION
FFA5 E82300 6151 CALL CRLF ; LINE FEED CARRIAGE RETURN
FFA8 5A 6152 POP DX ; RECALL CURSOR POSITION
FFA9 FEC6 6153 INC DH ; ADVANCE TO NEXT LINE
FFAB 3AEE 6154 CMP CH,DH ; FINISHED?
FFAD 75D0 6155 JNZ PRI10 ; IF NOT CONTINUE
FFAF 6156 PRI20:
FFAF 5A 6157 POP DX ; RECALL CURSOR POSITION
FFB0 B402 6158 MOV AH,2 ; TO INDICATE CURSOR SET REQUEST
FFB2 C010 6159 INT 10H ; CURSOR POSITION RESTORED
FFB4 C060000000 6160 MOV STATUS_BYTE,0 ; INDICATE FINISHED
FFB9 E80A 6161 JHP SHORT EXIT ; EXIT THE ROUTINE
FFBB 6162 ERR10:

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LOC OBJ          LINE  SOURCE
FFB8 5A          6163      POP      DX          ; GET CURSOR POSITION
FFBC B402        6164      MOV      AH,2      ; TO REQUEST CURSOR SET
FFBE CD10        6165      INT      10H      ; CURSOR POSITION RESTORED
FFC0             6166      ERR20:
FFC0 C6060000FF  6167      MOV      STATUS_BYTE,0FFH ; INDICATE ERROR
FFC5             6168      EXIT:
FFC5 5A          6169      POP      DX          ; RESTORE ALL THE REGISTERS USED
FFC6 59          6170      POP      CX
FFC7 5B          6171      POP      BX
FFC8 58          6172      POP      AX
FFC9 1F          6173      POP      DS
FFCA CF          6174      IRET
                6175      PRINT_SCREEN  ENDP
                6176
                6177      ;----- CARRIAGE RETURN, LINE FEED SUBROUTINE
                6178
FFCB             6179      CRLF  PROC  NEAR
FFCB 33D2        6180      XOR     DX,DX      ; PRINTER 0
FFCD 32E4        6181      XOR     AH,AH      ; WILL NOW SEND INITIAL LF,CR
                6182      ; TO PRINTER
FFCF B00A        6183      MOV     AL,120     ; LF
FFD1 CD17        6184      INT     17H      ; SEND THE LINE FEED
FFD3 32E4        6185      XOR     AH,AH      ; NOW FOR THE CR
FFD5 B00D        6186      MOV     AL,150     ; CR
FFD7 CD17        6187      INT     17H      ; SEND THE CARRIAGE RETURN
FFD9 C3          6188      RET
                6189      CRLF  ENDP
                6190
FFDA 50415249545920 6191      DI      DB      'PARITY CHECK 2',13,10
                434845434B2032
FFE8 0D          6192
FFE9 0A          6192      F3      DB      '601',13,10
FFEA 363031
FFED 0D          6193
FFEE 0A          6194      CODE  ENDS
                6195
                6196      ;-----
                6197      ; POWER ON RESET VECTOR :
                6198      ;-----
                6199      VECTOR SEGMENT AT 0FFFFH
                6200
                6201      ;----- POWER ON RESET
                6202
0000 EASB000F0    6203      JMP     RESET
                6204
0005 31302F32372F38 6205      DB     '10/27/82' ; RELEASE MARKER
                32
                6206      VECTOR ENDS
                6207      END

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1      $TITLE(FIXED DISK BIOS FOR IBM DISK CONTROLLER)
2
3      |-- INT 13 -----
4      ;
5      ; FIXED DISK I/O INTERFACE
6      ;
7      ; THIS INTERFACE PROVIDES ACCESS TO 5 1/4" FIXED DISKS
8      ; THROUGH THE IBM FIXED DISK CONTROLLER.
9      ;
10     ;-----
11
12     ;-----
13     ; THE BIOS ROUTINES ARE MEANT TO BE ACCESSED THROUGH
14     ; SOFTWARE INTERRUPTS ONLY. ANY ADDRESSES PRESENT IN
15     ; THE LISTINGS ARE INCLUDED ONLY FOR COMPLETENESS,
16     ; NOT FOR REFERENCE. APPLICATIONS WHICH REFERENCE
17     ; ABSOLUTE ADDRESSES WITHIN THE CODE SEGMENT
18     ; VIOLATE THE STRUCTURE AND DESIGN OF BIOS.
19     ;-----
20
21     ; INPUT (AH = HEX VALUE)
22
23     ; (AH)=00 RESET DISK (DL = 80H,81H) / DISKETTE
24     ; (AH)=01 READ THE STATUS OF THE LAST DISK OPERATION INTO (AL)
25     ; NOTE: DL < 80H - DISKETTE
26     ; DL > 80H - DISK
27     ; (AH)=02 READ THE DESIRED SECTORS INTO MEMORY
28     ; (AH)=03 WRITE THE DESIRED SECTORS FROM MEMORY
29     ; (AH)=04 VERIFY THE DESIRED SECTORS
30     ; (AH)=05 FORMAT THE DESIRED TRACK
31     ; (AH)=06 FORMAT THE DESIRED TRACK AND SET BAD SECTOR FLAGS
32     ; (AH)=07 FORMAT THE DRIVE STARTING AT THE DESIRED TRACK
33     ; (AH)=08 RETURN THE CURRENT DRIVE PARAMETERS
34
35     ; (AH)=09 INITIALIZE DRIVE PAIR CHARACTERISTICS
36     ; INTERRUPT 41 POINTS TO DATA BLOCK
37     ; (AH)=0A READ LONG
38     ; (AH)=0B WRITE LONG
39     ; NOTE: READ AND WRITE LONG ENCOMPASS 512 + 4 BYTES ECC
40     ; (AH)=0C SEEK
41     ; (AH)=0D ALTERNATE DISK RESET (SEE DL)
42     ; (AH)=0E READ SECTOR BUFFER
43     ; (AH)=0F WRITE SECTOR BUFFER,
44     ; (RECOMMENDED PRACTICE BEFORE FORMATTING)
45     ; (AH)=10 TEST DRIVE READY
46     ; (AH)=11 RECALIBRATE
47     ; (AH)=12 CONTROLLER RAM DIAGNOSTIC
48     ; (AH)=13 DRIVE DIAGNOSTIC
49     ; (AH)=14 CONTROLLER INTERNAL DIAGNOSTIC
50
51     ; REGISTERS USED FOR FIXED DISK OPERATIONS
52
53     ; (DL) - DRIVE NUMBER (80H-87H FOR DISK, VALUE CHECKED)
54     ; (DH) - HEAD NUMBER (0-7 ALLOWED, NOT VALUE CHECKED)
55     ; (CH) - CYLINDER NUMBER (0-1023, NOT VALUE CHECKED)(SEE CL)
56     ; (CL) - SECTOR NUMBER (1-17, NOT VALUE CHECKED)
57
58     ; NOTE: HIGH 2 BITS OF CYLINDER NUMBER ARE PLACED
59     ; IN THE HIGH 2 BITS OF THE CL REGISTER
60     ; (10 BITS TOTAL)
61     ; (AL) - NUMBER OF SECTORS (MAXIMUM POSSIBLE RANGE 1-80H,
62     ; FOR READ/WRITE LONG 1-79H)
63     ; (INTERLEAVE VALUE FOR FORMAT 1-16D)
64     ; (ES:BX) - ADDRESS OF BUFFER FOR READS AND WRITES,
65     ; (NOT REQUIRED FOR VERIFY)
66
67     ; OUTPUT
68     ; AH = STATUS OF CURRENT OPERATION
69     ; STATUS BITS ARE DEFINED IN THE EQUATES BELOW
70     ; CY = 0 SUCCESSFUL OPERATION (AH=0 ON RETURN)
71     ; CY = 1 FAILED OPERATION (AH HAS ERROR REASON)
72
73     ; NOTE: ERROR 11H INDICATES THAT THE DATA READ HAD A RECOVERABLE
74     ; ERROR WHICH WAS CORRECTED BY THE ECC ALGORITHM. THE DATA
75     ; IS PROBABLY GOOD, HOWEVER THE BIOS ROUTINE INDICATES AN
76     ; ERROR TO ALLOW THE CONTROLLING PROGRAM A CHANCE TO DECIDE
77     ; FOR ITSELF. THE ERROR MAY NOT RECUR IF THE DATA IS

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LOC OBJ

LINE SOURCE

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78 ; REWRITTEN. (AL) CONTAINS THE BURST LENGTH.
79 ;
80 ; IF DRIVE PARAMETERS WERE REQUESTED,
81 ;
82 ; DL = NUMBER OF CONSECUTIVE ACKNOWLEDGING DRIVES ATTACHED (0-2)
83 ; (CONTROLLER CARD ZERO TALLY ONLY)
84 ; DH = MAXIMUM USEABLE VALUE FOR HEAD NUMBER
85 ; CH = MAXIMUM USEABLE VALUE FOR CYLINDER NUMBER
86 ; CL = MAXIMUM USEABLE VALUE FOR SECTOR NUMBER
87 ; AND CYLINDER NUMBER HIGH BITS
88 ;
89 ; REGISTERS WILL BE PRESERVED EXCEPT WHEN THEY ARE USED TO RETURN
90 ; INFORMATION.
91 ;
92 ; NOTE: IF AN ERROR IS REPORTED BY THE DISK CODE, THE APPROPRIATE
93 ; ACTION IS TO RESET THE DISK, THEN RETRY THE OPERATION.
94 ;
95 ;-----

```

```

00FF 97 SENSE_FAIL EQU 0FFH ; SENSE OPERATION FAILED
00BB 98 UNDEF_ERR EQU 0BBH ; UNDEFINED ERROR OCCURRED
0080 99 TIME_OUT EQU 80H ; ATTACHMENT FAILED TO RESPOND
0040 100 BAD_SEEK EQU 40H ; SEEK OPERATION FAILED
0020 101 BAD_CHTLR EQU 20H ; CONTROLLER HAS FAILED
0011 102 DATA_CORRECTED EQU 11H ; ECC CORRECTED DATA ERROR
0010 103 BAD_ECC EQU 10H ; BAD ECC ON DISK READ
000B 104 BAD_TRACK EQU 0BH ; BAD TRACK FLAG DETECTED
0009 105 DMA_BOUNDARY EQU 09H ; ATTEMPT TO DMA ACROSS 64K BOUNDARY
0007 106 INIT_FAIL EQU 07H ; DRIVE PARAMETER ACTIVITY FAILED
0005 107 BAD_RESET EQU 05H ; RESET FAILED
0004 108 RECORD_HOT_FND EQU 04H ; REQUESTED SECTOR NOT FOUND
0002 109 BAD_ADDR_MARK EQU 02H ; ADDRESS MARK NOT FOUND
0001 110 BAD_CMD EQU 01H ; BAD COMMAND PASSED TO DISK I/O

```

```

111 ;-----
112 ; INTERRUPT AND STATUS AREAS :
113 ;-----
114 ;-----
115 ;-----

```

```

---- 116 DUMMY SEGMENT AT 0
0034 117 ORG 0DH*4 ; FIXED DISK INTERRUPT VECTOR
0034 118 HDISK_INT LABEL DWORD
004C 119 ORG 13H*4 ; DISK INTERRUPT VECTOR
004C 120 ORG_VECTOR LABEL DWORD
0064 121 ORG 19H*4 ; BOOTSTRAP INTERRUPT VECTOR
0064 122 BOOT_VEC LABEL DWORD
0078 123 ORG 1EH*4 ; DISKETTE PARAMETERS
0078 124 DISKETTE_PARM LABEL DWORD
0100 125 ORG 040H*4 ; NEW DISKETTE INTERRUPT VECTOR
0100 126 DISK_VECTOR LABEL DWORD
0104 127 ORG 041H*4 ; FIXED DISK PARAMETER VECTOR
0104 128 HF_TBL_VEC LABEL DWORD
7C00 129 ORG 7C00H ; BOOTSTRAP LOADER VECTOR
7C00 130 BOOT_LOCN LABEL FAR
---- 131 DUMMY ENDS
132 ;-----

```

```

---- 133 DATA SEGMENT AT 40H
0042 134 ORG 42H
0042 135 CMD_BLOCK LABEL BYTE
0042 (7 ??) 136 HD_ERROR DB 7 DUP(?) ; OVERLAYS DISKETTE STATUS
006C 137 ORG 06CH
006C ??? 138 TIMER_LOW DW ? ; TIMER LOW WORD
0072 139 ORG 72H
0072 ??? 140 RESET_FLAG DW ? ; 1234H IF KEYBOARD RESET UNDERWAY
0074 141 ORG 74H
0074 ?? 142 DISK_STATUS DB ? ; FIXED DISK STATUS BYTE
0075 ?? 143 HF_NUM DB ? ; COUNT OF FIXED DISK DRIVES
0076 ?? 144 CONTROL_BYTE DB ? ; CONTROL BYTE DRIVE OPTIONS
0077 ?? 145 PORT_OFF DB ? ; PORT OFFSET
---- 146 DATA ENDS
147 ;-----

```

```

---- 148 CODE SEGMENT
149 ;-----
150 ;-----
151 ; HARDWARE SPECIFIC VALUES :
152 ; :
153 ; - CONTROLLER I/O PORT :
154 ; > WHEN READ FROM: :

```

LOC OBJ

LINE SOURCE

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155 | HF_PORT+0 - READ DATA (FROM CONTROLLER TO CPU) |
156 | HF_PORT+1 - READ CONTROLLER HARDWARE STATUS |
157 | (CONTROLLER TO CPU) |
158 | HF_PORT+2 - READ CONFIGURATION SWITCHES |
159 | HF_PORT+3 - NOT USED |
160 | > WHEN WRITTEN TO: |
161 | HF_PORT+0 - WRITE DATA (FROM CPU TO CONTROLLER) |
162 | HF_PORT+1 - CONTROLLER RESET |
163 | HF_PORT+2 - GENERATE CONTROLLER SELECT PULSE |
164 | HF_PORT+3 - WRITE PATTERN TO DMA AND INTERRUPT |
165 | MASK REGISTER |
166 | |
167 |-----|
168
0320 169 HF_PORT EQU 0320H | DISK PORT
0008 170 RI_BUSY EQU 00001000B | DISK PORT 1 BUSY BIT
0004 171 RI_BUS EQU 00000100B | COMMAND/DATA BIT
0002 172 RI_IOMODE EQU 00000010B | MODE BIT
0001 173 RI_REQ EQU 00000001B | REQUEST BIT
174
0047 175 DMA_READ EQU 01000111B | CHANNEL 3 (047H)
004B 176 DMA_WRITE EQU 01001011B | CHANNEL 3 (04BH)
0000 177 DMA EQU 0 | DMA ADDRESS
0082 178 DMA_HIGH EQU 082H | PORT FOR HIGH 4 BITS OF DMA
179
0000 180 TST_RDY_CMD EQU 00000000B | CNTLR READY (00H)
0001 181 RECAL_CMD EQU 00000001B | RECAL (01H)
0003 182 SENSE_CMD EQU 00000011B | SENSE (03H)
0004 183 FMTDRV_CMD EQU 00000100B | DRIVE (04H)
0005 184 CHK_TRK_CMD EQU 00000101B | T CHK (05H)
0006 185 FMTTRK_CMD EQU 00000110B | TRACK (06H)
0007 186 FMTBAD_CMD EQU 00000111B | BAD (07H)
0008 187 READ_CMD EQU 00001000B | READ (08H)
000A 188 WRITE_CMD EQU 00001010B | WRITE (0AH)
000B 189 SEEK_CMD EQU 00001011B | SEEK (0BH)
000C 190 INIT_DRV_CMD EQU 00001100B | INIT (0CH)
000D 191 RD_ECC_CMD EQU 00001101B | BURST (0DH)
000E 192 RD_BUFF_CMD EQU 00001110B | BUFFER (0EH)
000F 193 WR_BUFF_CMD EQU 00001111B | BUFFER (0FH)
00E0 194 RAM_DIAG_CMD EQU 11100000B | RAM (E0H)
00E3 195 CHK_DRV_CMD EQU 11100011B | DRV (E3H)
00E4 196 CNTLR_DIAG_CMD EQU 11100100B | CNTLR (E4H)
00E5 197 RD_LONG_CMD EQU 11100101B | RLONG (E5H)
00E6 198 WR_LONG_CMD EQU 11100110B | WLONG (E6H)
199
0020 200 INT_CTL_PORT EQU 20H | 8259 CONTROL PORT
0020 201 EOI EQU 20H | END OF INTERRUPT COMMAND
202
0008 203 MAX_FILE EQU 8
0002 204 S_MAX_FILE EQU 2
205
206 ASSUME CS:CODE
207 ORG OH
0000 55 208 DB 055H | GENERIC BIOS HEADER
0001 AA 209 DB 0AAH
0002 10 210 DB 16D
211
212 |-----|
213 | FIXED DISK I/O SETUP |
214 | |
215 | - ESTABLISH TRANSFER VECTORS FOR THE FIXED DISK |
216 | - PERFORM POWER ON DIAGNOSTICS |
217 | SHOULD AN ERROR OCCUR A "1701" MESSAGE IS DISPLAYED |
218 | |
219 |-----|
220
0003 221 DISK_SETUP PROC FAR
0003 EB1E 222 JMP SHORT L3
0005 35303030303539 223 DB '5000059 (C)COPYRIGHT IBM 1982' | COPYRIGHT NOTICE
20284329434F50
59524947485420
20494240203139
3832
0023 224 L3:
225 ASSUME DS:DUMMY
0023 2BC0 226 SUB AX,AX | ZERO
0025 6E08 227 MOV DS,AX

```

Appendix A

LOC OBJ	LINE	SOURCE
0027 FA	228	CLI
0028 A14C00	229	MOV AX,WORD PTR ORG_VECTOR ; GET DISKETTE VECTOR
002B A30001	230	MOV WORD PTR DISK_VECTOR,AX ; INTO INT 40H
002E A14E00	231	MOV AX,WORD PTR ORG_VECTOR+2
0031 A30201	232	MOV WORD PTR DISK_VECTOR+2,AX
0034 C7064C005602	233	MOV WORD PTR ORG_VECTOR, OFFSET DISK_IO ; HDISK HANDLER
003A 8C0E4E00	234	MOV WORD PTR ORG_VECTOR+2,CS
003E B86007	235	MOV AX, OFFSET HD_INT ; HDISK INTERRUPT
0041 A33400	236	MOV WORD PTR HDISK_INT,AX
0044 8C0E3600	237	MOV WORD PTR HDISK_INT+2,CS
0048 C70664008601	238	MOV WORD PTR BOOT_VEC,OFFSET BOOT_STRAP ; BOOTSTRAP
004E 8C0E6600	239	MOV WORD PTR BOOT_VEC+2,CS
0052 C7060401E703	240	MOV WORD PTR HF_TBL_VEC,OFFSET FD_TBL ; PARAMETER TBL
0056 8C0E0601	241	MOV WORD PTR HF_TBL_VEC+2,CS
005C FB	242	STI
	243	
	244	ASSUME DS:DATA
005D B84000	245	MOV AX,DATA ; ESTABLISH SEGMENT
0060 8ED8	246	MOV DS,AX
0062 C606740000	247	MOV DISK_STATUS,0 ; RESET THE STATUS INDICATOR
0067 C606750000	248	MOV HF_NUM,0 ; ZERO COUNT OF DRIVES
006C C606430000	249	MOV CHD_BLOCK+1,0 ; DRIVE ZERO, SET VALUE IN BLOCK
0071 C606770000	250	MOV PORT_OFF,0 ; ZERO CARD OFFSET
	251	
0076 B92500	252	MOV CX,25H ; RETRY COUNT
0079	253	L4:
0079 E8F200	254	CALL HD_RESET_I ; RESET CONTROLLER
007C 7305	255	JHC L7
007E E2F9	256	LOOP L4 ; TRY RESET AGAIN
0080 E9BF00	257	JMP ERROR_EX
	258	L7:
0083	258	
0083 B90100	259	MOV CX,1
0086 BA8000	260	MOV DX,80H
	261	
0089 B80012	262	MOV AX,1200H ; CONTROLLER DIAGNOSTICS
008C CD13	263	INT 13H
008E 7303	264	JNC P7
0090 E9AF00	265	JMP ERROR_EX
	266	P7:
0093	266	
0093 B80014	267	MOV AX,1400H ; CONTROLLER DIAGNOSTICS
0096 CD13	268	INT 13H
0098 7303	269	JNC P9
009A E9A500	270	JMP ERROR_EX
	271	P9:
009D	271	
009D C7066C000000	272	MOV TIMER_LOW,0 ; ZERO TIMER
00A3 A17200	273	MOV AX,RESET_FLAG
00A6 3D3412	274	CMPI AX,1234H ; KEYBOARD RESET
00A9 7506	275	JNE P8
00AB C7066C009A01	276	MOV TIMER_LOW,410D ; SKIP WAIT ON RESET
	277	P8:
00B1	277	
00B1 E421	278	IN AL,021H ; TIMER
00B3 24FE	279	AND AL,0FEH ; ENABLE TIMER
00B5 E621	280	OUT 021H,AL ; START TIMER
	281	P4:
00B7	281	
00B7 E8B400	282	CALL HD_RESET_I ; RESET CONTROLLER
00BA 7207	283	JC P10
00BC B80010	284	MOV AX,1000H ; READY
00BF CD13	285	INT 13H
00C1 730B	286	JNC P2
	287	P10:
00C3	287	
00C3 A16C00	288	MOV AX,TIMER_LOW
00C6 3DBE01	289	CMPI AX,446D ; 25 SECONDS
00C9 72EC	290	JB P4
00CB EB7590	291	JMP ERROR_EX
	292	P2:
00CE	292	
00CE B90100	293	MOV CX,1
00D1 BA0000	294	MOV DX,80H
	295	
00D4 B80011	296	MOV AX,1100H ; RECALIBRATE
00D7 CD13	297	INT 13H
00D9 7267	298	JC ERROR_EX
	299	
00DB B80009	300	MOV AX,0900H ; SET DRIVE PARAMETERS
00DE CD13	301	INT 13H
00E0 7260	302	JC ERROR_EX
	303	
00E2 B800C8	304	MOV AX,0C800H ; DMA TO BUFFER

LOC OBJ	LINE	SOURCE		
00E5 BEC0	305	MOV	ES,AX	; SET SEGMENT
00E7 2BDB	306	SUB	BX,BX	
00E9 B8000F	307	MOV	AX,0F00H	; WRITE SECTOR BUFFER
00EC CD13	308	INT	13H	
00EE 7252	309	JC	ERROR_EX	
	310			
00F0 FE067500	311	INC	HF_NUM	; DRIVE ZERO RESPONDED
	312			
00F4 BA1302	313	MOV	DX,213H	; EXPANSION BOX
00F7 B000	314	MOV	AL,0	
00F9 EE	315	OUT	DX,AL	; TURN BOX OFF
00FA BA2103	316	MOV	DX,321H	; TEST IF CONTROLLER
00FD EC	317	IN	AL,DX	; ... IS IN THE SYSTEM UNIT
00FE 240F	318	AND	AL,0FH	
0100 3C0F	319	CMF	AL,0FH	
0102 7406	320	JE	BOX_ON	
0104 C7066C00A401	321	MOV	TIMER_LOW,4200	; CONTROLLER IS IN SYSTEM UNIT
010A	322	BOX_ON:		
010A BA1302	323	MOV	DX,213H	; EXPANSION BOX
010D B0FF	324	MOV	AL,0FFH	
010F EE	325	OUT	DX,AL	; TURN BOX ON
	326			
0110 B90100	327	MOV	CX,1	; ATTEMPT NEXT DRIVES
0113 BA8100	328	MOV	DX,081H	
0116	329	P3:		
0116 2BC0	330	SUB	AX,AX	; RESET
0118 CD13	331	INT	13H	
011A 7240	332	JC	POD_DONE	
011C B80011	333	MOV	AX,01100H	; RECAL
011F CD13	334	INT	13H	
0121 730B	335	JNC	P5	
0123 A16C00	336	MOV	AX,TIMER_LOW	
0126 3DBE01	337	CMF	AX,446D	; 25 SECONDS
0129 72EB	338	JB	P3	
012B EB2F90	339	JMP	POD_DONE	
012E	340	P5:		
012E B80009	341	MOV	AX,0900H	; INITIALIZE CHARACTERISTICS
0131 CD13	342	INT	13H	
0133 7227	343	JC	POD_DONE	
0135 FE067500	344	INC	HF_NUM	; TALLY ANOTHER DRIVE
0139 81FA8100	345	CMF	DX,(80H + S_MAX_FILE - 1)	
013D 731D	346	JAE	POD_DONE	
013F 42	347	INC	DX	
0140 EBD4	348	JMP	P3	
	349			
	350	;	POD ERROR	
	351			
0142	352	ERROR_EX:		
0142 B0DF00	353	MOV	BP,0FH	; POD ERROR FLAG
0145 2BC0	354	SUB	AX,AX	
0147 8BF0	355	MOV	SI,AX	
0149 B9060090	356	MOV	CX,F17L	; MESSAGE CHARACTER COUNT
014D B700	357	MOV	BI,0	; PAGE ZERO
014F	358	OUT_CH:		
014F 2E8A046B01	359	MOV	AL,CS:F171SI1	; GET BYTE
0154 B40E	360	MOV	AH,14D	; VIDEO OUT
0156 CD10	361	INT	10H	; DISPLAY CHARACTER
0158 46	362	INC	SI	; NEXT CHAR
0159 E2F4	363	LOOP	OUT_CH	; DO MORE
015B F9	364	STC		
015C	365	POD_DONE:		
015C FA	366	CLI		
015D E421	367	IN	AL,021H	; BE SURE TIMER IS DISABLED
015F 0C01	368	OR	AL,01H	
0161 E621	369	OUT	021H,AL	
0163 FB	370	STI		
0164 E8A500	371	CALL	DSBL	
0167 CB	372	RET		
	373			
0168 31373031	374	F17	DB	'1701',0DH,0AH

```

LOC OBJ          LINE   SOURCE
016C 00
016D 0A
0006
375 F17L EQU $-F17
376
016E 377 HD_RESET_1 PROC NEAR
016E 51 378 PUSH CX ; SAVE REGISTER
016F 52 379 PUSH DX
0170 F8 380 CLC ; CLEAR CARRY
0171 B90001 381 MOV CX,0100H ; RETRY COUNT
0174 382 L6:
0174 E80706 383 CALL PORT_1
0177 EE 384 OUT DX,AL ; RESET CARD
0178 E00306 385 CALL PORT_1
017B EC 386 IN AL,DX ; CHECK STATUS
017C 2402 387 AND AL,2 ; ERROR BIT
017E 7403 388 JZ R3
0180 E2F2 389 LOOP L6
0182 F9 390 STC
0183 391 R3:
0183 5A 392 POP DX ; RESTORE REGISTER
0184 59 393 POP CX
0185 C3 394 RET
395 HD_RESET_1 ENDP
396
397 DISK_SETUP ENDP
398
399 |----- INT 19 -----
400 |
401 | INTERRUPT 19 BOOT STRAP LOADER
402 |
403 | - THE FIXED DISK BIOS REPLACES THE INTERRUPT 19
404 | BOOT STRAP VECTOR WITH A POINTER TO THIS BOOT ROUTINE
405 | - RESET THE DEFAULT DISK AND DISKETTE PARAMETER VECTORS
406 | - THE BOOT BLOCK TO BE READ IN WILL BE ATTEMPTED FROM
407 | CYLINDER 0 SECTOR 1 OF THE DEVICE.
408 | - THE BOOTSTRAP SEQUENCE IS:
409 | > ATTEMPT TO LOAD FROM THE DISKETTE INTO THE BOOT
410 | LOCATION (0000:7C00) AND TRANSFER CONTROL THERE
411 | > IF THE DISKETTE FAILS THE FIXED DISK IS TRIED FOR A
412 | VALID BOOTSTRAP BLOCK. A VALID BOOT BLOCK ON THE
413 | FIXED DISK CONSISTS OF THE BYTES 055H 0AAH AS THE
414 | LAST TWO BYTES OF THE BLOCK
415 | > IF THE ABOVE FAILS CONTROL IS PASSED TO RESIDENT BASIC
416 |
417 |-----
418
0186 419 BOOT_STRAP:
419
0186 2BC0 420 ASSUME DS:DUMMY,ES:DUMMY
0186 8ED8 421 SUB AX,AX
422 MOV DS,AX ; ESTABLISH SEGMENT
423
424 |----- RESET PARAMETER VECTORS
425
018A FA 426 CLI
018B C7060401E703 427 MOV WORD PTR HF_TBL_VEC, OFFSET FD_TBL
0191 8C0E0601 428 MOV WORD PTR HF_TBL_VEC+2, CS
0195 C70678000102 429 MOV WORD PTR DISKETTE_PARM, OFFSET DISKETTE_TBL
0198 8C0E7A00 430 MOV WORD PTR DISKETTE_PARM+2, CS
019F FB 431 STI
432
433 |----- ATTEMPT BOOTSTRAP FROM DISKETTE
434
01A0 B90300 435 MOV CX,3 ; SET RETRY COUNT
01A3 436 H1: ; IPL_SYSTEM
01A3 51 437 PUSH CX ; SAVE RETRY COUNT
01A4 2BD2 438 SUB DX,DX ; DRIVE ZERO
01A6 2BC0 439 SUB AX,AX ; RESET THE DISKETTE
01A8 CD13 440 INT 13H ; FILE IO CALL
01AA 720F 441 JC H2 ; IF ERROR, TRY AGAIN
01AC B80102 442 MOV AX,0201H ; READ IN THE SINGLE SECTOR
443
01AF 2BD2 444 SUB DX,DX
01B1 8EC2 445 MOV ES,DX ; ESTABLISH SEGMENT
01B3 B8070C 446 MOV BX,OFFSET BOOT_LOCN
447
01B6 B90100 448 MOV CX,1 ; SECTOR 1, TRACK 0
01B9 CD13 449 INT 13H ; FILE IO CALL

```

LOC	OBJ	LINE	SOURCE	
01B8	59	450	H2: POP CX	; RECOVER RETRY COUNT
01BC	730A	451	JNC H4	; CF SET BY UNSUCCESSFUL READ
01BE	80FC80	452	CHP AH,80H	; IF TIME OUT, NO RETRY
01C1	740A	453	JZ H5	; TRY FIXED DISK
01C3	E2DE	454	LOOP H1	; DO IT FOR RETRY TIMES
01C5	EB0690	455	JMP H5	; UNABLE TO IPL FROM THE DISKETTE
01C8		456	H4: BOOT_LOCN	; IPL WAS SUCCESSFUL
01C8	EA007C0000	457	JMP BOOT_LOCN	
		458		
		459	;----- ATTEMPT BOOTSTRAP FROM FIXED DISK	
		460		
01CD		461	H5:	
01CD	2BC0	462	SUB AX,AX	; RESET DISKETTE
01CF	2B02	463	SUB DX,DX	
01D1	CD13	464	IHT 13H	
01D3	B90300	465	MOV CX,3	; SET RETRY COUNT
01D6		466	H6:	; IPL_SYSTEM
01D6	51	467	PUSH CX	; SAVE RETRY COUNT
01D7	BA8000	468	MOV DX,0080H	; FIXED DISK ZERO
01DA	2BC0	469	SUB AX,AX	; RESET THE FIXED DISK
01DC	CD13	470	IHT 13H	; FILE IO CALL
01DE	7212	471	JC H7	; IF ERROR, TRY AGAIN
01E0	B80102	472	MOV AX,0201H	; READ IN THE SINGLE SECTOR
01E3	2B0B	473	SUB BX,BX	
01E5	8EC3	474	MOV ES,BX	
01E7	B8007C	475	MOV BX,OFFSET BOOT_LOCN	; TO THE BOOT LOCATION
01EA	BA8000	476	MOV DX,80H	; DRIVE NUMBER
01ED	B90100	477	MOV CX,1	; SECTOR 1, TRACK 0
01F0	CD13	478	IHT 13H	; FILE IO CALL
01F2	59	479	H7: POP CX	; RECOVER RETRY COUNT
01F3	7208	480	JC H8	
01F5	A1FE7D	481	MOV AX,WORD PTR BOOT_LOCN+510D	
01F8	3D55AA	482	CHP AX,0AA55H	; TEST FOR GENERIC BOOT BLOCK
01FB	74CB	483	JZ H4	
01FD		484	H8: LOOP H6	; DO IT FOR RETRY TIMES
01FD	E2D7	485	LOOP H6	; DO IT FOR RETRY TIMES
		486		
		487	;----- UNABLE TO IPL FROM THE DISKETTE OR FIXED DISK	
		488		
01FF	CD18	489	INT 18H	; RESIDENT BASIC
		490		
0201		491	DISKETTE_TBL:	
		492		
0201	CF	493	DB 11001111B	; SRT=C, HD UNLOAD=0F - 1ST SPEC BYTE
0202	02	494	DB 2	; HD LOAD=1, MODE=DMA - 2ND SPEC BYTE
0203	25	495	DB 25H	; WAIT AFTER OPN TIL MOTOR OFF
0204	02	496	DB 2	; 512 BYTES PER SECTOR
0205	08	497	DB 8	; EOT (LAST SECTOR ON TRACK)
0206	2A	498	DB 02AH	; GAP LENGTH
0207	FF	499	DB 0FFH	; DTL
0208	50	500	DB 050H	; GAP LENGTH FOR FORMAT
0209	F6	501	DB 0F6H	; FILL BYTE FOR FORMAT
020A	19	502	DB 25	; HEAD SETTLE TIME (MILLISECONDS)
020B	04	503	DB 4	; MOTOR START TIME (1/8 SECOND)
		504		
		505	;----- MAKE SURE THAT ALL HOUSEKEEPING IS DONE BEFORE EXIT	
		506		
020C		507	DSBL PROC NEAR	
		508	ASSUME DS:DATA	
020C	1E	509	PUSH DS	; SAVE SEGMENT
020D	B84000	510	MOV AX,DATA	
0210	8ED8	511	MOV DS,AX	
		512		
0212	8A267700	513	MOV AH,PORT_OFF	
0216	50	514	PUSH AX	; SAVE OFFSET
		515		
0217	C606770000	516	MOV PORT_OFF,0H	
021C	E86905	517	CALL PORT_3	
021F	2AC0	518	SUB AL,AL	
0221	EE	519	OUT DX,AL	; RESET INT/DMA MASK
0222	C606770004	520	MOV PORT_OFF,4H	
0227	E05E05	521	CALL PORT_3	
022A	2AC0	522	SUB AL,AL	
022C	EE	523	OUT DX,AL	; RESET INT/DMA MASK
022D	C606770008	524	MOV PORT_OFF,8H	
0232	E85305	525	CALL PORT_3	
0235	2AC0	526	SUB AL,AL	

LOC OBJ	LINE	SOURCE	
0237 EE	527	OUT DX,AL	; RESET INT/DMA MASK
0238 C60677000C	528	MOV PORT_OFF,0CH	
0239 E84805	529	CALL PORT_3	
0240 2AC0	530	SUB AL,AL	
0242 EE	531	OUT DX,AL	; RESET INT/DMA MASK
0243 B007	532	MOV DH,07H	
0245 E60A	533	OUT DMA+10,AL	; SET DMA MODE TO DISABLE
0247 FA	534	CLI	; DISABLE INTERRUPTS
0248 E421	535	IN AL,021H	
024A 0C20	536	OR AL,020H	
024C E621	537	OUT 021H,AL	; DISABLE INTERRUPT 5
024E FB	538	STI	; ENABLE INTERRUPTS
024F 58	539	POP AX	; RESTORE OFFSET
0250 88267700	540	MOV PORT_OFF,AH	
0254 1F	541	POP DS	; RESTORE SEGMENT
0255 C3	542	RET	
	543	DSBL EHDP	
	544		
	545	-----	
	546	FIXED DISK BIOS ENTRY POINT	
	547	-----	
	548		
0256	549	DISK_IO PROC FAR	
	550	ASSUME DS:NOTHING,ES:NOTHING	
0256 80FA80	551	CHP DL,80H	; TEST FOR FIXED DISK DRIVE
0259 7305	552	JAE HARD_DISK	; YES, HANDLE HERE
025B C040	553	INT 40H	; DISKETTE HANDLER
025D	554	RET_2:	
025D CA0200	555	RET 2	; BACK TO CALLER
0260	556	HARD_DISK:	
	557	ASSUME DS:DATA	
0260 FB	558	STI	; ENABLE INTERRUPTS
0261 0AE4	559	OR AH,AH	
0263 75D9	560	JNZ A3	
0265 C040	561	INT 40H	; RESET NEC WHEN AH=0
0267 2AE4	562	SUB AH,AH	
0269 80FA81	563	CHP DL,(80H + S_MAX_FILE - 1)	
026C 77EF	564	JA RET_2	
026E	565	A3:	
026E 80FC08	566	CHP AH,08	; GET PARAMETERS IS A SPECIAL CASE
0271 7503	567	JNZ A2	
0273 E91A01	568	JMP GET_PARM_N	
0276	569	A2:	
0276 53	570	PUSH BX	; SAVE REGISTERS DURING OPERATION
0277 51	571	PUSH CX	
0278 52	572	PUSH DX	
0279 1E	573	PUSH DS	
027A 06	574	PUSH ES	
027B 56	575	PUSH SI	
027C 57	576	PUSH DI	
	577		
027D E86A00	578	CALL DISK_IO_CONT	; PERFORM THE OPERATION
	579		
0280 50	580	PUSH AX	
0281 E888FF	581	CALL DSBL	; BE SURE DISABLES OCCURRED
0284 B84000	582	MOV AX,DATA	
0287 B8D8	583	MOV DS,AX	; ESTABLISH SEGMENT
0289 58	584	POP AX	
028A 8A267400	585	MOV AH,DISK_STATUS	; GET STATUS FROM OPERATION
028E 80FC01	586	CHP AH,1	; SET THE CARRY FLAG TO INDICATE
0291 F5	587	CMC	; SUCCESS OR FAILURE
0292 5F	588	POP DI	; RESTORE REGISTERS
0293 5E	589	POP SI	
0294 07	590	POP ES	
0295 1F	591	POP DS	
0296 5A	592	POP DX	
0297 59	593	POP CX	
0298 58	594	POP BX	
0299 CA0200	595	RET 2	; THROW AWAY SAVED FLAGS
	596	DISK_IO EHDP	
	597		
029C	598	M1 LABEL WORD	; FUNCTION TRANSFER TABLE
029C 3803	599	DW DISK_RESET	; 000H
029E 4003	600	DW RETURN_STATUS	; 001H
02A0 5603	601	DW DISK_READ	; 002H
02A2 6003	602	DW DISK_WRITE	; 003H
02A4 6A03	603	DW DISK_VERIFY	; 004H

LOC OBJ	LINE	SOURCE	
02A6 7203	604	DW	FHT_TRK ; 005H
02A8 7903	605	DW	FHT_BAD ; 006H
02AA 8003	606	DW	FHT_DRV ; 007H
02AC 3003	607	DW	BAD_COMMAND ; 008H
02AE 2704	608	DW	INIT_DRV ; 009H
02B0 CF04	609	DW	RD_LONG ; 00AH
02B2 D004	610	DW	WR_LONG ; 00BH
02B4 F204	611	DW	DISK_SEEK ; 00CH
02B6 3803	612	DW	DISK_RESET ; 00DH
02B8 F904	613	DW	RD_BUFF ; 00EH
02BA 0705	614	DW	WR_BUFF ; 00FH
02BC 1505	615	DW	TST_RDY ; 010H
02BE 1C05	616	DW	HDISK_RECAL ; 011H
02C0 2305	617	DW	RAH_DIAG ; 012H
02C2 2A05	618	DW	CHK_DRV ; 013H
02C4 3105	619	DW	CHTLR_DIAG ; 014H
002A	620	MIL EQU	4-HL
	621		
02C6	622	SETUP_A PROC	NEAR
	623		
02C6 C066740000	624	MOV	DISK_STATUS,0 ; RESET THE STATUS INDICATOR
02CB 51	625	PUSH	CX ; SAVE CX
	626		
	627		1----- CALCULATE THE PORT OFFSET
	628		
02CC 8AEA	629	MOV	CH,DL ; SAVE DL
02CE 80CA01	630	OR	DL,1
02D1 FECA	631	DEC	DL
02D3 D0E2	632	SHL	DL,1 ; GENERATE OFFSET
02D5 88167700	633	MOV	PORT_OFF,DL ; STORE OFFSET
02D9 8AD5	634	MOV	DL,CH ; RESTORE DL
02DB 80E201	635	AND	DL,1
	636		
02DE B105	637	MOV	CL,5 ; SHIFT COUNT
02E0 D2E2	638	SHL	DL,CL ; DRIVE NUMBER (0,1)
02E2 0AD6	639	OR	DL,DH ; HEAD NUMBER
02E4 88164300	640	MOV	CHD_BLOCK+1,DL
02E8 59	641	POP	CX
02E9 C3	642	RET	
	643	SETUP_A ENDP	
	644		
02EA	645	DISK_IO_COUNT PROC	NEAR
02EA 50	646	PUSH	AX
02EB B94000	647	MOV	AX,DATA
02EE 8ED8	648	MOV	DS,AX ; ESTABLISH SEGMENT
02F0 58	649	POP	AX
02F1 80FC01	650	CMF	AH,01H ; RETURN STATUS
02F4 7503	651	JNZ	A4
02F6 EB5590	652	JMP	RETURN_STATUS
02F9	653	A4:	
02F9 80EA80	654	SUB	DL,80H ; CONVERT DRIVE NUMBER TO 0 BASED RANGE
02FC 80FA08	655	CMF	DL,MAX_FILE ; LEGAL DRIVE TEST
02FF 732F	656	JAE	BAD_COMMAND
	657		
0301 E8C2FF	658	CALL	SETUP_A
	659		
	660		1----- SET UP COMMAND BLOCK
	661		
0304 FEC9	662	DEC	CL ; SECTORS 0-16 FOR CONTROLLER
0306 C066420000	663	MOV	CHD_BLOCK+0,0
030B 880E4400	664	MOV	CHD_BLOCK+2,CL ; SECTOR AND HIGH 2 BITS CYLINDER
030F 882E4500	665	MOV	CHD_BLOCK+3,CH ; CYLINDER
0313 A24600	666	MOV	CHD_BLOCK+4,AL ; INTERLEAVE / BLOCK COUNT
0316 A07600	667	MOV	AL,CONTROL_BYTE ; CONTROL BYTE (STEP OPTION)
0319 A24700	668	MOV	CHD_BLOCK+5,AL
031C 50	669	PUSH	AX ; SAVE AX
031D 8AC4	670	MOV	AL,AH ; GET INTO LOW BYTE
031F 32E4	671	XOR	AH,AH ; ZERO HIGH BYTE
0321 D1E0	672	SAL	AX,1 ; *2 FOR TABLE LOOKUP
0323 8BF0	673	MOV	SI,AX ; PUT INTO SI FOR BRANCH
0325 3D2A00	674	CMF	AX,H1L ; TEST WITHIN RANGE
0328 58	675	POP	AX ; RESTORE AX
0329 7305	676	JNB	BAD_COMMAND
032B 2EFFA49C02	677	JMP	WORD PTR CS:[SI + OFFSET M1]
0330	678	BAD_COMMAND:	
0330 C066740001	679	MOV	DISK_STATUS,BAD_CHD ; COMMAND ERROR
0335 B000	680	MOV	AL,0

```

LOC OBJ          LINE  SOURCE
0337 C3          681      RET
                  682      DISK_IO_CONT  ENDP
                  683
                  684      ;-----
                  685      ;   RESET THE DISK SYSTEM  (AH = 000H)   :
                  686      ;-----
                  687
0338             688      DISK_RESET  PROC  NEAR
0338 E84304       689          CALL  PORT_1          ; RESET PORT
0338 EE          690          OUT   DX,AL          ; ISSUE RESET
0338 E83F04       691          CALL  PORT_1          ; CONTROLLER HARDWARE STATUS
0338 EC          692          IN   AL,DX          ; GET STATUS
0340 2402       693          AND  AL,2          ; ERROR BIT
0342 7406       694          JZ   DR1
0344 C606740005  695          MOV   DISK_STATUS,BAD_RESET
0349 C3          696          RET
034A             697      DR1:
034A E9DA00       698          JHP  INIT_DRV          ; SET THE DRIVE PARAMETERS
                  699      DISK_RESET  ENDP
                  700
                  701      ;-----
                  702      ;   DISK STATUS ROUTINE  (AH = 001H)   :
                  703      ;-----
                  704
0340             705      RETURN_STATUS  PROC  NEAR
0340 A07400       706          MOV   AL,DISK_STATUS    ; OBTAIN PREVIOUS STATUS
0350 C606740000  707          MOV   DISK_STATUS,0      ; RESET STATUS
0355 C3          708          RET
                  709      RETURN_STATUS  ENDP
                  710
                  711      ;-----
                  712      ;   DISK READ ROUTINE  (AH = 002H)   :
                  713      ;-----
                  714
0356             715      DISK_READ  PROC  NEAR
0356 B047         716          MOV   AL,DMA_READ          ; MODE BYTE FOR DMA READ
0358 C606420008  717          MOV   CHD_BLOCK+0,READ_CMD
0350 E9E501       718          JHP  DMA_OPN
                  719      DISK_READ  ENDP
                  720
                  721      ;-----
                  722      ;   DISK WRITE ROUTINE  (AH = 003H)   :
                  723      ;-----
                  724
0360             725      DISK_WRITE  PROC  NEAR
0360 B04B         726          MOV   AL,DMA_WRITE        ; MODE BYTE FOR DMA WRITE
0362 C60642000A  727          MOV   CHD_BLOCK+0,WRITE_CMD
0367 E9DB01       728          JHP  DMA_OPN
                  729      DISK_WRITE  ENDP
                  730
                  731      ;-----
                  732      ;   DISK VERIFY  (AH = 004H)   :
                  733      ;-----
                  734
036A             735      DISK_VERF  PROC  NEAR
036A C606420005  736          MOV   CHD_BLOCK+0,CHK_TRK_CMD
036F E9C401       737          JHP  NDMA_OPN
                  738      DISK_VERF  ENDP
                  739
                  740      ;-----
                  741      ;   FORMATTING  (AH = 005H 006H 007H)   :
                  742      ;-----
                  743
0372             744      FMT_TRK  PROC  NEAR          ; FORMAT TRACK  (AH = 005H)
0372 C606420006  745          MOV   CHD_BLOCK,FMTTRK_CMD
0377 EB0C         746          JHP  SHORT  FMT_CONT
                  747      FMT_TRK  ENDP
                  748
0379             749      FMT_BAD  PROC  NEAR          ; FORMAT BAD TRACK  (AH = 006H)
0379 C61420007    750          MOV   CHD_BLOCK,FMTBAD_CMD
037E EB05         751          JHP  SHORT  FMT_CONT
                  752      FMT_BAD  ENDP
                  753
0380             754      FMT_DRV  PROC  NEAR          ; FORMAT DRIVE  (AH = 007H)
0380 C606420004    755          MOV   CHD_BLOCK,FMTDRV_CMD
                  756      FMT_DRV  ENDP
                  757

```



LOC OBJ

LINE SOURCE

```

836 |           BITS 5-3 ZERO |
837 |           BITS 2-0 DRIVE OPTION |
838 | (1 BYTE) - STANDARD TIME OUT VALUE (SEE BELOW) |
839 | (1 BYTE) - TIME OUT VALUE FOR FORMAT DRIVE |
840 | (1 BYTE) - TIME OUT VALUE FOR CHECK DRIVE |
841 | (4 BYTES) |
842 | - RESERVED FOR FUTURE USE |
843 | |
844 | - TO DYNAMICALLY DEFINE A SET OF PARAMETERS |
845 | BUILD A TABLE OF VALUES AND PLACE THE |
846 | CORRESPONDING VECTOR INTO INTERRUPT 41. |
847 | |
848 | NOTE: |
849 | THE DEFAULT TABLE IS VECTORED IN FOR |
850 | AN INTERRUPT 19H (BOOTSTRAP) |
851 | |
852 | |
853 | ON THE CARD SWITCH SETTINGS |
854 | |
855 |           DRIVE 0   DRIVE 1 |
856 |           ----- |
857 | ON      :           /           : |
858 |         : -1-  -2- / -3-  -4- : |
859 | OFF     :           /           : |
860 |           ----- |
861 | |
862 | |
863 | TRANSLATION TABLE |
864 | |
865 | 1/3 : 2/4 : TABLE ENTRY |
866 | ----- |
867 | ON  : ON  : 0 |
868 | ON  : OFF : 1 |
869 | OFF : ON  : 2 |
870 | OFF : OFF : 3 |
871 | |
872 | ----- |
873 | |

```

03E7

FD\_TBL:

03E7 874 |----- DRIVE TYPE 00

```

875 |
876 |
877 |
878 | DW 0306D
879 | DB 02D
880 | DW 0306D
881 | DW 0000D
882 | DB 0BH
883 | DB 0D1H
884 | DB 0CH | STANDARD
885 | DB 0B4H | FORMAT DRIVE
886 | DB 028H | CHECK DRIVE
887 | DB 0,0,0,0
888 |

```

889 |----- DRIVE TYPE 01

```

890 |
891 | DW 0375D
892 | DB 06D
893 | DW 0375D
894 | DW 0000D
895 | DB 0BH
896 | DB 05H
897 | DB 0CH | STANDARD
898 | DB 0B4H | FORMAT DRIVE
899 | DB 028H | CHECK DRIVE
900 | DB 0,0,0,0
901 |

```

902 |----- DRIVE TYPE 02

```

903 |
904 | DW 0306D
905 | DB 06D
906 | DW 0128D
907 | DW 0256D
908 | DB 0BH
909 | DB 05H
910 | DB 0CH | STANDARD
911 | DB 0B4H | FORMAT DRIVE

```

LOC OBJ	LINE	SOURCE		
0412 28	912	DB	028H	; CHECK DRIVE
0413 00000000	913	DB	0,0,0,0	
	914			
	915	;----- DRIVE TYPE 03		
	916			
0417 3201	917	DW	0306D	
0419 04	918	DB	040	
041A 3201	919	DW	0306D	
041C 0000	920	DW	0000D	
041E 0B	921	DB	0B8H	
041F 05	922	DB	05H	
0420 0C	923	DB	0CH	; STANDARD
0421 B4	924	DB	0B4H	; FORMAT DRIVE
0422 28	925	DB	028H	; CHECK DRIVE
0423 00000000	926	DB	0,0,0,0	
	927			
0427	928	INIT_DRV	PROC NEAR	
	929			
	930	;----- DO DRIVE ZERO		
	931			
0427 C60642000C	932	MOV	CHD_BLOCK+0,INIT_DRV_CMD	
042C C60643000D	933	MOV	CHD_BLOCK+1,0	
0431 E81000	934	CALL	INIT_DRV_R	
0434 720D	935	JC	INIT_DRV_OUT	
	936			
	937	;----- DO DRIVE ONE		
	938			
0436 C60642000C	939	MOV	CHD_BLOCK+0,INIT_DRV_CMD	
043B C60643002D	940	MOV	CHD_BLOCK+1,00100000B	
0440 E80100	941	CALL	INIT_DRV_R	
0443	942	INIT_DRV_OUT:		
0443 C3	943	RET		
	944	INIT_DRV	ENDP	
	945			
0444	946	INIT_DRV_R	PROC NEAR	
	947	ASSUME	ES:CODE	
0444 2AC0	948	SUB	AL,AL	
0446 E81901	949	CALL	COMMAND	; ISSUE THE COMMAND
0449 7301	950	JNC	B1	
044B C3	951	RET		
044C	952	B1:		
044C 1E	953	PUSH	DS	; SAVE SEGMENT
	954	ASSUME	DS:DUMMY	
044D 2BC0	955	SUB	AX,AX	
044F 8ED8	956	MOV	DS,AX	; ESTABLISH SEGMENT
0451 C41E0401	957	LES	BX,HF_TBL_VEC	
0455 1F	958	POP	DS	; RESTORE SEGMENT
	959	ASSUME	DS:DATA	
0456 E83403	960	CALL	SH2_OFFS	
0459 7257	961	JC	B3	
045B 0308	962	ADD	BX,AX	
	963			
	964	;----- SEND DRIVE PARAMETERS MOST SIGNIFICANT BYTE FIRST		
	965			
045D BF0100	966	MOV	DI,1	
0460 E85F00	967	CALL	INIT_DRV_S	
0463 724D	968	JC	B3	
	969			
0465 BF0000	970	MOV	DI,0	
0460 E85700	971	CALL	INIT_DRV_S	
046B 7245	972	JC	B3	
	973			
046D BF0200	974	MOV	DI,2	
0470 E84F00	975	CALL	INIT_DRV_S	
0473 723D	976	JC	B3	
	977			
0475 BF0400	978	MOV	DI,4	
0470 E84700	979	CALL	INIT_DRV_S	
047B 7235	980	JC	B3	
	981			
047D BF0300	982	MOV	DI,3	
0480 E83F00	983	CALL	INIT_DRV_S	
0483 722D	984	JC	B3	
	985			
0485 BF0600	986	MOV	DI,6	
0488 E83700	987	CALL	INIT_DRV_S	
048B 7225	988	JC	B3	

LOC OBJ	LINE	SOURCE	
	989		
0480 BF0500	990	MOV	DI,5
0490 E82F00	991	CALL	INIT_DRV_S
0493 721D	992	JC	B3
	993		
0495 BF0700	994	MOV	DI,7
0498 E82700	995	CALL	INIT_DRV_S
049B 7215	996	JC	B3
	997		
049D BF0800	998	MOV	DI,8 ; DRIVE STEP OPTION
04A0 268A01	999	MOV	AL,ES:[BX + DI]
04A3 A27600	1000	MOV	CONTROL_BYTE,AL
	1001		
04A6 2BC9	1002	SUB	CX,CX
04A8	1003	B5:	
04A8 E8D302	1004	CALL	PORT_1
04AB EC	1005	IN	AL,DX
04AC A802	1006	TEST	AL,R1_IOMODE ; STATUS INPUT MODE
04AE 7509	1007	JNZ	B6
04B0 E2F6	1008	LOOP	B5
04B2	1009	B3:	
04B2 C606740007	1010	MOV	DISK_STATUS,INIT_FAIL ; OPERATION FAILED
04B7 F9	1011	STC	
04B8 C3	1012	RET	
	1013		
04B9	1014	B6:	
04B9 E8B502	1015	CALL	PORT_0
04BC EC	1016	IN	AL,DX
04BD 2402	1017	AND	AL,2 ; MASK ERROR BIT
04BF 75F1	1018	JNZ	B3
04C1 C3	1019	RET	
	1020	ASSUME	ES:NOTHING
	1021	INIT_DRV_R	ENDP
	1022		
	1023		;----- SEND THE BYTE OUT TO THE CONTROLLER
	1024		
04C2	1025	INIT_DRV_S	PROC NEAR
04C2 E8C501	1026	CALL	HD_WAIT_REG
04C5 7207	1027	JC	D1
04C7 E8A702	1028	CALL	PORT_0
04CA 268A01	1029	MOV	AL,ES:[BX + DI]
04CD EE	1030	OUT	DX,AL
04CE	1031	D1:	
04CE C3	1032	RET	
	1033	INIT_DRV_S	ENDP
	1034		
	1035		};-----
	1036	; READ LONG (AH = 0AH)	;
	1037	};-----	
	1038		
04CF	1039	RD_LONG	PROC NEAR
04CF E81900	1040	CALL	CHK_LONG
04D2 7268	1041	JC	68
04D4 C6064200E5	1042	MOV	CMD_BLOCK+0,RD_LONG_CMD
04D9 B047	1043	MOV	AL,DMA_READ
04DB EB68	1044	JMP	SHORT DMA_OPN
	1045	RD_LONG	ENDP
	1046		
	1047		};-----
	1048	; WRITE LONG (AH = 0BH)	;
	1049	};-----	
	1050		
04DD	1051	WR_LONG	PROC NEAR
04DD E80B00	1052	CALL	CHK_LONG
04E0 725D	1053	JC	68
04E2 C6064200E6	1054	MOV	CMD_BLOCK+0,WR_LONG_CMD
04E7 B04B	1055	MOV	AL,DMA_WRITE
04E9 EB5A	1056	JMP	SHORT DMA_OPN
	1057	WR_LONG	ENDP
	1058		
04EB	1059	CHK_LONG	PROC NEAR
04EB A04600	1060	MOV	AL,CMD_BLOCK+4
04EE 3C80	1061	CMF	AL,080H
04F0 F5	1062	CMC	
04F1 C3	1063	RET	
	1064	CHK_LONG	ENDP
	1065		

```

LOC OBJ          LINE  SOURCE
1066             ;-----
1067             ;     SEEK   (AH = 0CH)   ;
1068             ;-----
1069
04F2             DISK_SEEK   PROC   NEAR
04F2 C6064200B   1071             MOV   CMD_BLOCK,SEEK_CMD
04F7 EB30        1072             JMP   SHORT  NDMA_OPN
1073             DISK_SEEK   ENDP
1074
1075             ;-----
1076             ;     READ SECTOR BUFFER (AH = 0EH) ;
1077             ;-----
1078
04F9             RD_BUFF PROC   NEAR
04F9 C60642000E  1080             MOV   CMD_BLOCK+0,RD_BUFF_CMD
04FE C606460001  1081             MOV   CMD_BLOCK+4,1 ; ONLY ONE BLOCK
0503 B047        1082             MOV   AL,DMA_READ
0505 EB3E        1083             JMP   SHORT  DMA_OPN
1084             RD_BUFF ENDP
1085
1086             ;-----
1087             ;     WRITE SECTOR BUFFER (AH = 0FH) ;
1088             ;-----
1089
0507             WR_BUFF PROC   NEAR
0507 C60642000F  1091             MOV   CMD_BLOCK+0,WR_BUFF_CMD
050C C606460001  1092             MOV   CMD_BLOCK+4,1 ; ONLY ONE BLOCK
0511 B04B        1093             MOV   AL,DMA_WRITE
0513 EB30        1094             JMP   SHORT  DMA_OPN
1095             WR_BUFF ENDP
1096
1097             ;-----
1098             ;     TEST DISK READY (AH = 010H) ;
1099             ;-----
1100
0515             TST_RDY PROC   NEAR
0515 C606420000  1102             MOV   CMD_BLOCK+0,TST_RDY_CMD
051A EB1A        1103             JMP   SHORT  NDMA_OPN
1104             TST_RDY ENDP
1105
1106             ;-----
1107             ;     RECALIBRATE (AH = 011H) ;
1108             ;-----
1109
051C             HDISK_RECAL  PROC   NEAR
051C C606420001  1111             MOV   CMD_BLOCK,RECAL_CMD
0521 EB13        1112             JMP   SHORT  NDMA_OPN
1113             HDISK_RECAL  ENDP
1114
1115             ;-----
1116             ;     CONTROLLER RAM DIAGNOSTICS (AH = 012H) ;
1117             ;-----
1118
0523             RAM_DIAG   PROC   NEAR
0523 C6064200E0  1120             MOV   CMD_BLOCK+0,RAM_DIAG_CMD
0528 EB0C        1121             JMP   SHORT  NDMA_OPN
1122             RAM_DIAG   ENDP
1123
1124             ;-----
1125             ;     DRIVE DIAGNOSTICS (AH = 013H) ;
1126             ;-----
1127
052A             CHK_DRV PROC   NEAR
052A C6064200E3  1129             MOV   CMD_BLOCK+0,CHK_DRV_CMD
052F EB05        1130             JMP   SHORT  NDMA_OPN
1131             CHK_DRV ENDP
1132
1133             ;-----
1134             ;     CONTROLLER INTERNAL DIAGNOSTICS (AH = 014H) ;
1135             ;-----
1136
0531             CNTLR_DIAG  PROC   NEAR
0531 C6064200E4  1138             MOV   CMD_BLOCK+0,CNTRLR_DIAG_CMD
1139             CNTLR_DIAG  ENDP
1140

```

```

LOC OBJ          LINE    SOURCE
1141             |-----|
1142             |          SUPPORT ROUTINES          |
1143             |-----|
1144
0536             NDMA_OPN:
0536 B002         1146             MOV     AL,02H
0538 E02700      1147             CALL   COMMAND          ; ISSUE THE COMMAND
0538 7221         1148             JC     G11
053D EB16        1149             JMP    SHORT  G3
053F             1150             G8:
053F C606740009  1151             MOV     DISK_STATUS,DMA_BOUNDARY
0544 C3          1152             RET
0545             1153             DMA_OPN:
0545 E05701      1154             CALL   DMA_SETUP        ; SET UP FOR DMA OPERATION
0548 72F5        1155             JC     G8
054A B003        1156             MOV     AL,03H
054C E01300      1157             CALL   COMMAND          ; ISSUE THE COMMAND
054F 720D        1158             JC     G11
0551 B003        1159             MOV     AL,03H
0553 E60A        1160             OUT    DMA+10,AL       ; INITIALIZE THE DISK CHANNEL
0555             1161             G3:
0555 E421        1162             IN     AL,021H
0557 24DF        1163             AND    AL,0DFH
0559 E621        1164             OUT    021H,AL
055B E8AA01      1165             CALL   WAIT_INT
055E             1166             G11:
055E E03B00      1167             CALL   ERROR_CHK
0561 C3          1168             RET
1169
1170             |-----|
1171             | COMMAND                          |
1172             | THIS ROUTINE OUTPUTS THE COMMAND BLOCK |
1173             | INPUT                              |
1174             | AL = CONTROLLER DMA/INTERRUPT REGISTER MASK |
1175             |-----|
1176
0562             1177             COMMAND PROC HEAR
0562 BE4200      1179             MOV     SI,OFFSET CMD_BLOCK
0565 E01B02      1180             CALL   PORT_2
0568 EE          1181             OUT    DX,AL           ; CONTROLLER SELECT PULSE
0569 E01C02      1182             CALL   PORT_3
056C EE          1183             OUT    DX,AL
056D 2E:9       1184             SUB    CX,CX           ; WAIT COUNT
056F E00C02      1185             CALL   PORT_1
0572             1186             WAIT_BUSY:
0572 EC          1187             IN     AL,DX           ; GET STATUS
0573 240F        1188             AND    AL,0FH
0575 3C0D        1189             CMP    AL,R1_BUSY OR R1_BUS OR R1_REQ
0577 7409        1190             JE     C1
0579 E2F7        1191             LOOP  WAIT_BUSY
057B C606740000  1192             MOV     DISK_STATUS,TIME_OUT
0580 F9          1193             STC
0581 C3          1194             RET                   ; ERROR RETURN
0582             1195             C1:
0582 FC          1196             CLD
0583 B90600      1197             MOV     CX,6           ; BYTE COUNT
0586             1198             CH3:
0586 E0E801      1199             CALL   PORT_0
0589 AC          1200             LOOBS          ; GET THE NEXT COMMAND BYTE
058A EE          1201             OUT    DX,AL           ; OUT IT GOES
058B E2F9        1202             LOOP  CH3             ; DO MORE
1203
058D E0EE01      1204             CALL   PORT_1         ; STATUS
0590 EC          1205             IN     AL,DX
0591 A801        1206             TEST   AL,R1_REQ
0593 7406        1207             JZ     CH7
0595 C606740002  1208             MOV     DISK_STATUS,BAD_CNTL
059A F9          1209             STC
059B             1210             CH7:
059B C3          1211             RET
1212             COMMAND ENDP
1213
1214             |-----|
1215             |          SENSE STATUS BYTES          |
1216             |-----|
1217             | BYTE 0

```

```

LOC OBJ          LINE  SOURCE
1218             |   BIT   7   ADDRESS VALID, WHEN SET   :
1219             |   BIT   6   SPARE, SET TO ZERO           :
1220             |   BITS 5-4   ERROR TYPE                   :
1221             |   BITS 3-0   ERROR CODE                   :
1222             |   :                                         :
1223             |   BYTE 1                                     :
1224             |   BITS 7-6   ZERO                         :
1225             |   BIT   5   DRIVE (0-1)                   :
1226             |   BITS 4-0   HEAD NUMBER                  :
1227             |   :                                         :
1228             |   BYTE 2                                     :
1229             |   BITS 7-5   CYLINDER HIGH                :
1230             |   BITS 4-0   SECTOR NUMBER                :
1231             |   :                                         :
1232             |   BYTE 3                                     :
1233             |   BITS 7-0   CYLINDER LOW                 :
1234             |   :                                         :
1235             |-----|
1236
059C             1237   ERROR_CHK   PROC   NEAR
1238                   ASSUME ES:DATA
059C A07400       1239   MOV   AL,DISK_STATUS   ; CHECK IF THERE WAS AN ERROR
059F 0AC0        1240   OR    AL,AL
05A1 7501        1241   JNZ   621
05A3 C3         1242   RET
1243
1244             |-----| PERFORM SENSE STATUS
1245
05A4             1246   G21:
05A4 B04000      1247   MOV   AX,DATA
05A7 8EC0       1248   MOV   ES,AX           ; ESTABLISH SEGMENT
05A9 2BC0       1249   SUB   AX,AX
05AB 8BF8       1250   MOV   DI,AX
05AD C606420003 1251   MOV   CMO_BLOCK+0,SENSE_CHD
05B2 2AC0       1252   SUB   AL,AL
05B4 E8ABFF     1253   CALL  COMMAND         ; ISSUE SENSE STATUS COMMAND
05B7 7223       1254   JC    SENSE_ABORT     ; CANNOT RECOVER
05B9 B90400     1255   MOV   CX,4
05BC           1256   G22:
05BC E0CB00     1257   CALL  HD_WAIT_REQ
05BF 7220       1258   JC    G24
05C1 E8AD01     1259   CALL  PORT_0
05C4 EC         1260   IN    AL,DX
05C5 26884542   1261   MOV   ES:HD_ERROR(DI),AL ; STORE AWAY SENSE BYTES
05C9 47         1262   INC   DI
05CA E0B101     1263   CALL  PORT_1
05CD E2ED       1264   LOOP  G22
05CF E0B800     1265   CALL  HD_WAIT_REQ
05D2 720D       1266   JC    G24
05D4 E09A01     1267   CALL  PORT_0
05D7 EC         1268   IN    AL,DX
05DB A802       1269   TEST  AL,2
05DA 740F       1270   JZ    STAT_ERR
05DC           1271   SENSE_ABORT:
05DC C6067400FF 1272   MOV   DISK_STATUS,SENSE_FAIL
05E1           1273   G24:
05E1 F9         1274   STC
05E2 C3         1275   RET
1276   ERROR_CHK   ENDP
1277
05E3 1A06       1278   T_0   DW    TYPE_0
05E5 2706       1279   T_1   DW    TYPE_1
05E7 6A06       1280   T_2   DW    TYPE_2
05E9 7706       1281   T_3   DW    TYPE_3
1282
05EB           1283   STAT_ERR:
05EB 268A1E4200 1284   MOV   BL,ES:HD_ERROR   ; GET ERROR BYTE
05FO 0AC3       1285   MOV   AL,BL
05F2 240F       1286   AND   AL,0FH
05F4 80E330     1287   AND   BL,00110000B    ; ISOLATE TYPE
05F7 2AFF       1288   SUB   BH,BH
05F9 B103       1289   MOV   CL,3
05FB D3EB       1290   SHR   BX,CL           ; ADJUST
05FD 2EFA7E305 1291   JMP   WORD PTR CS:[BX + OFFSET T_0]
1292   ASSUME ES:NOTHING
1293
0602             1294   TYPE_TABLE LABEL BYTE

```

LOC OBJ	LINE	SOURCE	
0602 00204020800020	1295	DB	0,BAD_CNTLRL,BAD_SEEK,BAD_CNTLRL,TIME_OUT,0,BAD_CNTLRL
0609 0040	1296	DB	0,BAD_SEEK
0009	1297	TYPE0_LEN	EQU 4-TYPE0_TABLE
0608	1298	TYPE1_TABLE	LABEL BYTE
0608 1010020004	1299	DB	BAD_ECC,BAD_ECC,BAD_ADDR_MARK,0,RECORD_NOT_FND
0610 400000110B	1300	DB	BAD_SEEK,0,0,DATA_CORRECTED,BAD_TRACK
000A	1301	TYPE1_LEN	EQU 4-TYPE1_TABLE
0615	1302	TYPE2_TABLE	LABEL BYTE
0615 0102	1303	DB	BAD_CHD,BAD_ADDR_MARK
0002	1304	TYPE2_LEN	EQU 4-TYPE2_TABLE
0617	1305	TYPE3_TABLE	LABEL BYTE
0617 202010	1306	DB	BAD_CNTLRL,BAD_CNTLRL,BAD_ECC
0003	1307	TYPE3_LEN	EQU 4-TYPE3_TABLE
	1308		
	1309		!----- TYPE 0 ERROR
	1310		
061A	1311		TYPE_0:
061A BB0206	1312	MOV	BX,OFFSET TYPE0_TABLE
0610 3C09	1313	CHP	AL,TYPE0_LEN ; CHECK IF ERROR IS DEFINED
061F 7363	1314	JAE	UNDEF_ERR_L
0621 2ED7	1315	XLAT	CS:TYPE0_TABLE ; TABLE LOOKUP
0623 A27400	1316	MOV	DISK_STATUS,AL ; SET ERROR CODE
0626 C3	1317	RET	
	1318		
	1319		!----- TYPE 1 ERROR
	1320		
0627	1321		TYPE_1:
0627 BB0B06	1322	MOV	BX,OFFSET TYPE1_TABLE
062A 8BC8	1323	MOV	CX,AX
062C 3C0A	1324	CHP	AL,TYPE1_LEN ; CHECK IF ERROR IS DEFINED
062E 7354	1325	JAE	UNDEF_ERR_L
0630 2ED7	1326	XLAT	CS:TYPE1_TABLE ; TABLE LOOKUP
0632 A27400	1327	MOV	DISK_STATUS,AL ; SET ERROR CODE
0635 80E108	1328	AND	CL,08H ; CORRECTED ECC
0638 80F908	1329	CHP	CL,08H
063B 752A	1330	JNZ	G30
	1331		
	1332		!----- OBTAIN ECC ERROR BURST LENGTH
	1333		
0630 C60642000D	1334	MOV	CHD_BLOCK+0,RD_ECC_CHD
0642 2AC0	1335	SUB	AL,AL
0644 E81BFF	1336	CALL	COMPHAND
0647 721E	1337	JC	G30
0649 E83E00	1338	CALL	HD_WAIT_REQ
064C 7219	1339	JC	G30
064E E82001	1340	CALL	PORT_0
0651 EC	1341	IN	AL,DX
0652 8AC8	1342	MOV	CL,AL
0654 E83300	1343	CALL	HD_WAIT_REQ
0657 720E	1344	JC	G30
0659 E81501	1345	CALL	PORT_0
065C EC	1346	IN	AL,DX
065D A801	1347	TEST	AL,01H
065F 7406	1348	JZ	G30
0661 C606740020	1349	MOV	DISK_STATUS,BAD_CNTLRL
0666 F9	1350	STC	
0667	1351		G30:
0667 8AC1	1352	MOV	AL,CL
0669 C3	1353	RET	
	1354		
	1355		!----- TYPE 2 ERROR
	1356		
066A	1357		TYPE_2:
066A BB1506	1358	MOV	BX,OFFSET TYPE2_TABLE
066D 3C02	1359	CHP	AL,TYPE2_LEN ; CHECK IF ERROR IS DEFINED
066F 7313	1360	JAE	UNDEF_ERR_L
0671 2ED7	1361	XLAT	CS:TYPE1_TABLE ; TABLE LOOKUP
0673 A27400	1362	MOV	DISK_STATUS,AL ; SET ERROR CODE
0676 C3	1363	RET	
	1364		
	1365		!----- TYPE 3 ERROR
	1366		
0677	1367		TYPE_3:
0677 BB1706	1368	MOV	BX,OFFSET TYPE3_TABLE
067A 3C03	1369	CHP	AL,TYPE3_LEN
067C 7306	1370	JAE	UNDEF_ERR_L
067E 2ED7	1371	XLAT	CS:TYPE3_TABLE

```

LOC OBJ          LINE  SOURCE
0680 A27400      1372      MOV     DISK_STATUS,AL
0683 C3          1373      RET
                1374
0684            1375      UNDEF_ERR_L1:
0684 C6067400BB  1376      MOV     DISK_STATUS,UNDEF_ERR
0689 C3          1377      RET
                1378
068A            1379      HD_WAIT_REQ  PROC   NEAR
068A 51          1380      PUSH   CX
068B 2BC9        1381      SUB    CX,CX
068D E8EE00      1382      CALL   PORT_1
0690            1383      L1:
0690 EC          1384      IN     AL,DX
0691 A801        1385      TEST   AL,R1_REQ
0693 7508        1386      JNZ    L2
0695 E2F9        1387      LOOP   L1
0697 C6067400B0  1388      MOV     DISK_STATUS,TIME_OUT
069C F9          1389      STC
069D            1390      L2:
069D 59          1391      POP    CX
069E C3          1392      RET
                1393      HD_WAIT_REQ  ENDP
                1394
                1395      ;-----
                1396      ; DMA_SETUP                                ;
                1397      ; THIS ROUTINE SETS UP FOR DMA OPERATIONS. ;
                1398      ; INPUT                                    ;
                1399      ; (AL) = MODE BYTE FOR THE DMA             ;
                1400      ; (ES:BX) = ADDRESS TO READ/WRITE THE DATA ;
                1401      ; OUTPUT                                    ;
                1402      ; (AX) DESTROYED                          ;
                1403      ;-----
069F            1404      DMA_SETUP  PROC   NEAR
069F 50          1405      PUSH   AX
06A0 A04600      1406      MOV     AL,CHD_BLOCK+4
06A3 3CB1        1407      CMP    AL,81H                ; BLOCK COUNT OUT OF RANGE
06A5 58          1408      POP    AX
06A6 7202        1409      JB     J1
06A8 F9          1410      STC
06A9 C3          1411      RET
06AA            1412      J1:
06AA 51          1413      PUSH   CX                    ; SAVE THE REGISTER
06AB FA          1414      CLI                    ; NO MORE INTERRUPTS
06AC E60C        1415      OUT    DMA+12,AL            ; SET THE FIRST/LAST F/F
06AE 50          1416      PUSH   AX
06AF 58          1417      POP    AX
06B0 E60B        1418      OUT    DMA+11,AL            ; OUTPUT THE MODE BYTE
06B2 8CC0        1419      MOV     AX,ES                ; GET THE ES VALUE
06B4 B104        1420      MOV     CL,4                  ; SHIFT COUNT
06B6 D3C0        1421      ROL    AX,CL                 ; ROTATE LEFT
06B8 8AE8        1422      MOV     CH,AL                ; GET HIGHEST NYBBLE OF ES TO CH
06BA 24F0        1423      AND    AL,0F0H              ; ZERO THE LOW NYBBLE FROM SEGMENT
06BC 03C3        1424      ADD    AX,BX                 ; TEST FOR CARRY FROM ADDITION
06BE 7302        1425      JNC    J33
06C0 FECS        1426      INC    CH                    ; CARRY MEANS HIGH 4 BITS MUST BE INC
06C2            1427      J33:
06C2 50          1428      PUSH   AX                    ; SAVE START ADDRESS
06C3 E606        1429      OUT    DMA+6,AL              ; OUTPUT LOW ADDRESS
06C5 8AC4        1430      MOV     AL,AH
06C7 E606        1431      OUT    DMA+6,AL              ; OUTPUT HIGH ADDRESS
06C9 8ACS        1432      MOV     AL,CH                ; GET HIGH 4 BITS
06CB 240F        1433      AND    AL,0FH
06CD E602        1434      OUT    DMA_HIGH,AL           ; OUTPUT THE HIGH 4 BITS TO PAGE REG
                1435
                1436      ;----- DETERMINE COUNT
                1437
06CF A04600      1438      MOV     AL,CHD_BLOCK+4        ; RECOVER BLOCK COUNT
06D2 D0E0        1439      SHL    AL,1                  ; MULTIPLY BY 512 BYTES PER SECTOR
06D4 FEC8        1440      DEC    AL                    ; AND DECREMENT VALUE BY ONE
06D6 8AE0        1441      MOV     AH,AL
06DB B0FF        1442      MOV     AL,0FFH
                1443
                1444      ;----- HANDLE READ AND WRITE LONG (5160 BYTE BLOCKS)
                1445
06DA 50          1446      PUSH   AX                    ; SAVE REGISTER
06DB A04200      1447      MOV     AL,CHD_BLOCK+0        ; GET COMMAND
06DE 3CE5        1448      CMP    AL,RD_LONG_CMD

```

```

LOC OBJ          LINE  SOURCE
06E0 7407        1449          JE      ADD4
06E2 3CE6        1450          CMP     AL,HR_LONG_CMO
06E4 7403        1451          JE      ADD4
06E6 58          1452          POP     AX              ; RESTORE REGISTER
06E7 EB11        1453          JMP     SHORT  J20
06E9            1454          ADD4:
06E9 58          1455          POP     AX              ; RESTORE REGISTER
06EA B80402       1456          MOV     AX,516D        ; ONE BLOCK (512) PLUS 4 BYTES ECC
06ED 53          1457          PUSH   BX
06EE 2AFF        1458          SUB     BH,BH
06FD 8A1E4600    1459          MOV     BL,CHD_BLOCK+4
06F4 52          1460          PUSH   DX
06F5 F7E3        1461          MUL     BX              ; BLOCK COUNT TIMES 516
06F7 5A          1462          POP     DX
06F8 5B          1463          POP     BX
06F9 48          1464          DEC     AX              ; ADJUST
06FA            1465          J20:
06FA            1466
06FA 50          1467          PUSH   AX              ; SAVE COUNT VALUE
06FB E607        1468          OUT    DMA+7,AL        ; LOW BYTE OF COUNT
06FD 8AC4        1469          MOV     AL,AH
06FF E607        1470          OUT    DMA+7,AL        ; HIGH BYTE OF COUNT
0701 FB          1471          STI
0702 59          1472          POP     CX              ; INTERRUPTS BACK ON
0703 58          1473          POP     AX              ; RECOVER COUNT VALUE
0704 03C1        1474          ADD    AX,CX           ; RECOVER ADDRESS VALUE
0706 59          1475          POP     CX              ; ADD, TEST FOR 64K OVERFLOW
0707 C3          1476          RET                     ; RECOVER REGISTER
0707            1477          DMA_SETUP  ENDP      ; RETURN TO CALLER, CFL SET BY ABOVE IF ERROR
0707            1478
0707            1479          ;-----
0707            1480          ; WAIT_INT          :
0707            1481          ; THIS ROUTINE WAITS FOR THE FIXED DISK :
0707            1482          ; CONTROLLER TO SIGNAL THAT AN INTERRUPT :
0707            1483          ; HAS OCCURRED.    :
0707            1484          ;-----
0708            1485          WAIT_INT   PROC   NEAR
0708 FB          1486          STI              ; TURN ON INTERRUPTS
0709 53          1487          PUSH   BX        ; PRESERVE REGISTERS
070A 51          1488          PUSH   CX
070B 06          1489          PUSH   ES
070C 56          1490          PUSH   SI
070D 1E          1491          PUSH   DS
070E 2BC0        1492          ASSUME DS:DUMMY
0710 8ED8        1493          SUB    AX,AX
0712 C4360401    1494          MOV    DS,AX        ; ESTABLISH SEGMENT
0716 1F          1495          LES    SI,FB_TBL_VEC
0716            1496          ASSUME DS:DATA
0716            1497          POP    DS
0716            1498
0716            1499          ;----- SET TIMEOUT VALUES
0716            1500
0717 2AFF        1501          SUB    BH,BH
0719 268A5C09    1502          MOV    BL,BYTE PTR ES:[SI+9] ; STANDARD TIME OUT
071D 8A264200    1503          MOV    ...
0721 80FC04        1504          CMP    AH,FHDRV_CMO
0724 7506        1505          JNZ    W5
0726 268A5C0A    1506          MOV    BL,BYTE PTR ES:[SI+0AH] ; FORMAT DRIVE
072A EB09        1507          JMP    SHORT W4
072C 80FCE3      1508          M5:  CMP    AH,CHK_DRV_CMO
072F 7504        1509          JNZ    W4
0731 268A5C0B    1510          MOV    BL,BYTE PTR ES:[SI+0BH] ; CHECK DRIVE
0735            1511          W4:
0735 2BC9        1512          SUB    CX,CX
0735            1513
0735            1514          ;----- WAIT FOR INTERRUPT
0735            1515
0737            1516          W1:
0737 E84400        1517          CALL   PORT_1
073A EC          1518          IN     AL,DX
073B 2420        1519          AND    AL,020H
073D 3C20        1520          CMP    AL,020H        ; DID INTERRUPT OCCUR
073F 740A        1521          JZ     W2
0741 E2F4        1522          LOOP  W1              ; INNER LOOP
0743 4B          1523          DEC    BX
0744 75F1        1524          JNZ    W1              ; OUTER LOOP
0746 C06740080    1525          MOV    DISK_STATUS,TIME_OUT
074B            1526          W2:

```

```

LOC OBJ          LINE    SOURCE
074B E02300     1527    CALL    PORT_0
074E EC         1528    IN      AL,DX
074F 2402       1529    AND    AL,2
0751 08067400   1530    OR     DISK_STATUS,AL
0755 E03000     1531    CALL    PORT_3
0756 32C0       1532    XOR    AL,AL
075A EE         1533    OUT    DX,AL
075B 5E         1534    POP    SI
075C 07         1535    POP    ES
075D 59         1536    POP    CX
075E 5B         1537    POP    BX
075F C3         1538    RET
1539    WAIT_INT    ENDP
1540
0760           1541    HD_INT    PROC    NEAR
0760 50         1542    PUSH    AX
0761 B020       1543    MOV    AL,EDI
0763 E620       1544    OUT    INT_CTL_PORT,AL
0765 B007       1545    MOV    AL,07H
0767 E60A       1546    OUT    DMA+10,AL
0769 E421       1547    IN     AL,021H
076B 0C20       1548    OR     AL,020H
076D E621       1549    OUT    021H,AL
076F 58         1550    POP    AX
0770 CF         1551    IRET
1552    HD_INT    ENDP
1553
1554    ;-----
1555    ; PORTS
1556    ; GENERATE PROPER PORT VALUE
1557    ; BASED ON THE PORT OFFSET
1558    ;-----
1559
0771           1560    PORT_0    PROC    NEAR
0771 BA2003     1561    MOV    DX,HF_PORT
0774 50         1562    PUSH    AX
0775 2AE4       1563    SUB    AH,AH
0777 A07700    1564    MOV    AL,PORT_OFF
077A 03D0       1565    ADD    DX,AX
077C 58         1566    POP    AX
077D C3         1567    RET
1568    PORT_0    ENDP
1569
077E           1570    PORT_1    PROC    NEAR
077E E0F0FF     1571    CALL    PORT_0
0781 42         1572    INC    DX
0782 C3         1573    RET
1574    PORT_1    ENDP
1575
0783           1576    PORT_2    PROC    NEAR
0783 E0F8FF     1577    CALL    PORT_1
0786 42         1578    INC    DX
0787 C3         1579    RET
1580    PORT_2    ENDP
1581
0788           1582    PORT_3    PROC    NEAR
0788 E0F8FF     1583    CALL    PORT_2
078B 42         1584    INC    DX
078C C3         1585    RET
1586    PORT_3    ENDP
1587
1588    ;-----
1589    ; SM2_OFFS
1590    ; DETERMINE PARAMETER TABLE OFFSET
1591    ; USING CONTROLLER PORT TWO AND
1592    ; DRIVE NUMBER SPECIFIER (0-1)
1593    ;-----
1594
078D           1595    SM2_OFFS    PROC    NEAR
078D E0F3FF     1596    CALL    PORT_2
0790 EC         1597    IN     AL,DX
0791 50         1598    PUSH    AX
0792 E0E9FF     1599    CALL    PORT_1
0795 EC         1600    IN     AL,DX
0796 2402       1601    AND    AL,2
0798 58         1602    POP    AX
0799 7516       1603    JNZ    SM2_OFFS_ERR
079B 8A264300   1604    MOV    AH,CMD_BLOCK+1

```

LOC OBJ	LINE	SOURCE		
079F 80E420	1605	AND	AH,00100000B	; DRIVE 0 OR 1
07A2 7504	1606	JNZ	SH2_AND	
07A4 D0E8	1607	SHR	AL,1	; ADJUST
07A6 D0E8	1608	SHR	AL,1	
07A8	1609	SH2_AND:		
07A8 2403	1610	AND	AL,011B	; ISOLATE
07AA B104	1611	MOV	CL,4	
07AC D2E0	1612	SHL	AL,CL	; ADJUST
07AE 2AE4	1613	SUB	AH,AH	
07B0 C3	1614	RET		
07B1	1615	SH2_OFFS_ERR:		
07B1 F9	1616	STC		
07B2 C3	1617	RET		
	1618	SH2_OFFS	ENDP	
	1619			
07B3 30382F31362F38 32	1620	DB	'08/16/82'	; RELEASE MARKER
	1621			
07BB	1622	END_ADDRESS	LABEL	BYTE
----	1623	CODE	ENDS	
	1624	END		

# APPENDIX B: 8088 ASSEMBLY INSTRUCTION SET REFERENCE

Appendix B



## Operand Summary

"reg field Bit Assignments:

16-Bit (w=1)	8-Bit (w=0)	Segment
000 AX	000 AL	00 ES
001 CX	001 CL	01 CS
010 DX	010 DL	10 SS
011 BX	011 BL	11 DS
100 SP	100 AH	
101 BP	101 CH	
110 SI	110 DH	
111 DI	111 BH	

### Second Instruction Byte Summary

mod	xxx	r/m
-----	-----	-----

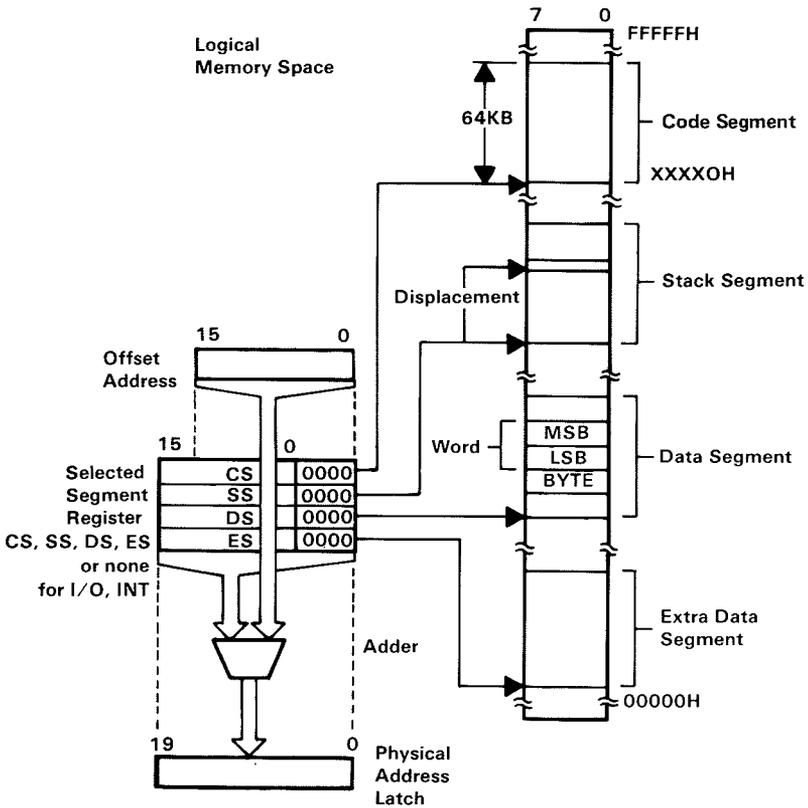
mod	Displacement
00	DISP=0*, disp-low and disp-high are absent
01	DISP=disp-low sign-extended to 16-bits, disp-high is absent
10	DISP=disp-high: disp-low
11	r/m is treated as a "reg" field

r/m	Operand Address
000	(BX) + (SI) + DISP
001	(BX) + (DI) + DISP
010	(BP) + (SI) + DISP
011	(BP) + (DI) + DISP
100	(SI) + DISP
101	(DI) + DISP
110	(BP) + DISP*
111	(BX) + DISP

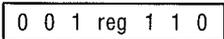
DISP follows 2nd byte of instruction (before data if required).

\*except if mod = 00 and r/m = 110 then EA = disp-high: disp-low.

# Memory Segmentation Model



## Segment Override Prefix



## Use of Segment Override

Operand Register	Default	With Override Prefix
IP (Code Address)	CS	Never
SP (Stack Address)	SS	Never
BP (Stack Address or Stack Marker)	SS	BP + DS or ES, or CS
SI or DI (not including strings)	DS	ES, SS, or CS
SI (Implicit Source Address for Strings)	DS	ES, SS, or CS
DI (Implicit Destination Address for Strings)	ES	Never

## Data Transfer

**MOV** = Move

Register/memory to/from register

1 0 0 0 1 0 d w	mod reg r/m
-----------------	-------------

Immediate to register/memory

1 1 0 0 0 1 1 w	mod 0 0 0 r/m	data	data if w=1
-----------------	---------------	------	-------------

Immediate to register

1 0 1 1 w reg	data	data if w=1
---------------	------	-------------

Memory to accumulator

1 0 1 0 0 0 0 w	addr-low	addr-high
-----------------	----------	-----------

Accumulator to memory

1 0 1 0 0 0 1 w	addr-low	addr-high
-----------------	----------	-----------

Register/memory to segment register

1 0 0 0 1 1 1 0	mod 0 reg r/m
-----------------	---------------

Segment register to register/memory

1 0 0 0 1 1 0 0	mod 0 reg r/m
-----------------	---------------

**PUSH** = Push

Register/memory

1 1 1 1 1 1 1 1	mod 1 1 0 r/m
-----------------	---------------

Register

0 1 0 1 0 reg
---------------

Segment register

0 0 0 reg 1 1 0
-----------------

**POP** = Pop

Register/memory

1 0 0 0 1 1 1 1	mod 0 0 0 r/m
-----------------	---------------

Register

0 1 0 1 1 reg
---------------

Segment register

0 0 0 reg 1 1 1
-----------------

**XCHG** = Exchange

Register/memory with register

1	0	0	0	0	1	1	w	mod	reg	r/m
---	---	---	---	---	---	---	---	-----	-----	-----

Register with accumulator

1	0	0	1	0	reg
---	---	---	---	---	-----

**IN** = Input to AL/AX from

Fixed port

1	1	1	0	0	1	0	w	port
---	---	---	---	---	---	---	---	------

Variable port (DX)

1	1	1	0	1	1	0	w
---	---	---	---	---	---	---	---

**OUT** = Output from AL/AX to

Fixed port

1	1	1	0	0	1	1	w	port
---	---	---	---	---	---	---	---	------

Variable port (DX)

1	1	1	0	1	1	0	w
---	---	---	---	---	---	---	---

**XLAT** = Translate byte to AL

1	1	0	1	0	1	1	1
---	---	---	---	---	---	---	---

**LEA** = Load EA to register

1	0	0	0	1	1	0	1	mod	reg	r/m
---	---	---	---	---	---	---	---	-----	-----	-----

**LDS** = Load pointer to DS

1	1	0	0	0	1	0	1	mod	reg	r/m
---	---	---	---	---	---	---	---	-----	-----	-----

**LES** = Load pointer to ES

1	1	0	0	0	1	0	0	mod	reg	r/m
---	---	---	---	---	---	---	---	-----	-----	-----

**LAHF** = Load AH with flags

1	0	0	1	1	1	1	1
---	---	---	---	---	---	---	---

**SAHF** = Store AH into flags

1	0	0	1	1	1	1	0
---	---	---	---	---	---	---	---

**PUSHF** = Push flags

1	0	0	1	1	1	0	0
---	---	---	---	---	---	---	---

**POPF** = Pop flags

1	0	0	1	1	1	0	1
---	---	---	---	---	---	---	---

## Arithmetic

### ADD = Add

Register/memory with register to either

0 0 0 0 0 0 d w	mod reg r/m
-----------------	-------------

Immediate to register/memory

1 0 0 0 0 0 s w	mod 0 0 0 r/m	data	data if s:w=01
-----------------	---------------	------	----------------

Immediate to accumulator

0 0 0 0 0 1 0 w	data	data if w=1
-----------------	------	-------------

### ADC = Add with carry

Register/memory with register to either

0 0 0 1 0 0 d w	mod reg r/m
-----------------	-------------

Immediate to register/memory

1 0 0 0 0 0 s w	mod 0 1 0 r/m	data	data if s:w=01
-----------------	---------------	------	----------------

Immediate to accumulator

0 0 0 1 0 1 0 w	data	data if w=1
-----------------	------	-------------

### INC = Increment

Register/memory

1 1 1 1 1 1 1 w	mod 0 0 0 r/m
-----------------	---------------

Register

0 1 0 0 0 reg
---------------

### AAA = ASCII adjust for add

0 0 1 1 0 1 1 1
-----------------

### DAA = Decimal adjust for add

0 0 1 0 0 1 1 1
-----------------

### SUB = Subtract

Register/memory and register to either

0 0 1 0 1 0 d w	mod reg r/m
-----------------	-------------

Immediate from register/memory

1 0 0 0 0 0 s w	mod 1 0 1 r/m	data	data if s:w=01
-----------------	---------------	------	----------------

Immediate from accumulator

0 0 1 0 1 1 0 w	data	data if w=1
-----------------	------	-------------

**SBB** = Subtract with borrow  
Register/memory and register to either

0 0 0 1 1 0 d w	mod reg r/m
-----------------	-------------

Immediate from register/memory

1 0 0 0 0 0 s w	mod 0 1 1 r/m	data	data if s:w=01
-----------------	---------------	------	----------------

Immediate from accumulator

0 0 0 1 1 1 0 w	data	data if w=1
-----------------	------	-------------

**DEC** = Decrement  
Register/memory

1 1 1 1 1 1 1 w	mod 0 0 1 r/m
-----------------	---------------

Register

0 1 0 0 1 reg
---------------

**NEG** = Change sign

1 1 1 1 0 1 1 w	mod 0 1 1 r/m
-----------------	---------------

**CMP** = Compare  
Register/memory and register

0 0 1 1 1 0 d w	mod reg r/m
-----------------	-------------

Immediate with register/memory

1 0 0 0 0 0 s w	mod 1 1 1 r/m	data	data if s:w=01
-----------------	---------------	------	----------------

Immediate with accumulator

0 0 1 1 1 1 0 w	data	data if w=1
-----------------	------	-------------

**AAS** = ASCII adjust for subtract

0 0 1 1 1 1 1 1
-----------------

**DAS** = Decimal adjust for subtract

0 0 1 0 1 1 1 1
-----------------

**MUL** = Multiply (unsigned)

1 1 1 1 0 1 1 w	mod 1 0 0 r/m
-----------------	---------------

**IMUL** = Integer multiply (signed)

1 1 1 1 0 1 1 w	mod 1 0 1 r/m
-----------------	---------------

**AAM** = ASCII adjust for multiply

1 1 0 1 0 1 0 0	0 0 0 0 1 0 1 0
-----------------	-----------------

**DIV** = Divide (unsigned)

1 1 1 1 0 1 1 w	mod 1 1 0 r/m
-----------------	---------------

**IDIV** = Integer divide (signed)

1	1	1	1	0	1	1	w	mod	1	1	1	r/m
---	---	---	---	---	---	---	---	-----	---	---	---	-----

**AAD** = ASCII adjust for divide

1	1	0	1	0	1	0	1	0	0	0	0	1	0	1	0
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

**CBW** = Convert byte to word

1	0	0	1	1	0	0	0
---	---	---	---	---	---	---	---

**CWD** = Convert word to double word

1	0	0	1	1	0	0	1
---	---	---	---	---	---	---	---

## Logic

**NOT** = Invert

1	1	1	1	0	1	1	w	mod	0	1	0	r/m
---	---	---	---	---	---	---	---	-----	---	---	---	-----

**SHL/SAL** = Shift logical/arithmetic left

1	1	0	1	0	0	v	w	mod	1	0	0	r/m
---	---	---	---	---	---	---	---	-----	---	---	---	-----

**SHR** = Shift logical right

1	1	0	1	0	0	v	w	mod	1	0	1	r/m
---	---	---	---	---	---	---	---	-----	---	---	---	-----

**SAR** = Shift arithmetic right

1	1	0	1	0	0	v	w	mod	1	1	1	r/m
---	---	---	---	---	---	---	---	-----	---	---	---	-----

**ROL** = Rotate left

1	1	0	1	0	0	v	w	mod	0	0	0	r/m
---	---	---	---	---	---	---	---	-----	---	---	---	-----

**ROR** = Rotate right

1	1	0	1	0	0	v	w	mod	0	0	1	r/m
---	---	---	---	---	---	---	---	-----	---	---	---	-----

**RCL** = Rotate through carry left

1	1	0	1	0	0	v	w	mod	0	1	0	r/m
---	---	---	---	---	---	---	---	-----	---	---	---	-----

**RCR** = Rotate through carry right

1	1	0	1	0	0	v	w	mod	0	1	1	r/m
---	---	---	---	---	---	---	---	-----	---	---	---	-----

**AND** = And

Register/memory and register to either

0	0	1	0	0	0	d	w	mod	reg	r/m
---	---	---	---	---	---	---	---	-----	-----	-----

Immediate to register/memory

1	0	0	0	0	0	0	w	mod	1	0	0	r/m	data	data if w=1
---	---	---	---	---	---	---	---	-----	---	---	---	-----	------	-------------

Immediate to accumulator

0	0	1	0	0	1	0	w	data	data if w=1
---	---	---	---	---	---	---	---	------	-------------

**TEST** = And function to flags, no result  
Register/memory and register

1 0 0 0 0 1 0 w	mod reg r/m
-----------------	-------------

Immediate data and register/memory

1 1 1 1 0 1 1 w	mod 0 0 0 r/m	data	data if w=1
-----------------	---------------	------	-------------

Immediate data and accumulator

1 0 1 0 1 0 0 w	data	data if w=1
-----------------	------	-------------

**OR** = OR

Register/memory and register to either

0 0 0 0 1 0 d w	mod reg r/m
-----------------	-------------

Immediate to register/memory

1 0 0 0 0 0 0 w	mod 0 0 1 r/m	data	data if w=1
-----------------	---------------	------	-------------

Immediate to accumulator

0 0 0 0 1 1 0 w	data	data if w=1
-----------------	------	-------------

**XOR** = Exclusive or

Register/memory and register to either

0 0 1 1 0 0 d w	mod reg r/m
-----------------	-------------

Immediate to register/memory

1 0 0 0 0 0 0 w	mod 1 1 0 r/m	data	data if w=1
-----------------	---------------	------	-------------

Immediate to accumulator

0 0 1 1 0 1 0 w	data	data if w=1
-----------------	------	-------------

## String Manipulation

**REP** = Repeat

1 1 1 1 0 0 1 z
-----------------

**MOVS** = Move String

1 0 1 0 0 1 0 w
-----------------

**CMPS** = Compare String

1 0 1 0 0 1 1 w
-----------------

**SCAS** = Scan String

1 0 1 0 1 1 1 w
-----------------

**LODS** = Load String

1	0	1	0	1	1	0	w
---	---	---	---	---	---	---	---

**STOS** = Store String

1	0	1	0	1	0	1	w
---	---	---	---	---	---	---	---

## Control Transfer

**CALL** = Call

Direct within segment

1	1	1	0	1	0	0	0	disp-low	disp-high
---	---	---	---	---	---	---	---	----------	-----------

Indirect within segment

1	1	1	1	1	1	1	1	mod	0	1	0	r/m
---	---	---	---	---	---	---	---	-----	---	---	---	-----

Direct intersegment

1	0	0	1	1	0	1	0	offset-low	offset-high
---	---	---	---	---	---	---	---	------------	-------------

seg-low	seg-high
---------	----------

Indirect intersegment

1	1	1	1	1	1	1	1	mod	0	1	1	r/m
---	---	---	---	---	---	---	---	-----	---	---	---	-----

**JMP** = Unconditional Jump

Direct within segment

1	1	1	0	1	0	0	1	disp-low	disp-high
---	---	---	---	---	---	---	---	----------	-----------

Direct within segment-short

1	1	1	0	1	0	1	1	disp
---	---	---	---	---	---	---	---	------

Indirect within segment

1	1	1	1	1	1	1	1	mod	1	0	0	r/m
---	---	---	---	---	---	---	---	-----	---	---	---	-----

Direct intersegment

1	1	1	0	1	0	1	0	offset-low	offset-high
---	---	---	---	---	---	---	---	------------	-------------

seg-low	seg-high
---------	----------

Indirect intersegment

1	1	1	1	1	1	1	1	mod	1	0	1	r/m
---	---	---	---	---	---	---	---	-----	---	---	---	-----

**RET** = Return from CALL

Within segment

1	1	0	0	0	0	1	1
---	---	---	---	---	---	---	---

Within segment adding immediate to SP

1	1	0	0	0	0	1	0	
							data-low	data-high

Intersegment

1	1	0	0	1	0	1	1
---	---	---	---	---	---	---	---

Intersegment, adding immediate to SP

1	1	0	0	0	0	1	0	
							data-low	data-high

**JE/JZ** = Jump on equal/zero

0	1	1	1	0	1	0	0
							disp

**JL/JNGE** = Jump on less/not greater or equal

0	1	1	1	1	1	0	0
							disp

**JLE/JNG** = Jump on less or equal/not greater

0	1	1	1	1	1	1	0
							disp

**JB/JNAE** = Jump on below/not above or equal

0	1	1	1	0	0	1	0
							disp

**JBE/JNA** = Jump on below or equal/not above

0	1	1	1	0	1	1	0
							disp

**JP/JPE** = Jump on parity/parity even

0	1	1	1	1	0	1	0
							disp

**JO** = Jump on overflow

0	1	1	1	0	0	0	0
							disp

**JS** = Jump on sign

0	1	1	1	1	0	0	0
							disp

**JNE/JNZ** = Jump on not equal/not zero

0	1	1	1	0	1	0	1
							disp

**JNL/JGE** = Jump on not less/greater or equal

0	1	1	1	1	1	0	1
							disp

**JNLE/JG** = Jump on not less or equal/greater

0 1 1 1 1 1 1 1	disp
-----------------	------

**JNB/JAE** = Jump on not below/above or equal

0 1 1 1 0 0 1 1	disp
-----------------	------

**JNBE/JA** = Jump on not below or equal/above

0 1 1 1 0 1 1 1	disp
-----------------	------

**JNP/JPO** = Jump on not parity/parity odd

0 1 1 1 1 0 1 1	disp
-----------------	------

**JNO** = Jump on not overflow

0 1 1 1 0 0 0 1	disp
-----------------	------

**JNS** = Jump on not sign

0 1 1 1 1 0 0 1	disp
-----------------	------

**LOOP** = Loop CX times

1 1 1 0 0 0 1 0	disp
-----------------	------

**LOOPZ/LOOPE** = Loop while zero/equal

1 1 1 0 0 0 0 1	disp
-----------------	------

**LOOPNZ/LOOPNE** = Loop while not zero/not equal

1 1 1 0 0 0 0 0	disp
-----------------	------

**JCXZ** = Jump on CX zero

1 1 1 0 0 0 1 1	disp
-----------------	------

## 8088 Conditional Transfer Operations

Instruction	Condition	Interpretation
JE or JZ	ZF = 1	"equal" or "zero"
JL or JNGE	(SF xor OF) = 1	"less" or "not greater or equal"
JLE or JNG	((SF xor OF) or ZF) = 1	"less or equal" or "not greater"
JB or JNAE or JC	CF = 1	"below" or "not above or equal"
JBE or JNA	(CF or ZF) = 1	"below or equal" or "not above"
JP or JPE	PF = 1	"parity" or "parity even"
JO	OF = 1	"overflow"
JS	SF = 1	"sign"
JNE or JNZ	ZF = 0	"not equal" or "not zero"
JNL or JGE	(SF xor OF) = 0	"not less" or "greater or equal"
JNLE or JG	((SF xor OF) or ZF) = 0	"not less or equal" or "greater"
JNB or JAE or JNC	CF = 0	"not below" or "above or equal"
JNBE or JA	(CF or ZF) = 0	"not below or equal" or "above"
JNP or JPO	PF = 0	"not parity" or "parity odd"
JNO	OF = 0	"not overflow"
JNS	SF = 0	"not sign"

\*"Above" and "below" refer to the relation between two unsigned values, while "greater" and "less" refer to the relation between two signed values.

**INT** = Interrupt

Type specified

1 1 0 0 1 1 0 1	type
-----------------	------

Type 3

1 1 0 0 1 1 0 0
-----------------

**INTO** = Interrupt on overflow

1 1 0 0 1 1 1 0
-----------------

**IRET** = Interrupt return

1 1 0 0 1 1 1 1
-----------------

## Processor Control

**CLC** = Clear carry

1	1	1	1	1	0	0	0
---	---	---	---	---	---	---	---

**STC** = Set carry

1	1	1	1	1	0	0	1
---	---	---	---	---	---	---	---

**CMC** = Complement carry

1	1	1	1	0	1	0	1
---	---	---	---	---	---	---	---

**NOP** = No operation

1	0	0	1	0	0	0	0
---	---	---	---	---	---	---	---

**CLD** = Clear direction

1	1	1	1	1	1	0	0
---	---	---	---	---	---	---	---

**STD** = Set direction

1	1	1	1	1	1	0	1
---	---	---	---	---	---	---	---

**CLI** = Clear interrupt

1	1	1	1	1	0	1	0
---	---	---	---	---	---	---	---

**STI** = Set interrupt

1	1	1	1	1	0	1	1
---	---	---	---	---	---	---	---

**HLT** = Halt

1	1	1	1	0	1	0	0
---	---	---	---	---	---	---	---

**WAIT** = Wait

1	0	0	1	1	0	1	1
---	---	---	---	---	---	---	---

**LOCK** = Bus lock prefix

1	1	1	1	0	0	0	0
---	---	---	---	---	---	---	---

**ESC** = Escape (to external device)

1	1	0	1	1	x	x	x	mod	x	x	x	r/m
---	---	---	---	---	---	---	---	-----	---	---	---	-----

### Footnotes:

if d = 1 then "to"; if d = 0 then "from"

if w = 1 then word instruction; if w = 0 then byte instruction

if s:w = 01 then 16 bits of immediate data from the operand

if s:w = 11 then an immediate data byte is sign extended to form the 16-bit operand

if v = 0 then "count" = 1; if v = 1 then "count" in (CL)

x = don't care

z is used for some string primitives to compare with ZF FLAG

AL = 8-bit accumulator

AX = 16-bit accumulator

CX = Count register

DS = Data segment

DX = Variable port register

ES = Extra segment

Above/below refers to unsigned value

Greater = more positive;

Less = less positive (more negative) signed values

## 8088 Instruction Set Matrix

LO HI	0	1	2	3	4	5	6	7
0	ADD b,f,r/m	ADD w,f,r/m	ADD b,t,r/m	ADD w,t,r/m	ADD b,ia	ADD w,ia	PUSH ES	POP ES
1	ADC b,f,r/m	ADC w,f,r/m	ADC b,t,r/m	ADC w,t,r/m	ADC b,i	ADC w,i	PUSH SS	POP SS
2	AND b,f,r/m	AND w,f,r/m	AND b,t,r/m	AND w,t,r/m	AND b,i	AND w,i	SEG =ES	DAA
3	XOR b,f,r/m	XOR w,f,r/m	XOR b,t,r/m	XOR w,t,r/m	XOR b,i	XOR w,i	SEG =SS	AAA
4	INC AX	INC CX	INC DX	INC BX	INC SP	INC BP	INC SI	INC DI
5	PUSH AX	PUSH CX	PUSH DX	PUSH BX	PUSH SP	PUSH BP	PUSH SI	PUSH DI
6								
7	JO	JNO	JB/ JNAE	JNB/ JAE	JE/ JZ	JNE/ JNZ	JBE/ JNA	JNBE/ JA
8	Immed b,r/m	Immed w,r/m	Immed b,r/m	Immed is,r/m	TEST b,r/m	TEST w,r/m	XCHG b,r/m	XCHG w,r/m
9	NOP	XCHG CX	XCHG DX	XCHG BX	XCHG SP	XCHG BP	XCHG SI	XCHG DI
A	MOV m AL	MOV m AL	MOV AL m	MOV AL m	MOVS b	MOVS w	CMPS b	CMPS w
B	MOV i AL	MOV i CL	MOV i DL	MOV i BL	MOV i AH	MOV i CH	MOV i DH	MOV i BH
C			RET (i+SP)	RET	LES	LDS	MOV b,i,r/m	MOV w,i,r/m
D	Shift b	Shift w	Shift b,v	Shift w,v	AAM	AAD		XLAT
E	LOOPNZ/ LOOPNE	LOOPZ/ LOOPE	LOOP	JCXZ	IN b	IN w	OUT b	OUT w
F	LOCK		REP	REP z	HLT	CMC	Grp 1 b,r/m	Grp 1 w,r/m

b = byte operation

d = direct

f = from CPU reg

i = immediate

ia = immed. to accum.

id = indirect

is = immed. byte, sign ext.

l = long ie. intersegment

m = memory

r/m = EA is second byte

si = short intrasegment

sr = segment register

t = to CPU reg

v = variable

w = word operation

z = zero

## 8088 Instruction Set Matrix

LO HI	8	9	A	B	C	D	E	F
0	OR b,f,r/m	w,f,r/m	OR b,t,r/m	OR w,t,r/m	OR b,i	OR w,i	PUSH CS	
1	SBB b,f,r/m	SBB w,f,r/m	SBB b,t,r/m	SBB w,t,r/m	SBB b,i	SBB w,i	PUSH DS	POP DS
2	SUB b,f,r/m	SUB w,f,r/m	SUB b,t,r/m	SUB w,t,r/m	SUB b,i	SUB w,i	SEG= CS	DAS
3	CMP b,f,r/m	CMP w,f,r/m	CMP b,t,r/m	CMP w,t,r/m	CMP b,i	CMP w,i	SEG= CS	AAS
4	DEC AX	DEC CX	DEC DX	DEC BX	DEC SP	DEC BP	DEC SI	DEC DI
5	POP AX	POP CX	POP DX	POP BX	POP SP	POP BP	POP SI	POP DI
6								
7	JS	JNS	JP/ JPE	JNP/ JPO	JL/ JNGE	JNL/ JGE	JLE/ JNG	JNLE/ JG
8	MOV b,f,r/m	MOV w,f,r/m	MOV b,t,r/m	MOV w,t,r/m	MOV sr,t,r/m	LEA	MOV sr,f,r/m	POP r/m
9	CBW	CWD	CALL l,d	WAIT	PUSHF	POPF	SAHF	LAHF
A	TEST b,i	TEST w,i	STOS b	STOS w	LODS b	LODS w	SCAS b	SCAS w
B	MOV i AX	MOV i CX	MOV i DX	MOV i BX	MOV i SP	MOV i BP	MOV i SI	MOV i DI
C			RET l,(i+SP)	RET l	INT Type 3	INT (Any)	INTO	IRET
D	ESC 0	ESC 1	ESC 2	ESC 3	ESC 4	ESC 5	ESC 6	ESC 7
E	CALL d	JMP d	JMP l,d	JMP si,d	IN v,b	IN v,w	OUT v,b	OUT v,w
F	CLC	STC	CLI	STI	CLD	STD	Grp 2 b,r/m	Grp 2 w,r/m

where:

mod $\square$ r/m	000	001	010	011	100	101	110	111
Immed	ADD	OR	ADC	SBB	AND	SUB	XOR	CMP
Shift	ROL	ROR	RCL	RCR	SHL/SAL	SHR	—	SAR
Grp 1	TEST	—	NOT	NEG	MUL	IMUL	DIV	IDIV
Grp 2	INC	DEC	CALL id	CALL l,id	JMP id	JMP l,id	PUSH	—

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# APPENDIX C: OF CHARACTERS, KEYSTROKES, AND COLOR

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
00	0	Blank (Null)	Ctrl 2		Black	Black	Non-Display
01	1	☺	Ctrl A		Black	Blue	Underline
02	2	☹	Ctrl B		Black	Green	Normal
03	3	♥	Ctrl C		Black	Cyan	Normal
04	4	♦	Ctrl D		Black	Red	Normal
05	5	♣	Ctrl E		Black	Magenta	Normal
06	6	♠	Ctrl F		Black	Brown	Normal
07	7	•	Ctrl G		Black	Light Grey	Normal
08	8	•	Ctrl H, Backspace, Shift Backspace		Black	Dark Grey	Non-Display
09	9	○	Ctrl I		Black	Light Blue	High Intensity Underline
0A	10	○	Ctrl J, Ctrl ↵		Black	Light Green	High Intensity
0B	11	♂	Ctrl K		Black	Light Green	High Intensity
0C	12	♀	Ctrl L		Black	Light Red	High Intensity
0D	13	♪	Ctrl M, ↵, Shift ↵		Black	Light Magenta	High Intensity
0E	14	♪	Ctrl N		Black	Yellow	High Intensity
0F	15	☀	Ctrl O		Black	White	High Intensity
10	16	▶	Ctrl P		Blue	Black	Normal
11	17	◀	Ctrl Q		Blue	Blue	Underline
12	18	↕	Ctrl R		Blue	Green	Normal
13	19	!!	Ctrl S		Blue	Cyan	Normal
14	20	¶	Ctrl T		Blue	Red	Normal
15	21	§	Ctrl U			Magenta	Normal
16	22	■	Ctrl V		Blue	Brown	Normal
17	23	↑	Ctrl W		Blue	Light Grey	Normal

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
18	24	↑	Ctrl X		Blue	Dark Grey	High Intensity
19	25	↓	Ctrl Y		Blue	Light Blue	High Intensity Underline
1A	26	→	Ctrl Z		Blue	Light Green	High Intensity
1B	27	←	Ctrl [, Esc, Shift Esc, Ctrl Esc		Blue	Light Cyan	High Intensity
1C	28	↖	Ctrl \		Blue	Light Red	High Intensity
1D	29	↔	Ctrl ]		Blue	Light Magenta	High Intensity
1E	30	▲	Ctrl 6		Blue	Yellow	High Intensity
1F	31	▼	Ctrl -		Blue	White	High Intensity
20	32	Blank Space	Space Bar, Shift, Space, Ctrl Space, Alt Space		Green	Black	Normal
21	33	!	!	Shift	Green	Blue	Underline
22	34	"	"	Shift	Green	Green	Normal
23	35	#	#	Shift	Green	Cyan	Normal
24	36	\$	\$	Shift	Green	Red	Normal
25	37	%	%	Shift	Green	Magenta	Normal
26	38	&	&	Shift	Green	Brown	Normal
27	39	'	'		Green	Light Grey	Normal
28	40	(	(	Shift	Green	Dark Grey	High Intensity
29	41	)	)	Shift	Green	Light Blue	High Intensity Underline
2A	42	*	*	Note 1	Green	Light Green	High Intensity
28	43	+	+	Shift	Green	Light Cyan	High Intensity
2C	44	,	,		Green	Light Red	High Intensity
2D	45	-	-		Green	Light Magenta	High Intensity
2E	46	.	.	Note 2	Green	Yellow	High Intensity

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
2F	47	/	/		Green	White	High Intensity
30	48	0	0	Note 3	Cyan	Black	Normal
31	49	1	1	Note 3	Cyan	Blue	Underline
32	50	2	2	Note 3	Cyan	Green	Normal
33	51	3	3	Note 3	Cyan	Cyan	Normal
34	52	4	4	Note 3	Cyan	Red	Normal
35	53	5	5	Note 3	Cyan	Magenta	Normal
36	54	6	6	Note 3	Cyan	Brown	Normal
37	55	7	7	Note 3	Cyan	Light Grey	Normal
38	56	8	8	Note 3	Cyan	Dark Grey	High Intensity
39	57	9	9	Note 3	Cyan	Light Blue	High Intensity Underline
3A	58	:	:	Shift	Cyan	Light Green	High Intensity
3B	59	;	;		Cyan	Light Cyan	High Intensity
3C	60	<	<	Shift	Cyan	Light Red	High Intensity
3D	61	=	=		Cyan	Light Magenta	High Intensity
3E	62	>	>	Shift	Cyan	Yellow	High Intensity
3F	63	?	?	Shift	Cyan	White	High Intensity
40	64	@	@	Shift	Red	Black	Normal
41	65	A	A	Note 4	Red	Blue	Underline
42	66	B	B	Note 4	Red	Green	Normal
43	67	C	C	Note 4	Red	Cyan	Normal
44	68	D	D	Note 4	Red	Red	Normal
45	69	E	E	Note 4	Red	Magenta	Normal
46	70	F	F	Note 4	Red	Brown	Normal
47	71	G	G	Note 4	Red	Light Grey	Normal
48	72	H	H	Note 4	Red	Dark Grey	High Intensity
49	73	I	I	Note 4	Red	Light Blue	High Intensity Underline
4A	74	J	J	Note 4	Red	Light Green	High Intensity

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
4B	75	K	K	Note 4	Red	Light Cyan	High Intensity
4C	76	L	L	Note 4	Red	Light Red	High Intensity
4D	77	M	M	Note 4	Red	Light Magenta	High Intensity
4E	78	N	N	Note 4	Red	Yellow	High Intensity
4F	79	O	O	Note 4	Red	White	High Intensity
50	80	P	P	Note 4	Magenta	Black	Normal
51	81	Q	Q	Note 4	Magenta	Blue	Underline
52	82	R	R	Note 4	Magenta	Green	Normal
53	83	S	S	Note 4	Magenta	Cyan	Normal
54	84	T	T	Note 4	Magenta	Red	Normal
55	85	U	U	Note 4	Magenta	Magenta	Normal
56	86	V	V	Note 4	Magenta	Brown	Normal
57	87	W	W	Note 4	Magenta	Light Grey	Normal
58	88	X	X	Note 4	Magenta	Dark Grey	High Intensity
59	89	Y	Y	Note 4	Magenta	Light Blue	High Intensity Underline
5A	90	Z	Z	Note 4	Magenta	Light Green	High Intensity
5B	91	[	[		Magenta	Light Cyan	High Intensity
5C	92	\	\		Magenta	Light Red	High Intensity
5D	93	]	]		Magenta	Light Magenta	High Intensity
5E	94	^	^	Shift	Magenta	Yellow	High Intensity
5F	95	—	—	Shift	Magenta	White	High Intensity
60	96	.	.		Yellow	Black	Normal
61	97	a	a	Note 5	Yellow	Blue	Underline
62	98	b	b	Note 5	Yellow	Green	Normal
63	99	c	c	Note 5	Yellow	Cyan	Normal
64	100	d	d	Note 5	Yellow	Red	Normal
65	101	e	e	Note 5	Yellow	Magenta	Normal
66	102	f	f	Note 5	Yellow	Brown	Normal

## C-4 Of Characters, Keystrokes, and Colors

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
67	103	g	g	Note 5	Yellow	Light Grey	Normal
68	104	h	h	Note 5	Yellow	Dark Grey	High Intensity
69	105	i	i	Note 5	Yellow	Light Blue	High Intensity Underline
6A	106	j	j	Note 5	Yellow	Light Green	High Intensity
6B	107	k	k	Note 5	Yellow	Light Cyan	High Intensity
6C	108	l	l	Note 5	Yellow	Light Red	High Intensity
6D	109	m	m	Note 5	Yellow	Light Magenta	High Intensity
6E	110	n	n	Note 5	Yellow	Yellow	High Intensity
6F	111	o	o	Note 5	Yellow	White	High Intensity
70	112	p	p	Note 5	White	Black	Reverse Video
71	113	q	q	Note 5	White	Blue	Underline
72	114	r	r	Note 5	White	Green	Normal
73	115	s	s	Note 5	White	Cyan	Normal
74	116	f	f	Note 5	White	Red	Normal
75	117	u	u	Note 5	White	Magenta	Normal
76	118	v	v	Note 5	White	Brown	Normal
77	119	w	w	Note 5	White	Light Grey	Normal
78	120	x	x	Note 5	White	Dark Grey	Reverse Video
79	121	y	y	Note 5	White	Light Blue	High Intensity Underline
7A	122	z	z	Note 5	White	Light Green	High Intensity
7B	123	{	{	Shift	White	Light Cyan	High Intensity
7C	124			Shift	White	Light Red	High Intensity
7D	125	}	}	Shift	White	Light Magenta	High Intensity
7E	126	~	~	Shift	White	Yellow	High Intensity
7F	127	Δ	Ctrl ←		White	White	High Intensity

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
* * * * 80 to FF Hex are Flashing in both Color & IBM Monochrome * * * *							
80	128	Ç	Alt 128	Note 6	Black	Black	Non-Display
81	129	ü	Alt 129	Note 6	Black	Blue	Underline
82	130	é	Alt 130	Note 6	Black	Green	Normal
83	131	â	Alt 131	Note 6	Black	Cyan	Normal
84	132	ä	Alt 132	Note 6	Black	Red	Normal
85	133	à	Alt 133	Note 6	Black	Magenta	Normal
86	134	å	Alt 134	Note 6	Black	Brown	Normal
87	135	ç	Alt 135	Note 6	Black	Light Grey	Normal
88	136	ê	Alt 136	Note 6	Black	Dark Grey	Non-Display
89	137	ë	Alt 137	Note 6	Black	Light Blue	High Intensity Underline
8A	138	è	Alt 138	Note 6	Black	Light Green	High Intensity
8B	139	ï	Alt 139	Note 6	Black	Light Cyan	High Intensity
8C	140	î	Alt 140	Note 6	Black	Light Red	High Intensity
8D	141	ì	Alt 141	Note 6	Black	Light Magenta	High Intensity
8E	142	Ā	Alt 142	Note 6	Black	Yellow	High Intensity
8F	143	Ă	Alt 143	Note 6	Black	White	High Intensity
90	144	É	Alt 144	Note 6	Blue	Black	Normal
91	145	æ	Alt 145	Note 6	Blue	Blue	Underline
92	146	Æ	Alt 146	Note 6	Blue	Green	Normal
93	147	ô	Alt 147	Note 6	Blue	Cyan	Normal
94	148	ö	Alt 148	Note 6	Blue	Red	Normal
95	149	ò	Alt 149	Note 6	Blue	Magenta	Normal
96	150	û	Alt 150	Note 6	Blue	Brown	Normal
97	151	ù	Alt 151	Note 6	Blue	Light Grey	Normal
98	152	ÿ	Alt 152	Note 6	Blue	Dark Grey	High Intensity
99	153	ö	Alt 153	Note 6	Blue	Light Blue	High Intensity Underline
9A	154	ü	Alt 154	Note 6	Blue	Light Green	High Intensity

## C-6 Of Characters, Keystrokes, and Colors

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
9B	155	¢	Alt 155	Note 6	Blue	Light Cyan	High Intensity
9C	156	£	Alt 156	Note 6	Blue	Light Red	High Intensity
9D	157	¥	Alt 157	Note 6	Blue	Light Magenta	High Intensity
9E	158	Pt	Alt 158	Note 6	Blue	Yellow	High Intensity
9F	159	∫	Alt 159	Note 6	Blue	White	High Intensity
A0	160	á	Alt 160	Note 6	Green	Black	Normal
A1	161	í	Alt 161	Note 6	Green	Blue	Underline
A2	162	ó	Alt 162	Note 6	Green	Green	Normal
A3	163	ú	Alt 163	Note 6	Green	Cyan	Normal
A4	164	ñ	Alt 164	Note 6	Green	Red	Normal
A5	165	Ñ	Alt 165	Note 6	Green	Magenta	Normal
A6	166	<u>a</u>	Alt 166	Note 6	Green	Brown	Normal
A7	167	<u>o</u>	Alt 167	Note 6	Green	Light Grey	Normal
A8	168	¿	Alt 168	Note 6	Green	Dark Grey	High Intensity
A9	169	┌	Alt 169	Note 6	Green	Light Blue	High Intensity Underline
AA	170	→	Alt 170	Note 6	Green	Light Green	High Intensity
AB	171	½	Alt 171	Note 6	Green	Light Cyan	High Intensity
AC	172	¼	Alt 172	Note 6	Green	Light Red	High Intensity
AD	173	i	Alt 173	Note 6	Green	Light Magenta	High Intensity
AE	174	<<	Alt 174	Note 6	Green	Yellow	High Intensity
AF	175	>>	Alt 175	Note 6	Green	White	High Intensity
B0	176	⋮	Alt 176	Note 6	Cyan	Black	Normal
B1	177	⋮	Alt 177	Note 6	Cyan	Blue	Underline
B2	178	⋮	Alt 178	Note 6	Cyan	Green	Normal
B3	179		Alt 179	Note 6	Cyan	Cyan	Normal
B4	180		Alt 180	Note 6	Cyan	Red	Normal
B5	181		Alt 181	Note 6	Cyan	Magenta	Normal
B6	182		Alt 182	Note 6	Cyan	Brown	Normal

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
B7	183		Alt 183	Note 6	Cyan	Light Grey	Normal
B8	184		Alt 184	Note 6	Cyan	Dark Grey	High Intensity
B9	185		Alt 185	Note 6	Cyan	Light Blue	High Intensity Underline
BA	186		Alt 186	Note 6	Cyan	Light Green	High Intensity
BB	187		Alt 187	Note 6	Cyan	Light Cyan	High Intensity
BC	188		Alt 188	Note 6	Cyan	Light Red	High Intensity
BD	189		Alt 189	Note 6	Cyan	Light Magenta	High Intensity
BE	190		Alt 190	Note 6	Cyan	Yellow	High Intensity
BF	191		Alt 191	Note 6	Cyan	White	High Intensity
C0	192		Alt 192	Note 6	Red	Black	Normal
C1	193		Alt 193	Note 6	Red	Blue	Underline
C2	194		Alt 194	Note 6	Red	Green	Normal
C3	195		Alt 195	Note 6	Red	Cyan	Normal
C4	196		Alt 196	Note 6	Red	Red	Normal
C5	197		Alt 197	Note 6	Red	Magenta	Normal
C6	198		Alt 198	Note 6	Red	Brown	Normal
C7	199		Alt 199	Note 6	Red	Light Grey	Normal
C8	200		Alt 200	Note 6	Red	Dark Grey	High Intensity
C9	201		Alt 201	Note 6	Red	Light Blue	High Intensity Underline
CA	202		Alt 202	Note 6	Red	Light Green	High Intensity
CB	203		Alt 203	Note 6	Red	Light Cyan	High Intensity
CC	204		Alt 204	Note 6	Red	Light Red	High Intensity
CD	205		Alt 205	Note 6	Red	Light Magenta	High Intensity
CE	206		Alt 206	Note 6	Red	Yellow	High Intensity
CF	207		Alt 207	Note 6	Red	White	High Intensity
D0	208		Alt 208	Note 6	Magenta	Black	Normal

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
D1	209		Alt 209	Note 6	Magenta	Blue	Underline
D2	210		Alt 210	Note 6	Magenta	Green	Normal
D3	211		Alt 211	Note 6	Magenta	Cyan	Normal
D4	212		Alt 212	Note 6	Magenta	Red	Normal
D5	213		Alt 213	Note 6	Magenta	Magenta	Normal
D6	214		Alt 214	Note 6	Magenta	Brown	Normal
D7	215		Alt 215	Note 6	Magenta	Light Grey	Normal
D8	216		Alt 216	Note 6	Magenta	Dark Grey	High Intensity
D9	217		Alt 217	Note 6	Magenta	Light Blue	High Intensity Underline
DA	218		Alt 218	Note 6	Magenta	Light Green	High Intensity
DB	219		Alt 219	Note 6	Magenta	Light Cyan	High Intensity
DC	220		Alt 220	Note 6	Magenta	Light Red	High Intensity
DD	221		Alt 221	Note 6	Magenta	Light Magenta	High Intensity
DE	222		Alt 222	Note 6	Magenta	Yellow	High Intensity
DF	223		Alt 223	Note 6	Magenta	White	High Intensity
E0	224	$\alpha$	Alt 224	Note 6	Yellow	Black	Normal
E1	225	$\beta$	Alt 225	Note 6	Yellow	Blue	Underline
E2	226	$\Gamma$	Alt 226	Note 6	Yellow	Green	Normal
E3	227	$\pi$	Alt 227	Note 6	Yellow	Cyan	Normal
E4	228	$\Sigma$	Alt 228	Note 6	Yellow	Red	Normal
E5	229	$\sigma$	Alt 229	Note 6	Yellow	Magenta	Normal
E6	230	$\mu$	Alt 230	Note 6	Yellow	Brown	Normal
E7	231	$\tau$	Alt 231	Note 6	Yellow	Light Grey	Normal
E8	232	$\Phi$	Alt 232	Note 6	Yellow	Dark Grey	High Intensity
E9	233	$\theta$	Alt 233	Note 6	Yellow	Light Blue	High Intensity Underline
EA	234	$\Omega$	Alt 234	Note 6	Yellow	Light Green	High Intensity
EB	235	$\delta$	Alt 235	Note 6	Yellow	Light Cyan	High Intensity

Value		As Characters			As Text Attributes		
					Color/Graphics Monitor Adapter		IBM Monochrome Display Adapter
Hex	Dec	Symbol	Keystrokes	Modes	Background	Foreground	
EC	236	∞	Alt 236	Note 6	Yellow	Light Red	High Intensity
ED	237	ϕ	Alt 237	Note 6	Yellow	Light Magenta	High Intensity
EE	238	ε	Alt 238	Note 6	Yellow	Yellow	High Intensity
EF	239	∩	Alt 239	Note 6	Yellow	White	High Intensity
F0	240	≡	Alt 240	Note 6	White	Black	Reverse Video
F1	241	±	Alt 241	Note 6	White	Blue	Underline
F2	242	≥	Alt 242	Note 6	White	Green	Normal
F3	243	≤	Alt 243	Note 6	White	Cyan	Normal
F4	244	↵	Alt 244	Note 6	White	Red	Normal
F5	245	↵	Alt 245	Note 6	White	Magenta	Normal
F6	246	÷	Alt 246	Note 6	White	Brown	Normal
F7	247	≈	Alt 247	Note 6	White	Light Grey	Normal
F8	248	○	Alt 248	Note 6	White	Dark Grey	Reverse Video
F9	249	●	Alt 249	Note 6	White	Light Blue	High Intensity Underline
FA	250	•	Alt 250	Note 6	White	Light Green	High Intensity
FB	251	√	Alt 251	Note 6	White	Light Cyan	High Intensity
FC	252	η	Alt 252	Note 6	White	Light Red	High Intensity
FD	253	2	Alt 253	Note 6	White	Light Magenta	High Intensity
FE	254	■	Alt 254	Note 6	White	Yellow	High Intensity
FF	255	<b>BLANK</b>	Alt 255	Note 6	White	White	High Intensity

NOTE 1 Asterisk (\*) can easily be keyed using two methods:

1) hit the  key or 2) in shift mode hit the

 key.

NOTE 2 Period (.) can easily be keyed using two methods:

1) hit the  key or 2) in shift or Num Lock

mode hit the  key.

NOTE 3 Numeric characters (0—9) can easily be keyed using two methods: 1) hit the numeric keys on the top row of the typewriter portion of the keyboard or 2) in shift or Num Lock mode hit the numeric keys in the 10—key pad portion of the keyboard.

NOTE 4 Upper case alphabetic characters (A—Z) can easily be keyed in two modes: 1) in shift mode the appropriate alphabetic key or 2) in Caps Lock mode hit the appropriate alphabetic key.

NOTE 5 Lower case alphabetic characters (a—z) can easily be keyed in two modes: 1) in "normal" mode hit the appropriate key or 2) in Caps Lock combined with shift mode hit the appropriate alphabetic key.

NOTE 6 The 3 digits after the Alt key must be typed from the numeric key pad (keys 71—73, 75—77, 79—82). Character codes 000 through 255 can be entered in this fashion. (With Caps Lock activated, Character codes 97 through 122 will display upper case rather than lower case alphabetic characters.)

# Character Set (00-7F) Quick Reference

DECIMAL VALUE	➡	0	16	32	48	64	80	96	112
↕	HEXA-DECIMAL VALUE	0	1	2	3	4	5	6	7
0	0	BLANK (NULL)	▶	BLANK (SPACE)	0	@	P	'	p
1	1	😊	◀	!	1	A	Q	a	q
2	2	😬	↕	"	2	B	R	b	r
3	3	♥	!!	#	3	C	S	c	s
4	4	♦	¶	\$	4	D	T	d	t
5	5	♣	§	%	5	E	U	e	u
6	6	♠	■	&	6	F	V	f	v
7	7	•	↕	'	7	G	W	g	w
8	8	●	↑	(	8	H	X	h	x
9	9	○	↓	)	9	I	Y	i	y
10	A	◉	→	*	:	J	Z	j	z
11	B	♂	←	+	;	K	I	k	{
12	C	♀	└	,	<	L	\	l	
13	D	🎵	↔	—	=	M	I	m	}
14	E	🎶	▲	.	>	N	^	n	~
15	F	☀	▼	/	?	O	_	o	△

# Character Set (80-FF) Quick Reference

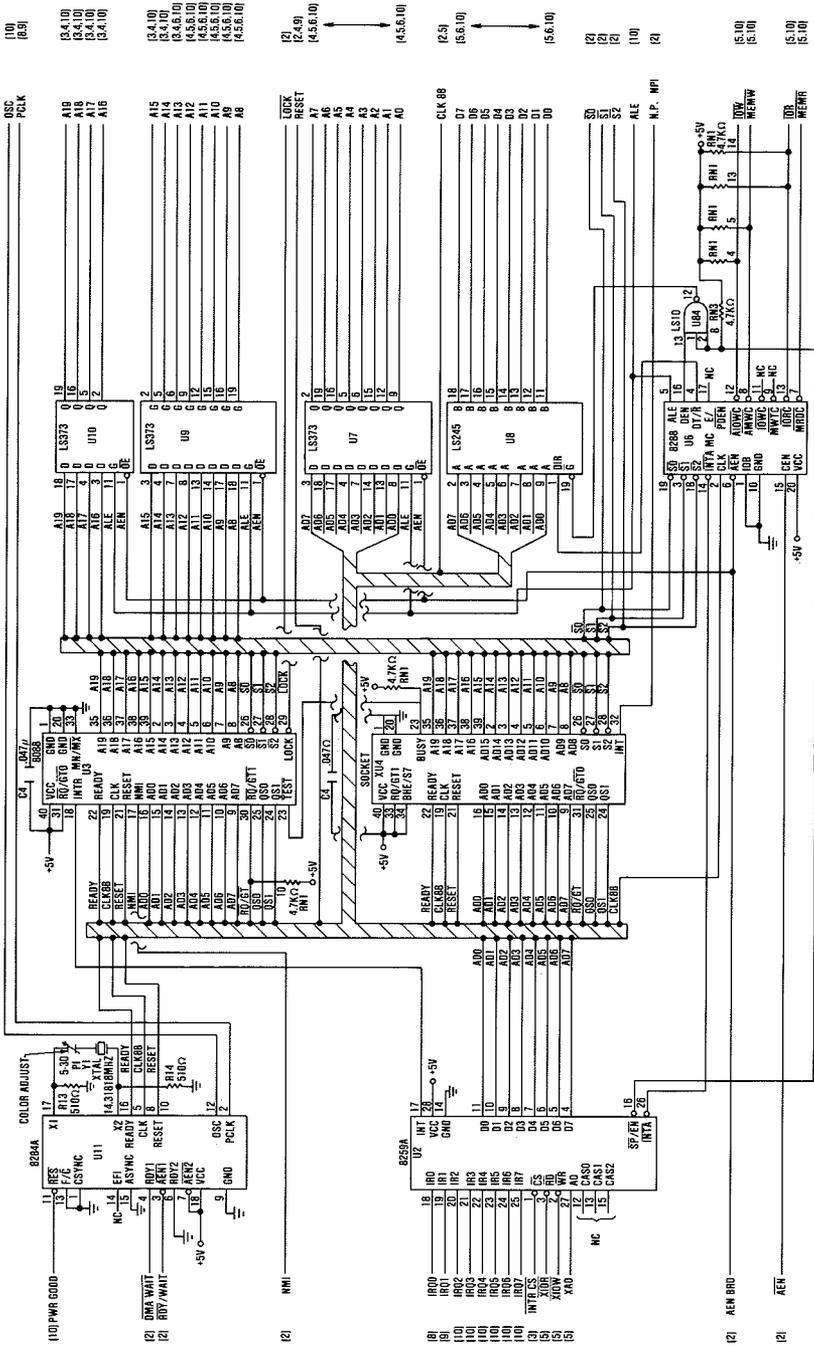
DECIMAL VALUE	➡	128	144	160	176	192	208	224	240
↙	HEXA-DECIMAL VALUE	8	9	A	B	C	D	E	F
0	0	Ç	É	á	⋮	⌌	⌌	∞	≡
1	1	ü	æ	í	⋮	⌌	⌌	β	±
2	2	é	Æ	ó	⋮	⌌	⌌	Γ	≥
3	3	â	ô	ú	⌌	⌌	⌌	π	≤
4	4	ä	ö	ñ	⌌	⌌	⌌	Σ	∫
5	5	à	ò	Ñ	⌌	⌌	⌌	σ	∫
6	6	å	û	à	⌌	⌌	⌌	μ	÷
7	7	ç	ù	ó	⌌	⌌	⌌	τ	≈
8	8	ê	ÿ	¿	⌌	⌌	⌌	ϕ	◦
9	9	ë	Ö	⌌	⌌	⌌	⌌	θ	•
10	A	è	Ü	⌌	⌌	⌌	⌌	Ω	•
11	B	ï	ç	½	⌌	⌌	⌌	δ	√
12	C	î	£	¼	⌌	⌌	⌌	∞	n
13	D	ì	¥	¡	⌌	⌌	⌌	φ	2
14	E	Ä	℞	«	⌌	⌌	⌌	€	■
15	F	Å	ƒ	»	⌌	⌌	⌌	∩	BLANK 'FF'

Appendix C

**Notes:**

# APPENDIX D: LOGIC DIAGRAMS

System Board (16/64K) .....	D-2
System Board (64/256K) .....	D-12
Keyboard – Type 1 .....	D-22
Keyboard – Type 2 .....	D-24
Expansion Board .....	D-25
Extender Card .....	D-26
Receiver Card .....	D-29
Printer .....	D-32
Printer Adapter .....	D-35
Monochrome Display Adapter .....	D-36
Color/Graphics Monitor Adapter .....	D-46
Color Display .....	D-52
Monochrome Display .....	D-54
5–1/4 Inch Diskette Drive Adapter .....	D-55
5–1/4 Inch Diskette Drive – Type 1 .....	D-59
5–1/4 Inch Diskette Drive – Type 2 .....	D-62
Fixed Disk Drive Adapter .....	D-64
Fixed Disk Drive – Type 1 .....	D-70
Fixed Disk Drive – Type 2 .....	D-73
32K Memory Expansion Option .....	D-76
64K Memory Expansion Option .....	D-79
64/256K Memory Expansion Option .....	D-82
Game Control Adapter .....	D-86
Prototype Card .....	D-87
Asynchronous Communications Adapter .....	D-88
Binary Synchronous Communications Adapter .....	D-89
SDLC Communications Adapter .....	D-91



16/64K System Board (Sheet 1 of 10)

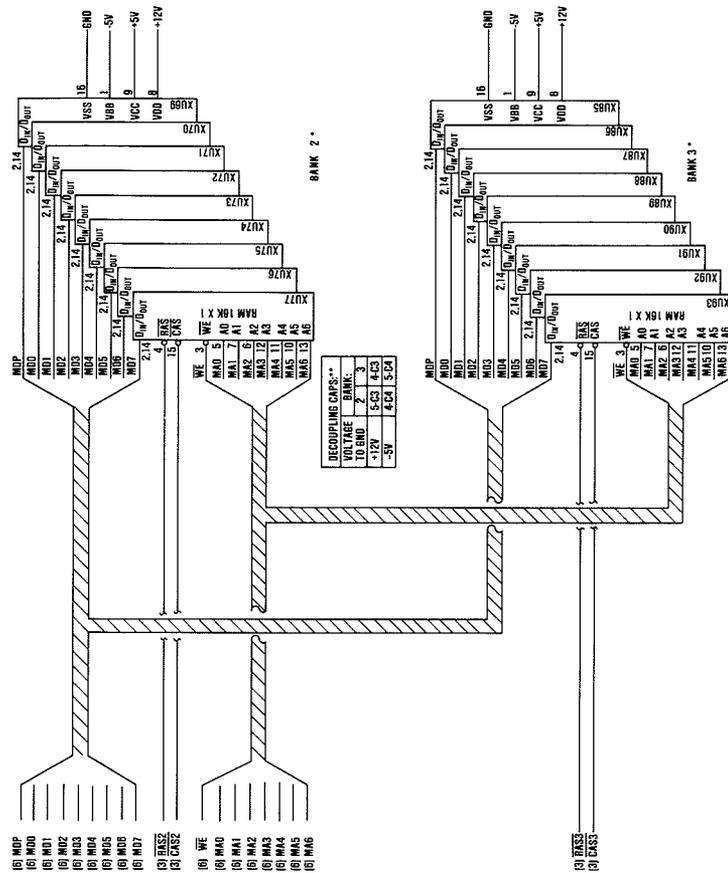










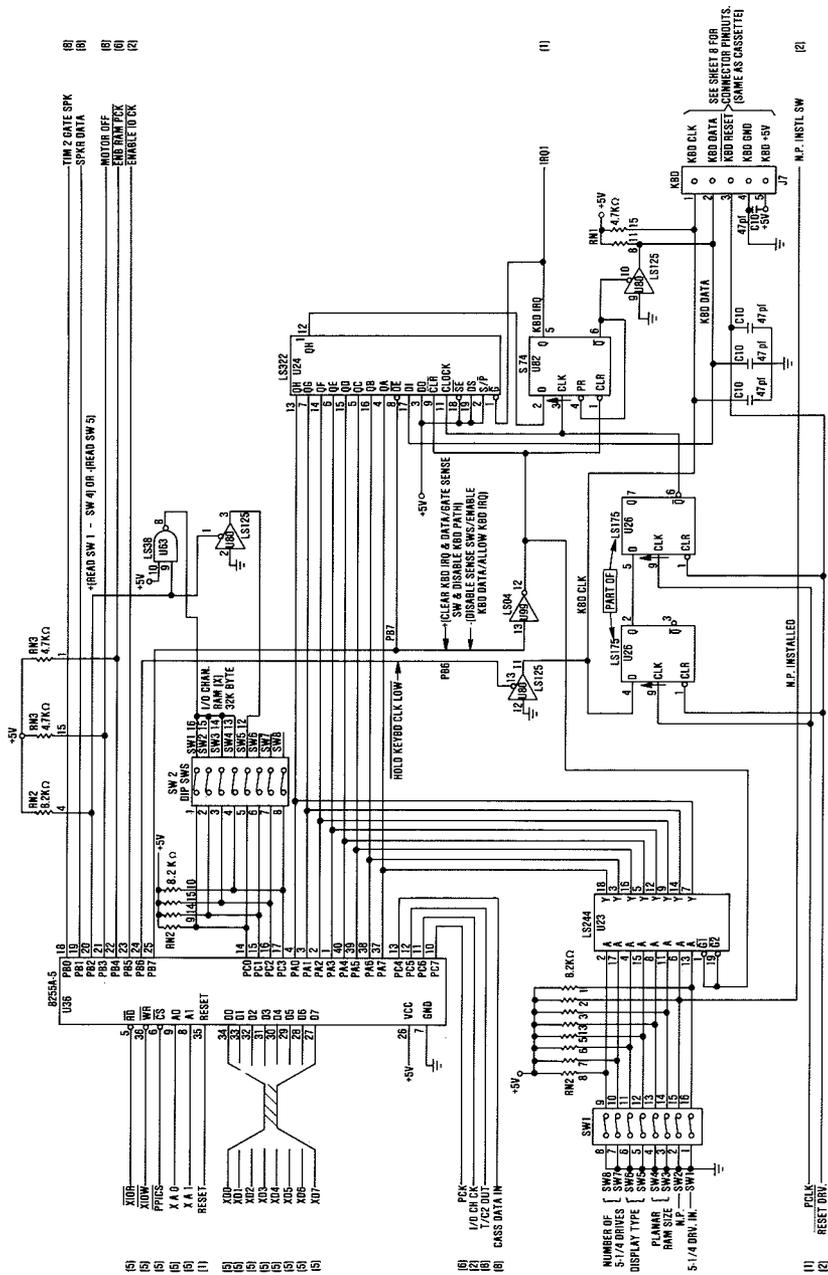


\* BANKS 2 & 3 ARE FEATURES.  
 \*\* C3 = 0.047µF ±5%WVDC } DECOUPLING  
 C4 = .001µF ±1%WVDC }

### 16/64K System Board (Sheet 7 of 10)

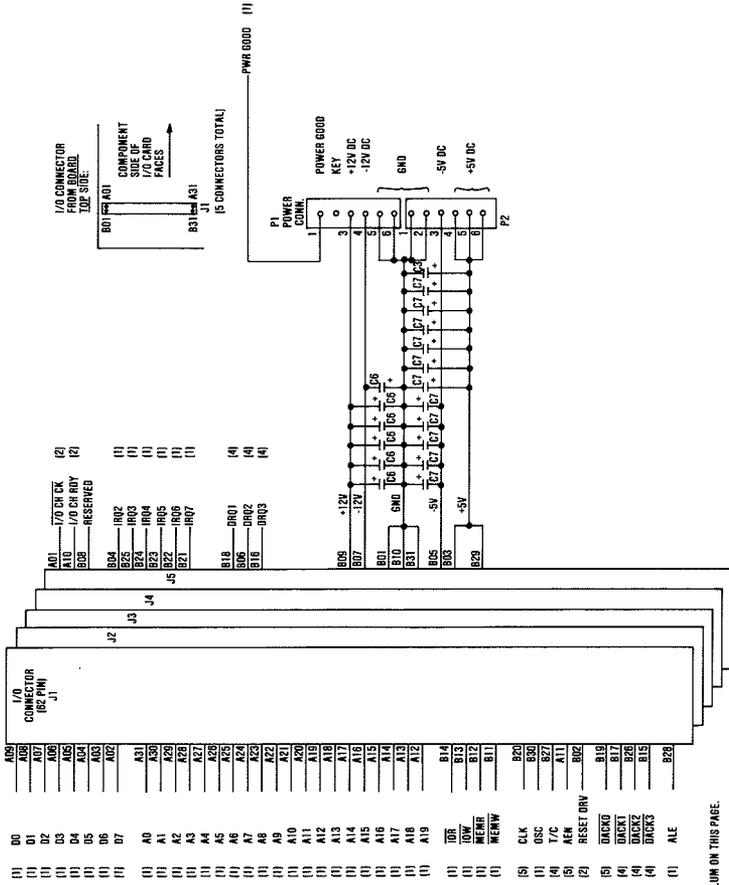


# D-10 Logic Diagrams



16/64K System Board (Sheet 9 of 10)

(1) (2)

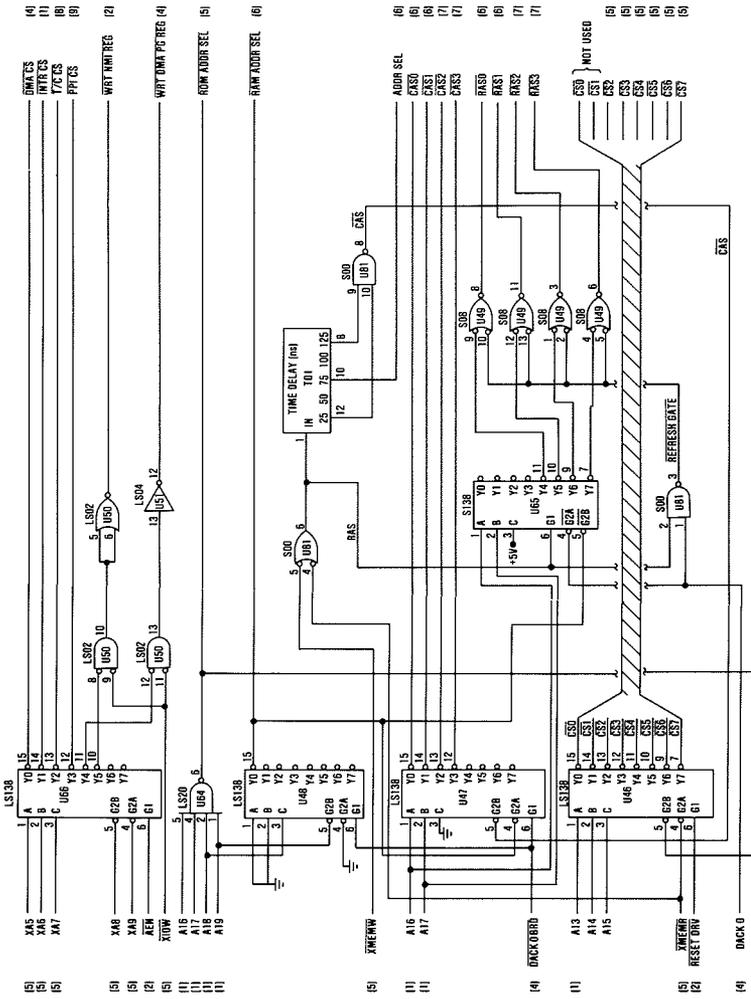


NOTE: ALL CAPS ARE 8.2UF TANTALUM ON THIS PAGE.

16/64K System Board (Sheet 10 of 10)

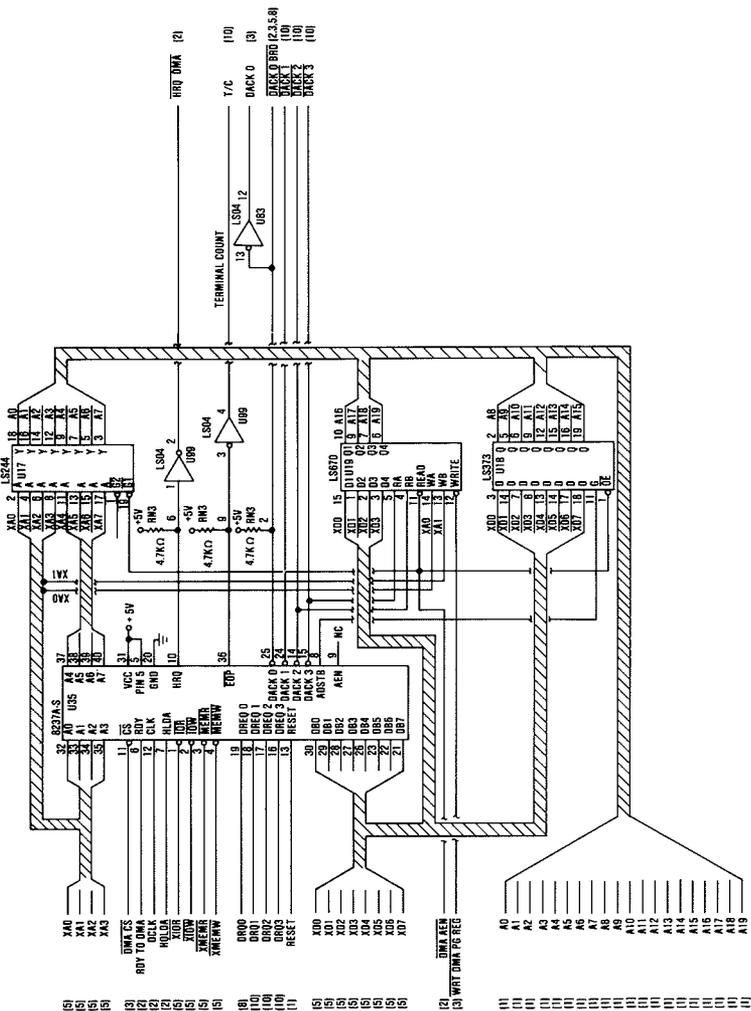




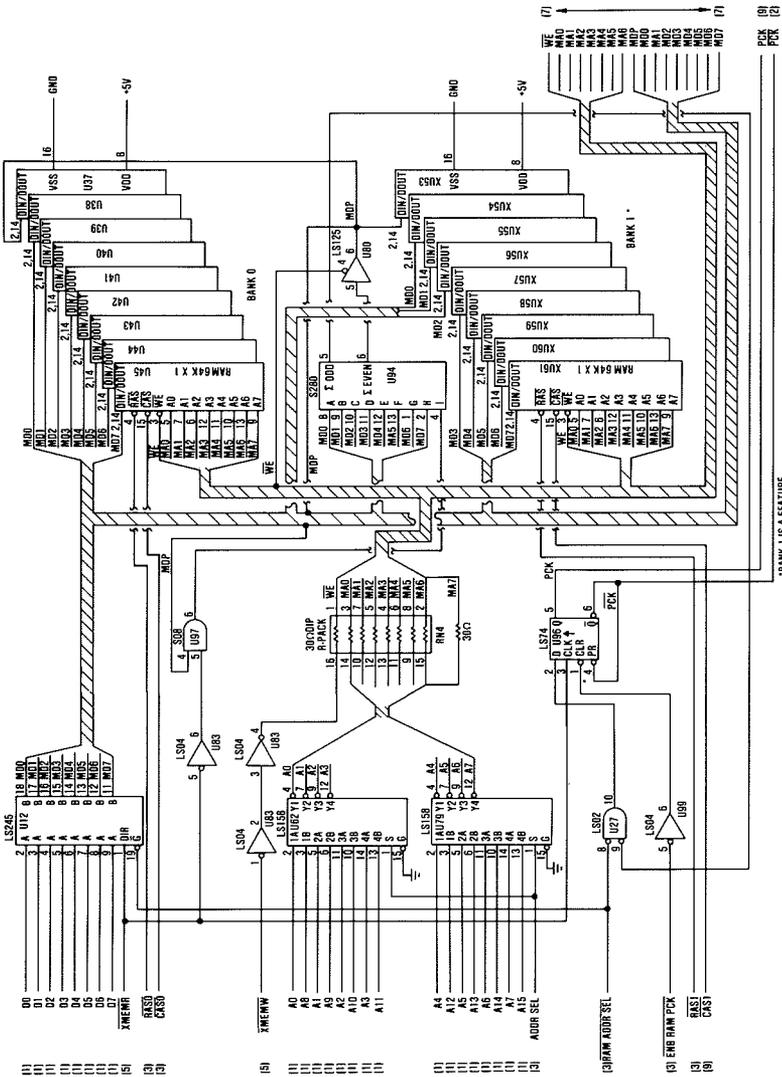


64/256K System Board (Sheet 3 of 10)

64/256K System Board (Sheet 4 of 10)

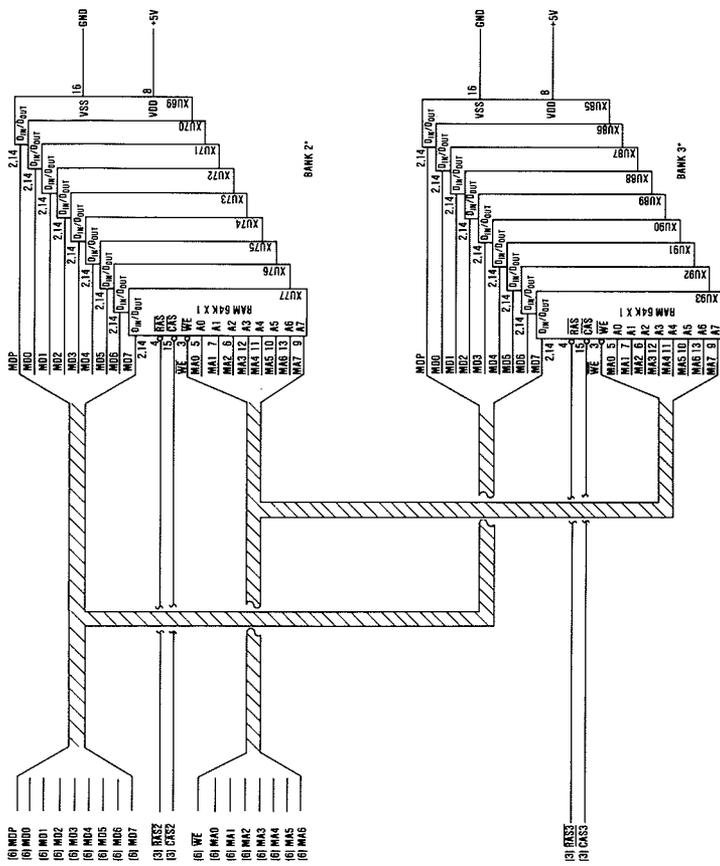






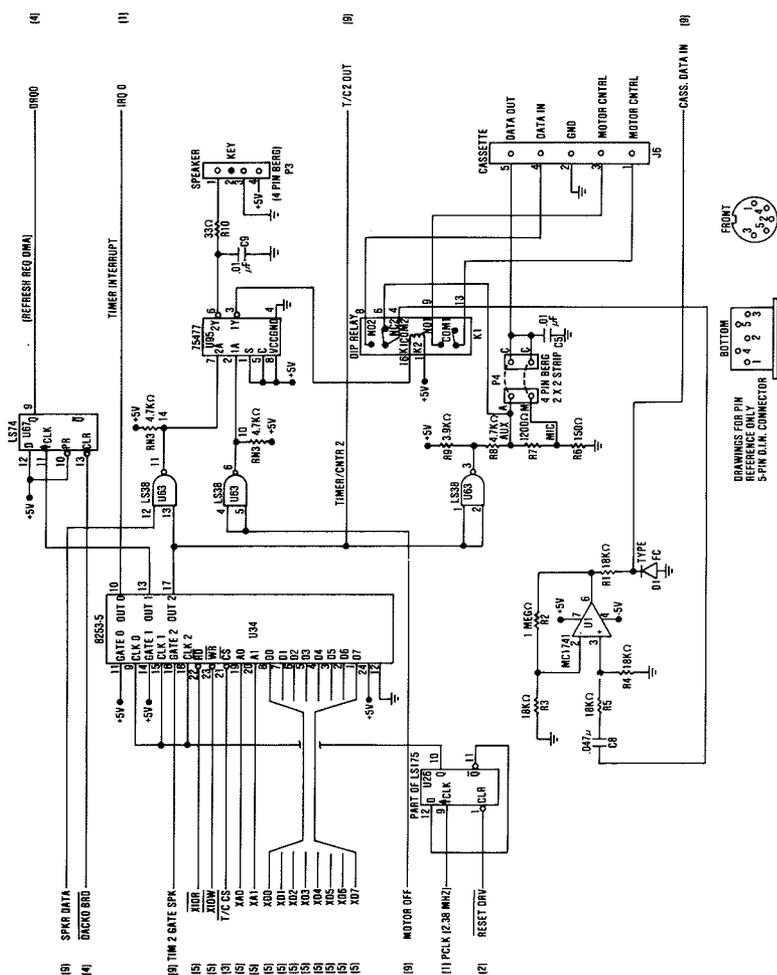
64/256K System Board (Sheet 6 of 10)

\*BANK 1 IS A FEATURE



\*BANKS 2 & 3 ARE FEATURES.

64/256K System Board (Sheet 7 of 10)



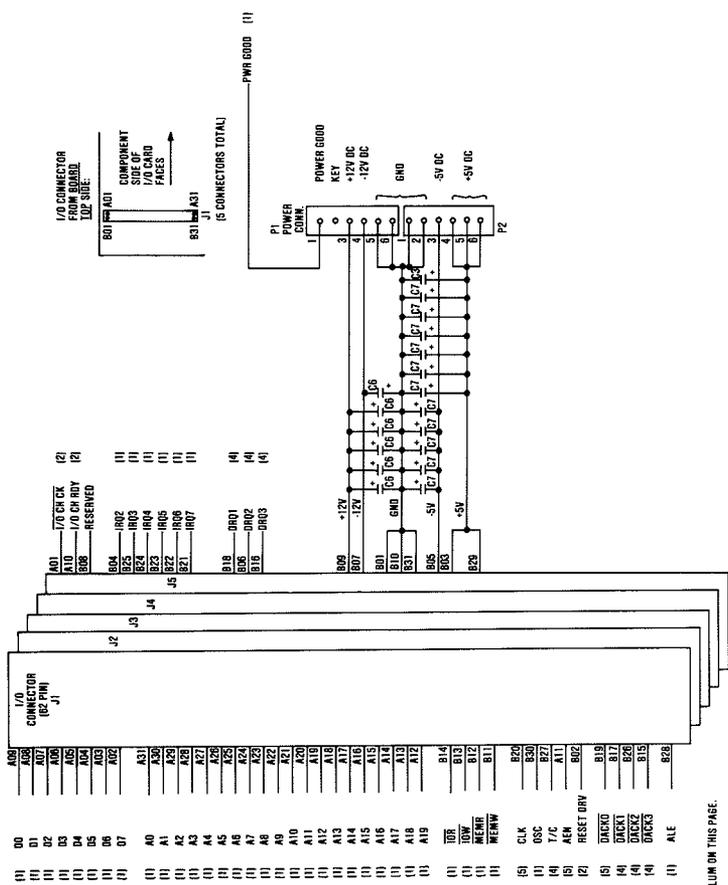
- (10) SPKR DATA
- (14) DRACO RST
- (15) X0R
- (16) X0W
- (17) 7/C CS
- (18) X0A
- (19) X0A1
- (20) X0A2
- (21) X0A3
- (22) X0A4
- (23) X0A5
- (24) X0A6
- (25) X0A7
- (26) MOTOR OFF
- (27) PCLK (2.38 MHz)
- (28) RESET DRV
- (29) 1/2 OUT
- (30) CASSETTE DATA IN
- (31) CASSETTE DATA OUT
- (32) MOTOR CONTROL
- (33) MOTOR CONTROL

64/256K System Board (Sheet 8 of 10)

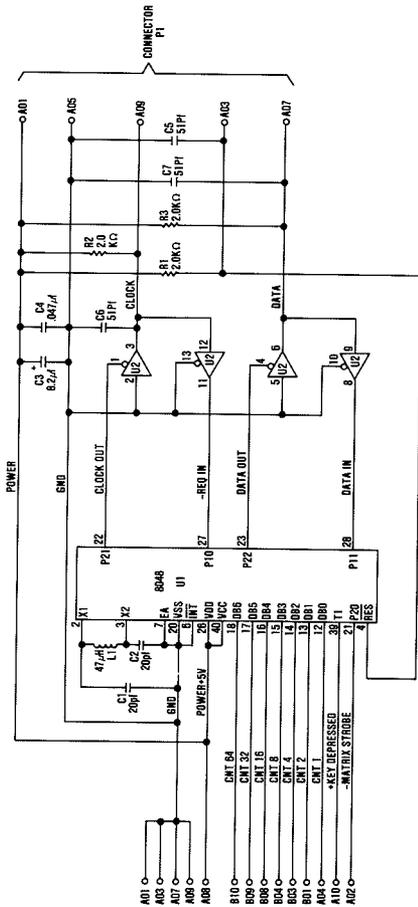
FRONT  
 0 1 2 3 4 5  
 6 7 8 9  
 BOTTOM  
 0 1 2 3  
 DRAWINGS FOR PWB  
 SERVICE CONNECTIONS  
 5-PIN DIN CONNECTOR



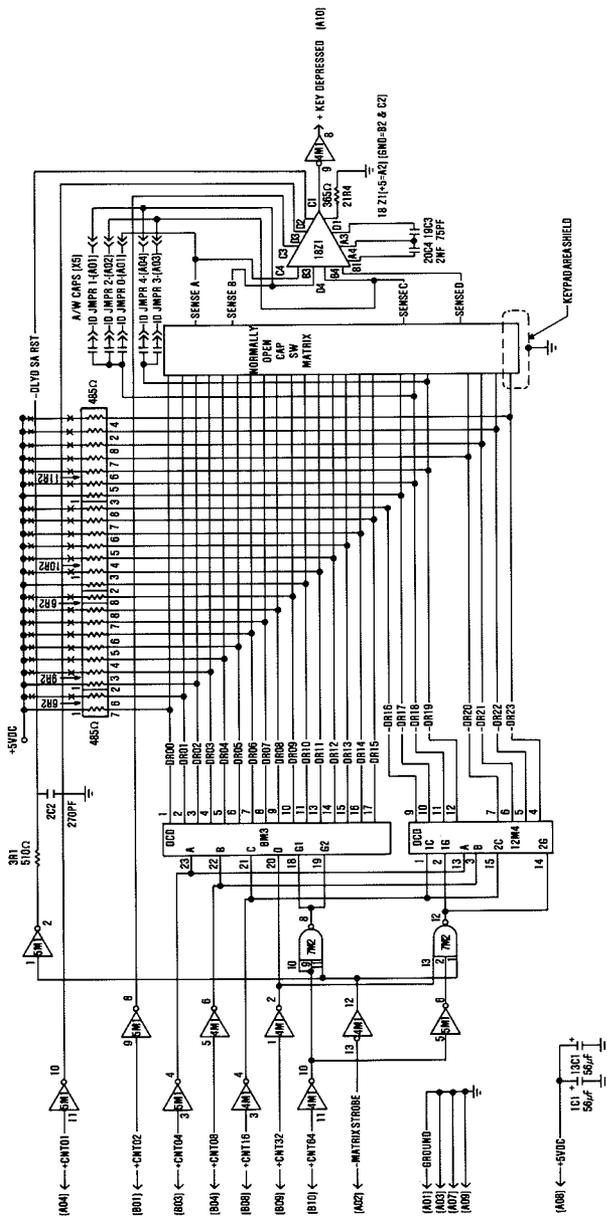
64/256K System Board (Sheet 10 of 10)



NOTE:  
1. ALL CRPS ARE B2AF TANTALUM ON THIS PAGE.

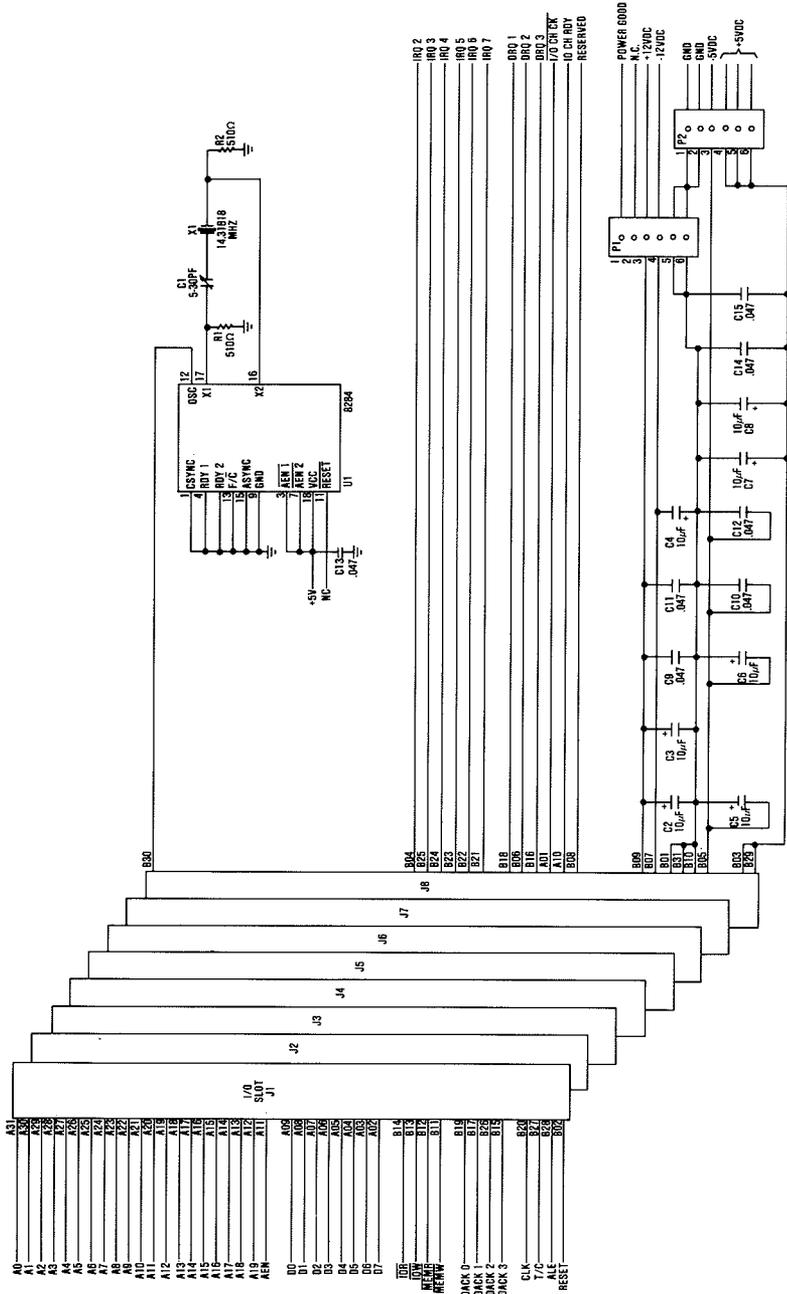


Keyboard -- Type 1 (Sheet 1 of 2)



Keyboard - Type 1 (Sheet 2 of 2)





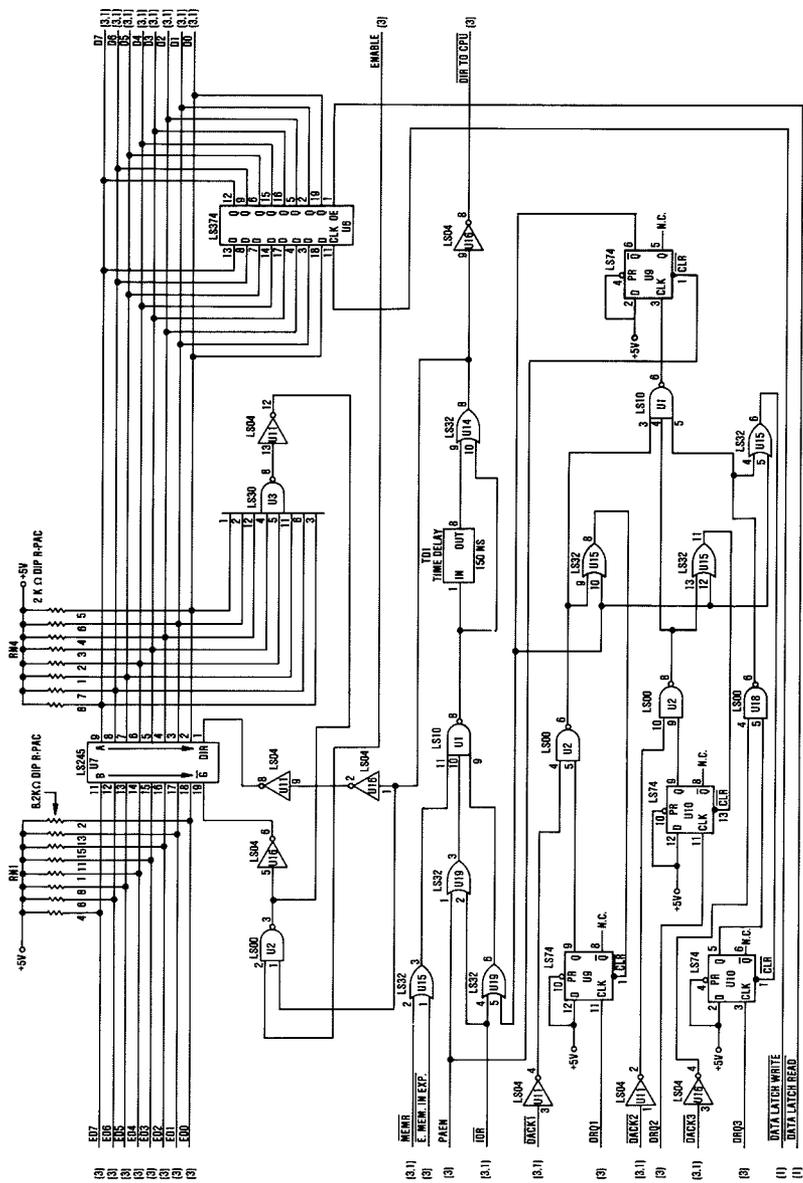
Expansion Board (Sheet 1 of 1)



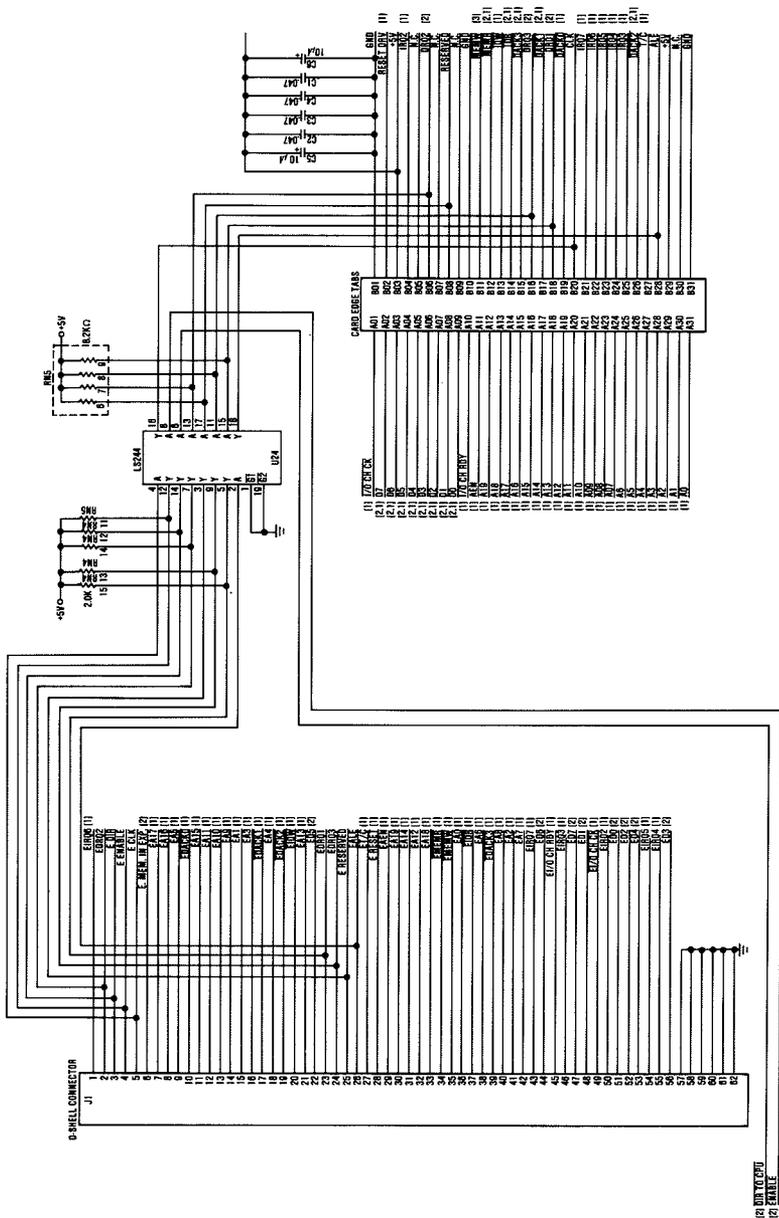








Receiver Card (Sheet 2 of 3)



Receiver Card (Sheet 3 of 3)

