

There are two set command forms:

```

ATSr?<CR>
ATSr=<CR>

```

The first reveals the contents for the register indicated by r. The second changes the contents of r to the value specified by n.

| Register | Range      | Unit       | Default | Description                                       |
|----------|------------|------------|---------|---|
| S0*      | 0-255      | rings      | 0       | Ring to answer on                                 |
| S1       | 0-255      | rings      | 0       | Number of rings passed                            |
| S2       | 0-127      | ASCII      | 43      | Escape code character                             |
| S3       | 0-127      | ASCII      | 13      | Command terminator<br>(Carriage return character) |
| S4       | 0-127      | ASCII      | 10      | Line feed character                               |
| S5       | 0-127      | ASCII      | 8       | Backspace character                               |
| S6       | 0-255      | seconds    | 2       | Wait time for dial tone                           |
| S7       | 0-255      | seconds    | 30      | Wait time for Carrier                             |
| S8       | 0-255      | seconds    | 2       | Pause time for comma                              |
| S9       |            | not used   |         |   |
| S10      | 0-255      | 1/10 sec.  | 14      | Hang up delay after loss of carrier               |
| S11      | 0-255      | 1/100 sec. | 95      | Touch-tone duration, spacing                      |
| S12      | 0-255      | 150 sec.   | 50      | Escape code time                                  |
| S13      |            | not used   |         |   |
| S14*     | Bit mapped |            |         | Option register                                   |
| S15      |            | not used   |         |   |
| S16      | Bit mapped |            |         | Test modes  |
| S17      |            | not used   |         |   |
| S18*     | 0-255      |            | 0       | Test duration                                     |
| S19      |            | not used   |         |   |

|      |            |                 |
|------|------------|-----------------|
| S20  |            | not used        |
| S21* | Bit mapped | Option register |
| S22* | Bit mapped | Option register |
| S23* | Bit mapped | Option register |
| S24  |            | not used        |
| S25  |            | not used        |
| S26  |            | not used        |
| S27* | Bit mapped | Option register |

\* The values in these registers can be stored in NVRAM with the &W command.

(dkh-07/28/93)