

Register	Function	Range	Default
S0	Numbers of rings to wait before answering. When set to 0, auto answer is disabled.	255	000
S1	Count of current number of rings. Cleared if no rings during last eight seconds.	0-255	000
S2	ASCII value of escape code character	0-127	043 (+)
S3	ASCII value of carriage return character	0-127	013 (CR)
S4	ASCII value of line feed character	0-127	010 (LF)
S5	ASCII value of backspace character	0-32,127	008 (BS)
S6	Maximum dial tone wait time before dialing (seconds)	2-255	002
S7	Wait time for carrier after dialing (seconds)	1-255	030
S8	Pause time for comma (seconds)	0-255	002
S9	Carrier detect response time (0.1 seconds)	1-255	006
S10	Carrier loss response time (0.1 seconds)	1-255	007
S11	Controls the speed of tone dialing (milliseconds)	50-255	070
S12	Escape code guard time (.02 seconds)	20-255	050
S13	Bit mapped options (as below)		016
	Bit 0 - Undefined		0
	Bit 1 - Undefined		0
	Bit 2 - Parity Disabled/Enabled (0/1)		0
	Bit 3 - Parity Bit, Odd/Even (0/1)		0
	Bit 4 - Data Bits, 7/8 (0/1)		1
	Bit 5 - Undefined		0
	Bit 6 - Buffer Overflow Flag (Causes ERROR Result code to be sent)		0

	Bit 7 - Parity Bit, Space/Mark (0/1) (If bit 4=0)	0
S14	Bit mapped options (as below)	106
	Bit 0 - Autoanswer Disabled/Enabled (0/1)	0
	Bit 1 - Local Echo Disabled/Enabled (0/1)	1
	Bit 2 - Result Codes, Enabled/Disabled (0)	0
	Bit 3 - Result Codes, Numeric/Verbal (0/1)	1
	Bit 4 - Command Received Enabled/Disabled (1)	0
	Bit 5 - Make/Break Ratio, Europe/US (0/1)	1
	Bit 6 - CCITTV.22/Bell212A,103 mode (0/1)	1
	Bit 7 - Long Space Disconnect Disable/Enable (0/1)	0
S15	Bit mapped options (as below)	059
	Bit 0 - Same as Bit 4	1
	Bit 1 - Same as Bit 5	1
	Bit 2 - Answer Mode/Originate Mode (0/1)	0
	Bit 3 - Half Duplex/Full Duplex (0/1)	1
	Bit 5 Bit 4	
	0 0 Undefined	
	1 0 110 bps	
	0 1 300 bps	
	1 1 1200 bps	
	Bit 6 - Connection/No Connection (0/1)	0
Bit 7 - Undefined	0	
S16	Self Test Register	
	Value 0 - Normal Operation	
	Value 1 - Analog Loop Back	
	Value 2 - DTMF Tone Test	
	Value 3 - Digital Loop Back	

NOTE:

Registers S13 through S15 are bit-mapped and are for internal use only.
Writing into these registers might result in unpredictable results.

(css 07/28/93)